

ABSTRACT

A power-on operation execution method and apparatus are provided. The method includes: starting a processor of the chip in response to the chip being connected to a host and a frequency of a first clock signal of the chip being switched to a stable state; receiving a reset signal transmitted from the host, the reset signal being transmitted in response to a frequency of a second clock signal of the host being switched to a stable state; and executing a power-on operation in response to the reset signal, a time point at which the frequency of the first clock signal is switched to the stable state being earlier than a time point at which the frequency of the second clock signal is switched to the stable state.