



- (51) International Patent Classification:
H02M 3/335 (2006.01) *G05F 1/00* (2006.01)
- (21) International Application Number:
PCT/AU2011/001598
- (22) International Filing Date:
9 December 2011 (09.12.2011)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
2010905447 9 December 2010 (09.12.2010) AU
61/432,564 13 January 2011 (13.01.2011) US
- (71) Applicant (for all designated States except US): **INDICE PTY LTD** [AU/AU]; Level 1, 16 Harvey Street, Richmond, Victoria 3121 (AU).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): **HAMOND, James** [AU/AU]; Level 1, 16 Harvey Street, Richmond, Victoria 3121 (AU).
- (74) Agent: **MACPHERSON + KELLEY LAWYERS**; Level 22, 114 William Street, Melbourne, Victoria 3000 (AU).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

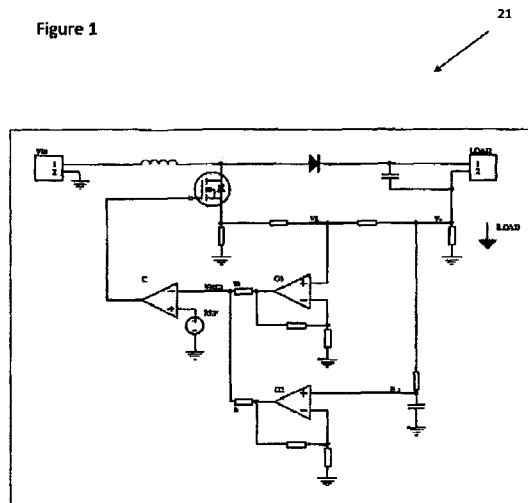
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))

(54) Title: POWER SUPPLY CONTROL SYSTEM AND DEVICE

Figure 1



(57) Abstract: Power supply control system for a switching-mode power supply with cycle by cycle, asynchronous control scheme, making use of simulated electronic logic gates for the logical switching means and further including assimilation and artificial intelligence neural control network, such as a perceptron, to determine the electronic simulation and to make use of a mixing of instant and average signals through a feedback system.