

REACTION TREATMENT DEVICE

[ABSTRACT]

A reaction processor 30 is provided with a reaction processing vessel 10 in which a channel 12 is formed, a liquid feeding system 37, a temperature control system 32 for providing a hightemperature region and a lowtemperature region to the channel 12, and a fluorescence detector 50 for detecting the sample 20 passing through a fluorescence detection region of the channel 12, and a CPU 36 for controlling the liquid feeding system 37 based on a signal that is detected. A target stop position $X^{[L]}_0(n+1)$ of the sample in the low temperature region in an (n+1)th cycle is corrected from a target stop position $X^{[L]}_0(n)$ of the sample in the low temperature region in the nth cycle based on the result of stopping control on the sample 20 in the nth cycle.

[Figure 2]