

ABSTRACT

Analog-to-Digital Converter Circuit

The offset from the ideal value for the bias voltage to be added to the input signal to the A/D converter (4) and the offset from the ideal value of the average voltage in the analog signal are detected. The detected offset is subtracted from the output data of the A/D converter (4). Alternately, the bias voltage may be changed by the detected offset so that the average voltage of the input signal to the A/D converter (4) is equal to the ideal value. (Fig. 1)