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COMPUTER SYSTEM STATE MONITOR FOR GATING POWER-ON CONTROL

ABSTRACT

A method of controlling the power-on of a computer system is disclosed. The method provides a plurality of monitor lines interconnected with respective components of the computer system, monitors the lines using a logic control circuit (110) in response to a power-on reset signal, and executes a system power-on sequence in response to a determination by the logic control circuit (110) that the components of the computer system are functioning properly. The logic control circuit (110) determines whether one of the CPUs or terminator cartridges is plugged into each of the CPU slots (112), whether a properly matched CPU cartridge has been installed into the system, and whether multiple CPUs contain compatible power supply requirements. The logic control circuit (110) can also determine that the main power supply has been properly connected to the motherboard (114), all daughter cards (118) are properly installed and power supply connections to the daughter cards (118) have been properly installed.

Fig. 4



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