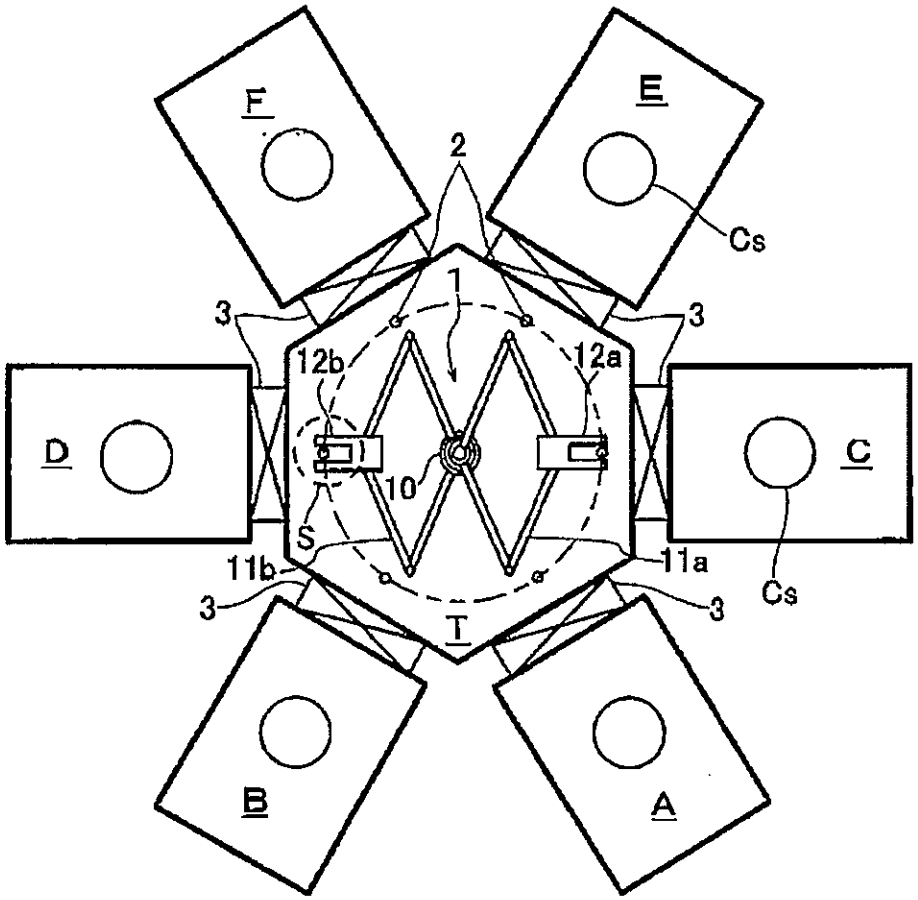
Fig.1

(A)


F
E
2
Cs
1
3
3
12b
12a
D
C
10
S
11b
11a
3
3
Cs
T
B
A

(B)

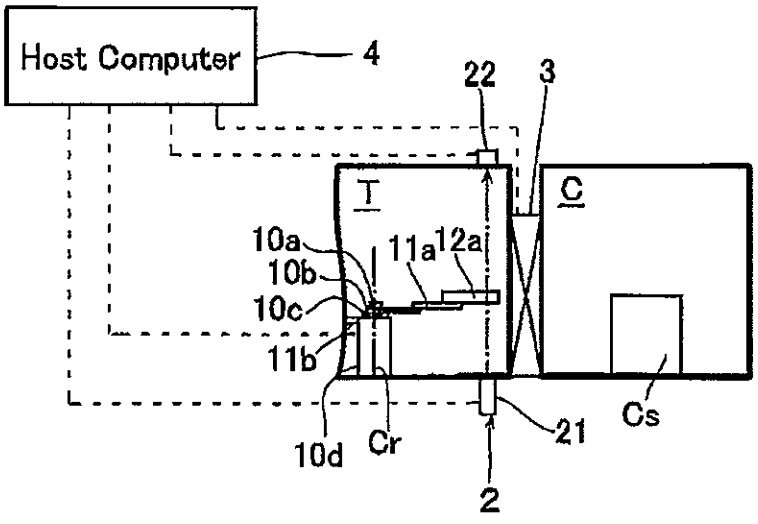

Host Computer
4
22
3
T
C
10a
10b
10c
11b
11a
12a
10d
Cr
21
Cs
2

Fig.2

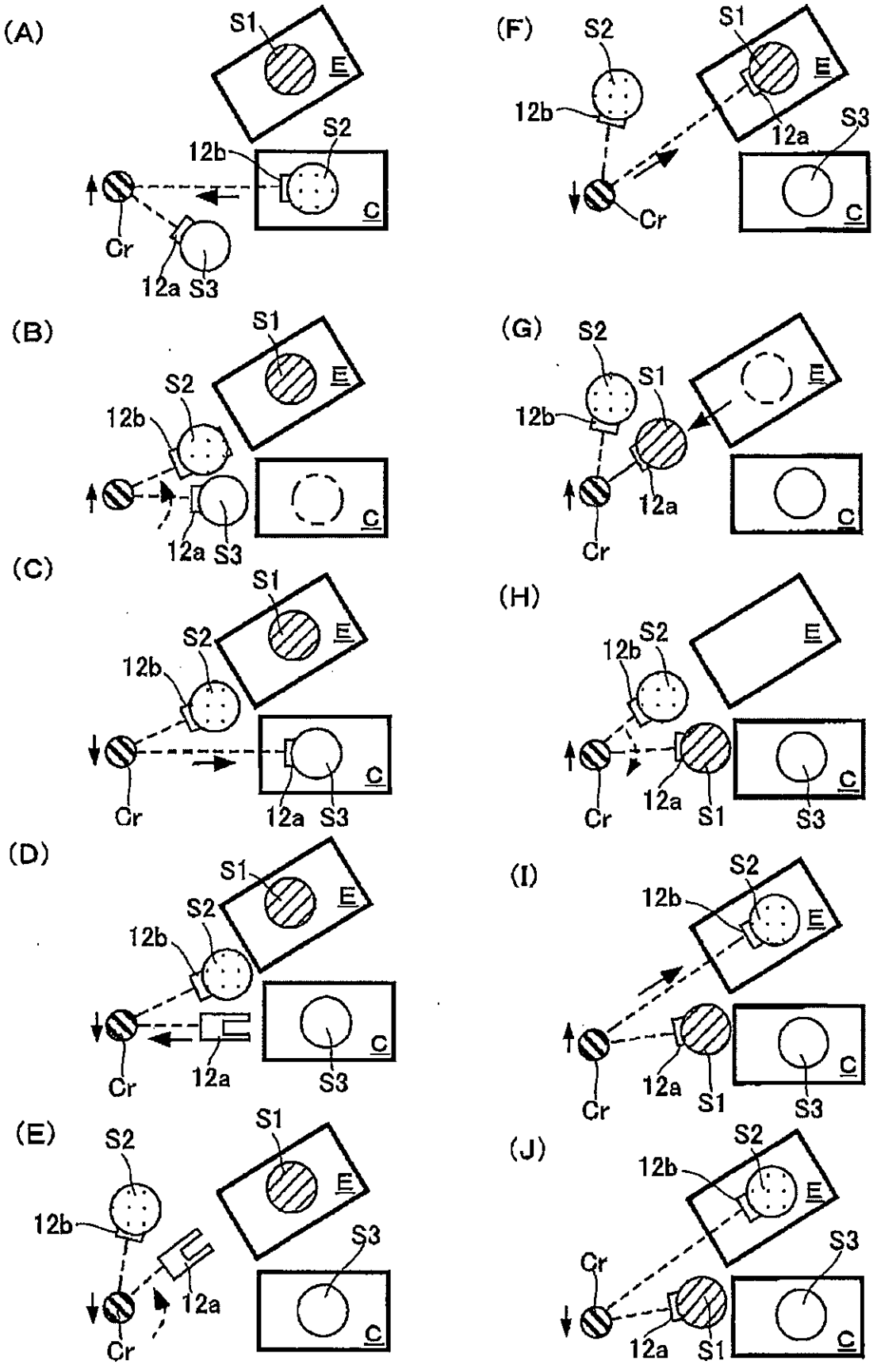

(A)
S1
E
S2
12b
C
Cr
12a
S3
(B)
S1
E
S2
12b
C
12a
S3
(C)
S1
E
S2
12b
C
Cr
12a
S3
(D)
S1
E
S2
12b
C
Cr
12a
S3
(E)
S2
S1
E
12b
S3
12a
C
Cr
(F)
S2
S1
E
12b
12a
S3
C
Cr
(G)
S2
E
S1
12b
12a
C
Cr
(H)
E
12b
S2
C
12a
Cr
S1
S3
(I)
S2
12b
E
C
12a
Cr
S1
S3
(J)
S2
12b
E
S3
Cr
C
12a
S1

Fig.3

			Hand12a		Hand12b	
Step	Up/Down	Turn	Extension/Contraction	Substrate Yes/No	Extension/Contraction	Substrate Yes/No
1	Up		Contraction	Yes	Contraction	Yes
2	Up	Turn	Contraction	Yes	Contraction	Yes
3	Down		Extension	No	Contraction	Yes
4	Down		Contraction	No	Contraction	Yes
5	Down	Turn	Contraction	No	Contraction	Yes
6	Down		Extension	No	Contraction	Yes
7	Up		Contraction	Yes	Contraction	Yes
8	Up	Turn	Contraction	Yes	Contraction	Yes
9	Up		Contraction	Yes	Extension	Yes
10	Down		Contraction	Yes	Extension	No

Fig.4

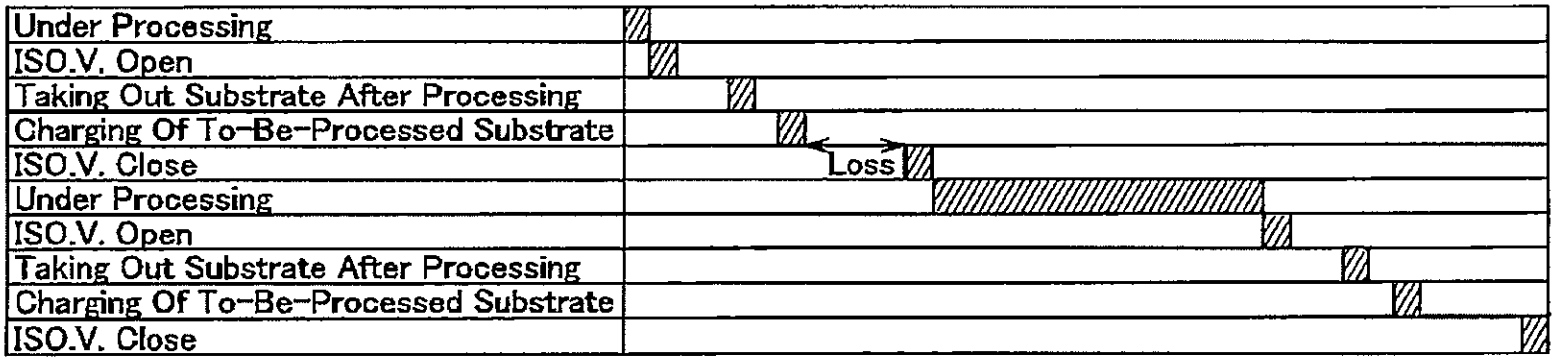


Fig.5

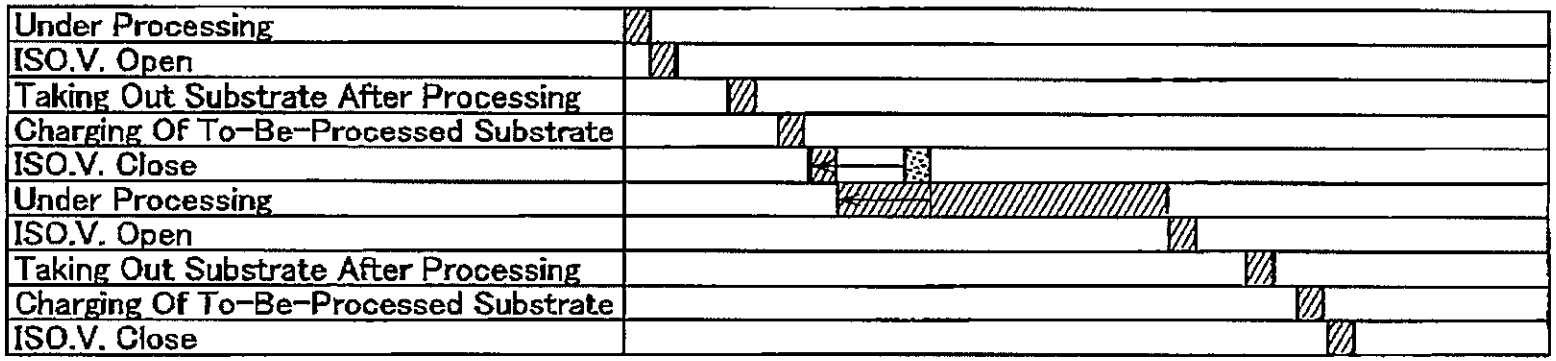

Under Processing
ISO.V. Open
Taking Out Substrate After Processing
Charging Of To-Be-Processed Substrate
ISO.V. Close
Under Processing
ISO.V. Open
Taking Out Substrate After Processing
Charging Of To-Be-Processed Substrate
ISO.V. Close

Fig.6

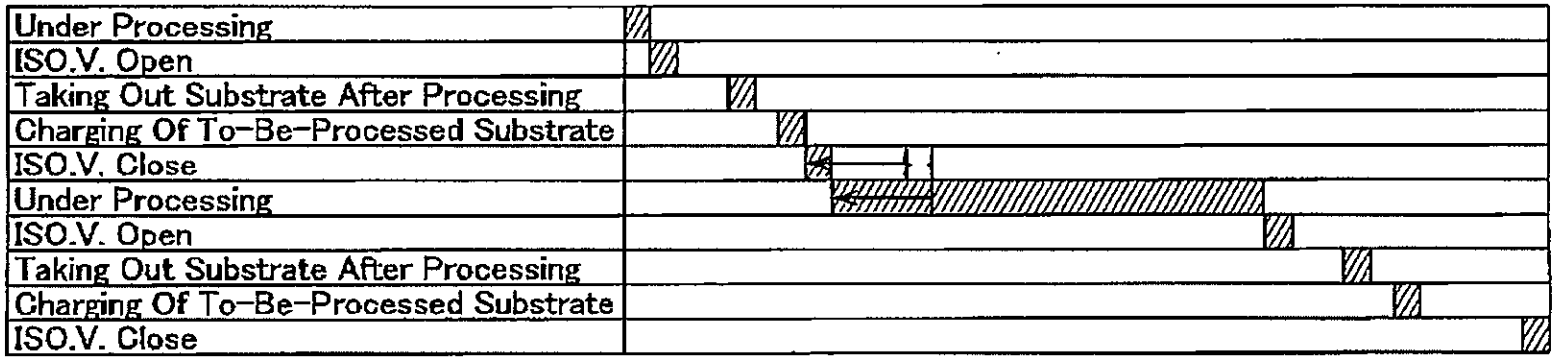

Under Processing
ISO.V. Open
Taking Out Substrate After Processing
Charging Of To-Be-Processed Substrate
ISO.V. Close
Under Processing
ISO.V. Open
Taking Out Substrate After Processing
Charging Of To-Be-Processed Substrate
ISO.V. Close

Fig.7

