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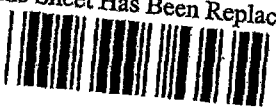
DELAY DETECTION DEMODULATION SYSTEM

ABSTRACT

Disclosed is a delay detection modulation system which has: a plurality of diversity type receiving systems (1 to 4); a plurality of instantaneous phase detecting circuits (5 to 8) for detecting an instantaneous phase from the output of each of the plurality of receiving systems (1 to 4); a plurality of operating circuits (9 to 12) for operating a phase difference in one symbol section from the output of each of the plurality of instantaneous phase detecting circuits; a combining circuit (13) for combining the outputs of the plurality of operating circuits; a delay detection circuit (14) for conducting error diffusion type delay detection after the combining of the combining circuit (13); and a demodulation logical circuit (15) for conducting demodulation logical operation.

Fig 3

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