

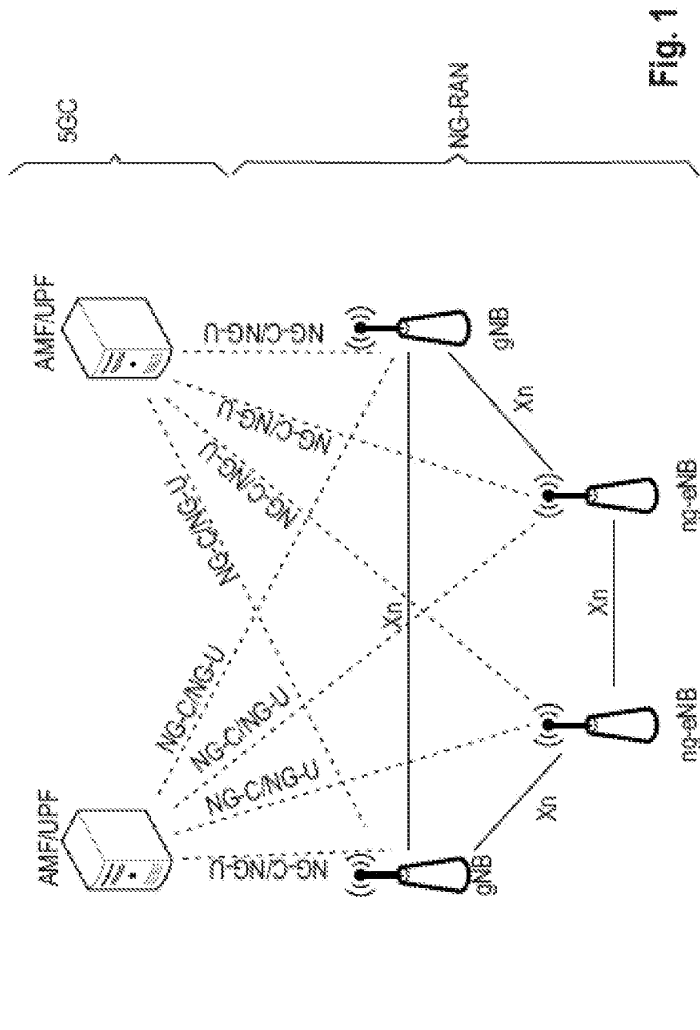


Fig. 1

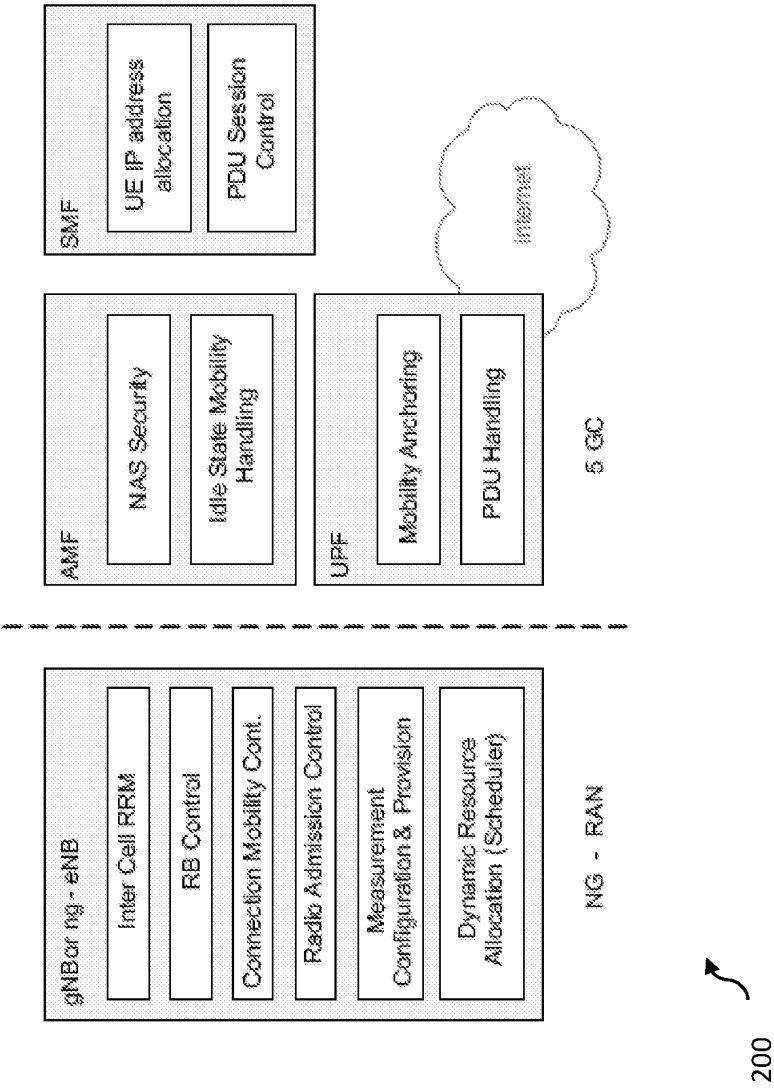


Fig. 2

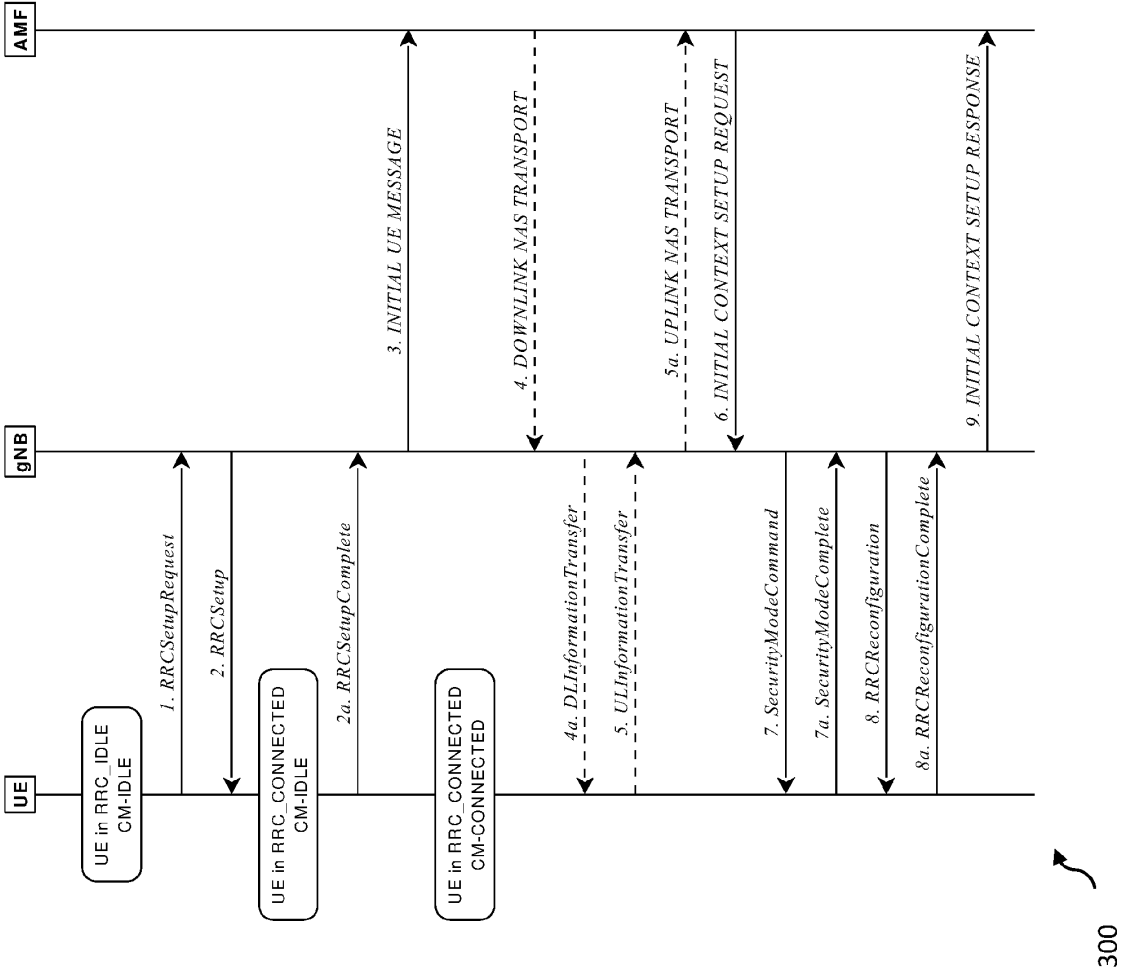


Fig. 3

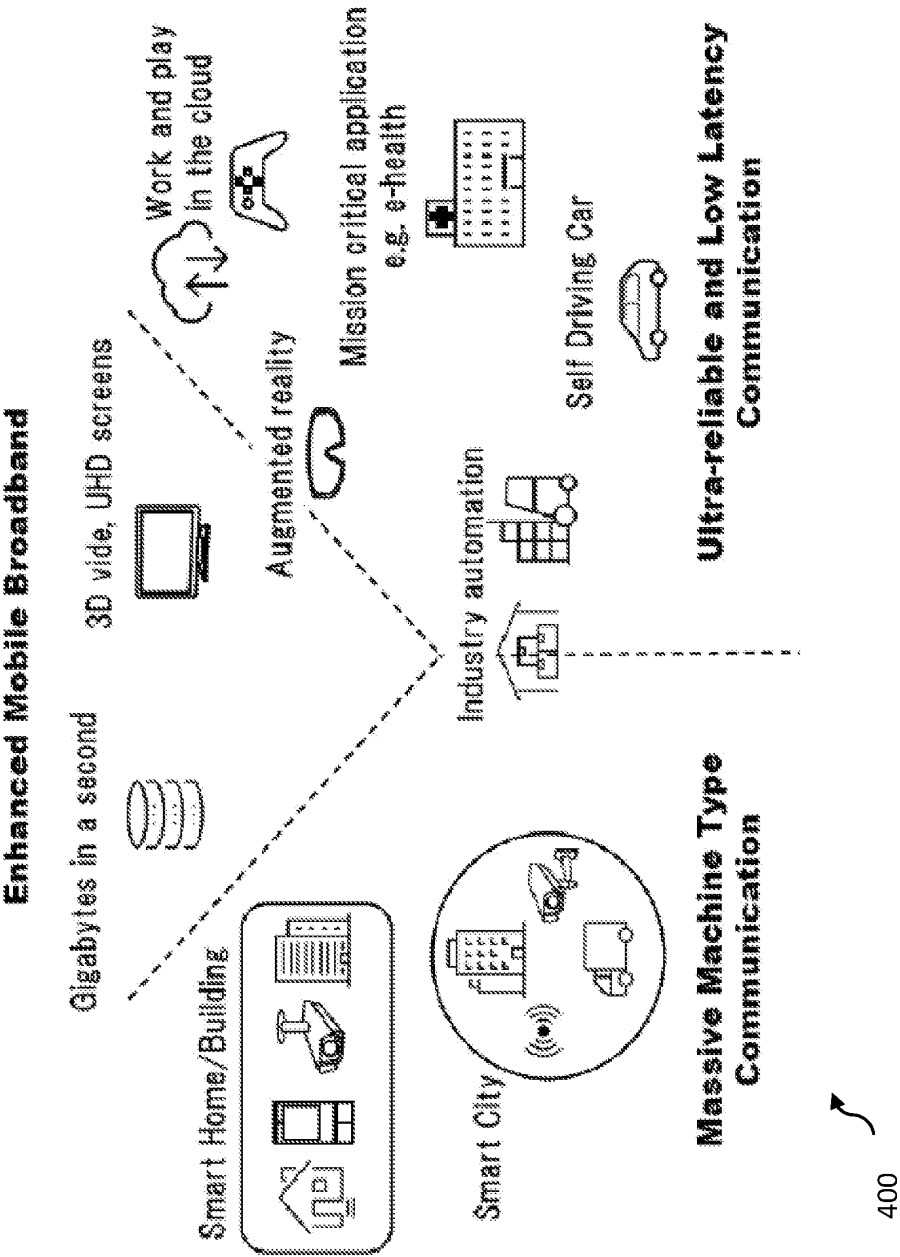


Fig. 4

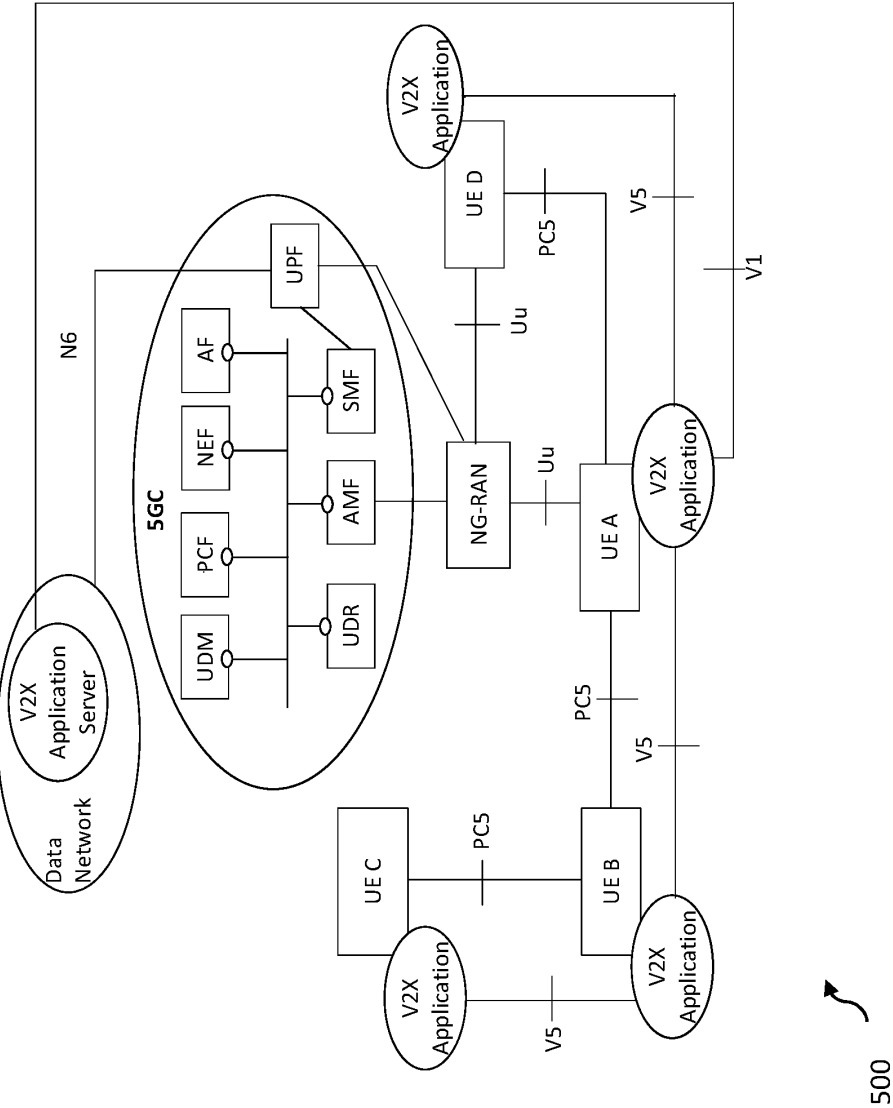


Fig. 5

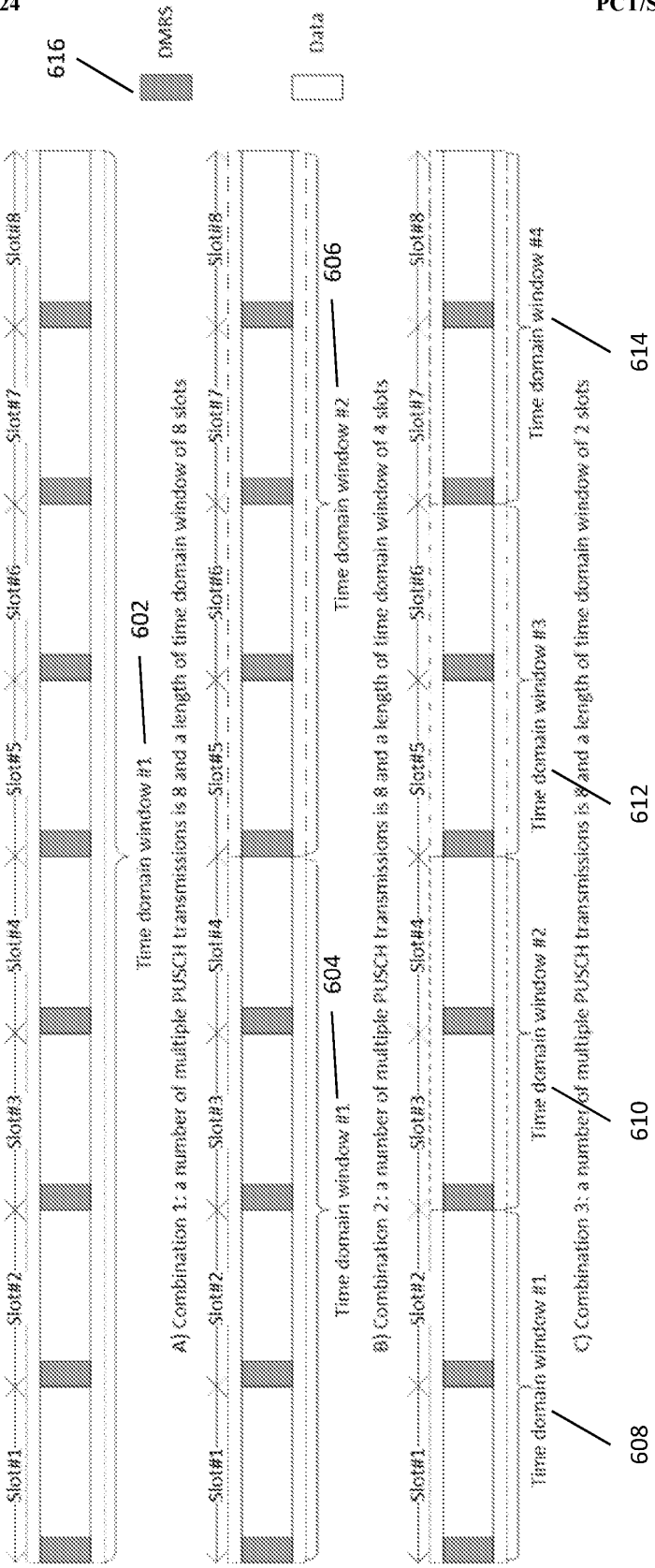


Fig. 6

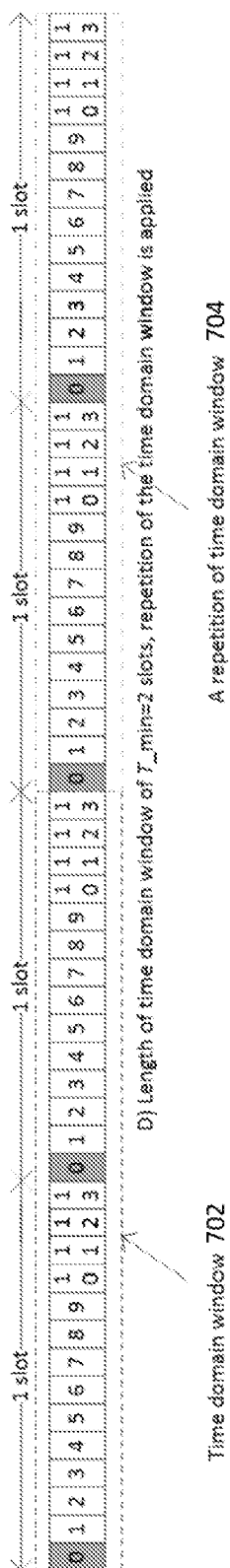


Fig. 7

700

802

FUSCH-TimeDomainResourceAllocationList-r16 ::= SEQUENCE (SIZE(1..MAXINT)) OF FUSCH-TimeDomainResourceAllocation-r16

FUSCH-TimeDomainResourceAllocation-r16 ::= SEQUENCE {

k2-r16

puschAllocationList-r16

...}

INTEGER (0..32)

OPTIONAL, -- Need S

SEQUENCE (SIZE(1..MAXINT)) OF FUSCH-Allocation-r16,

FUSCH-Allocation-r16 ::= SEQUENCE {

mappingType-r16

startSymbolAndLength-r16

startSymbol-r16

length-r16

numberOfRepetitions-r16

windowLength

...}

ENUMERATED (typeA, typeB) OPTIONAL,

INTEGER (0..127) OPTIONAL,

INTEGER (0..13) OPTIONAL,

INTEGER (1..14) OPTIONAL,

ENUMERATED (n1, n2, n3, n4, n7, n8, n12, n18)

SEQUENCE (SIZE(1..MAXINT)) OF windowLength

804

Fig. 8

800

DCI index	PUSCH mapping type	SLIV	Number of repetitions specified by numberOFRepetitions--r16	Length of time domain window specified by windowLength
0	A	$\{S_0, L_0\}$	$n1$	T_0
1	B	$\{S_1, L_1\}$	$n2$	T_1
...	...	...	...	...

Fig. 9

900

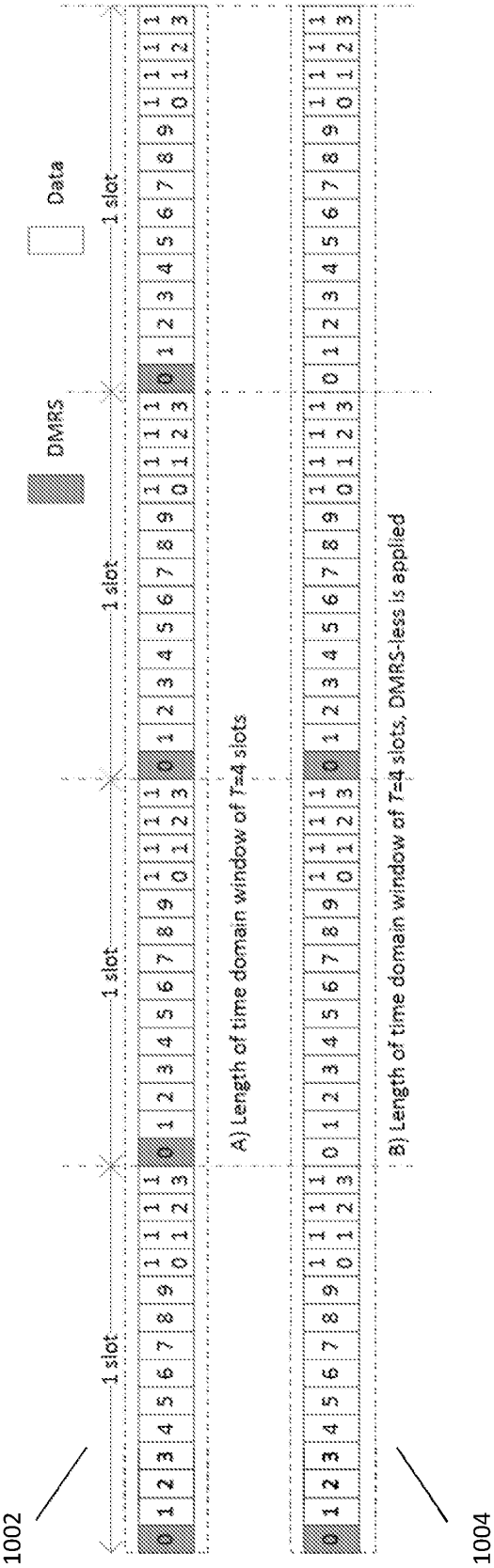
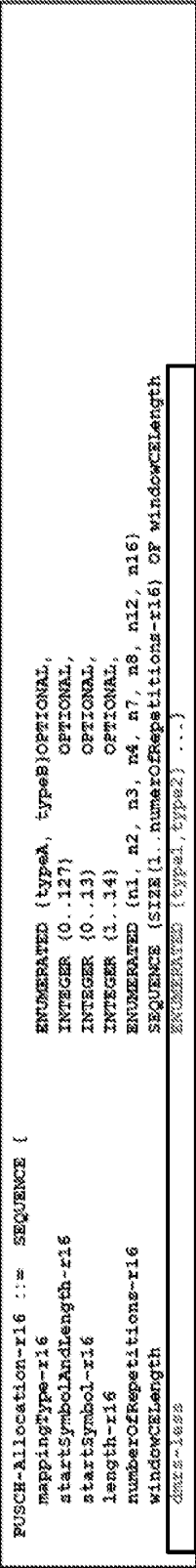


Fig. 10

1000



1102

Fig. 11

1100

DCI index	PUSCH mapping type	SLIV	Number of repetitions	Length of time domain window	DMRS-less
0	A	$\{S_0, L_0\}$	n_1	T_0 slots	Type 1
1	B	$\{S_1, L_1\}$	n_2	T_1 slots	Type 2
...	...	...	...	...	...

1200



Fig. 12

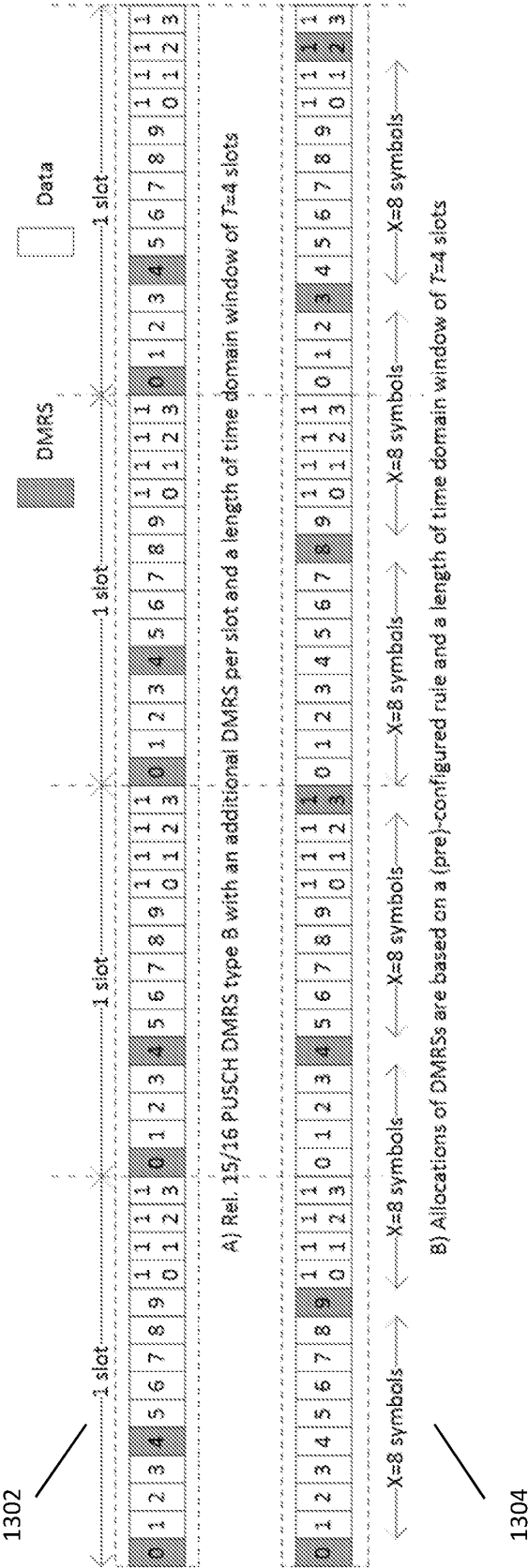
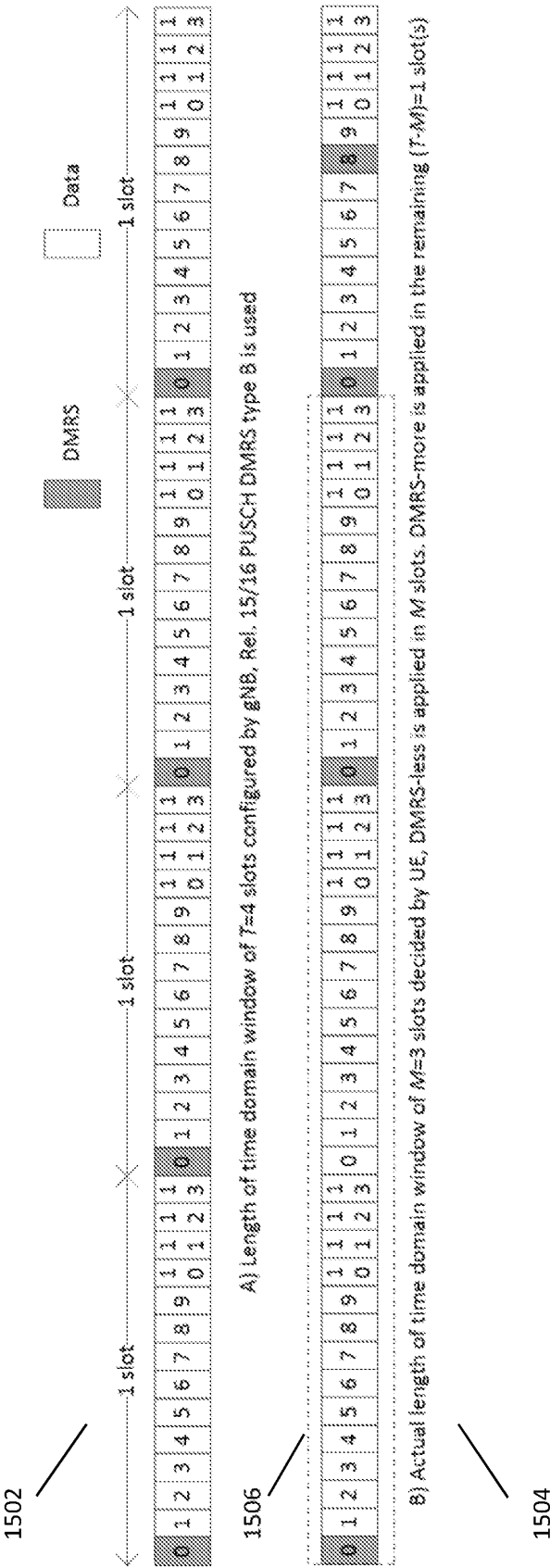


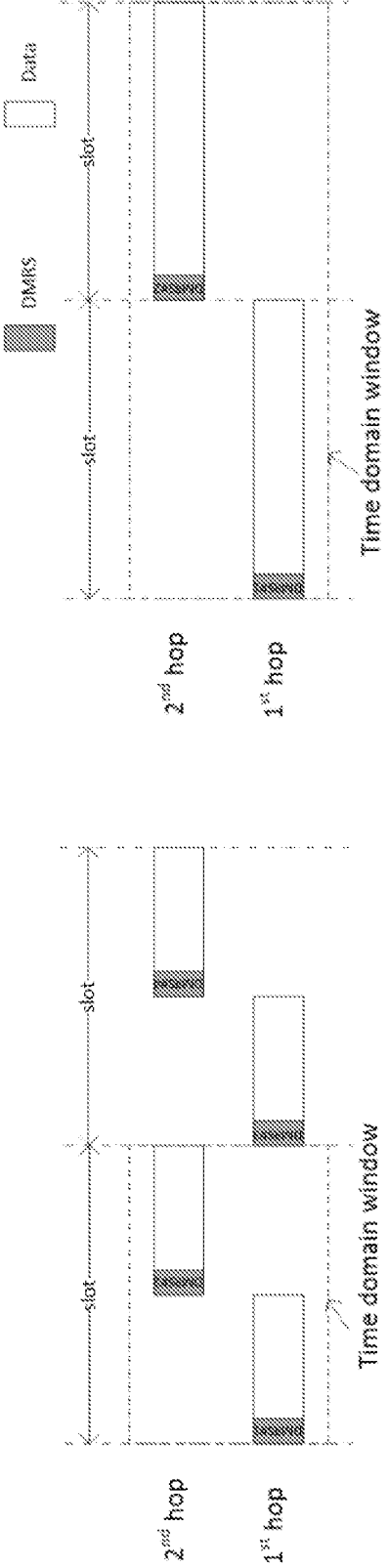
Fig. 13

DCI index	PUSCH mapping type	SLIV	Number of repetitions	Length of time domain window	DMRS-less	DMRS-more
0	A	$\{S_0, L_0\}$	n1	T_0 slots	Type 1	Rel. 15/16 dmrs-AdditionalPosition
1	B	$\{S_1, L_1\}$	n2	T_1 slots	Type 2	Rel. 15/16 dmrs-AdditionalPosition
...	...	...	...	...	...	...

1400

Fig. 14





1600

Fig. 16

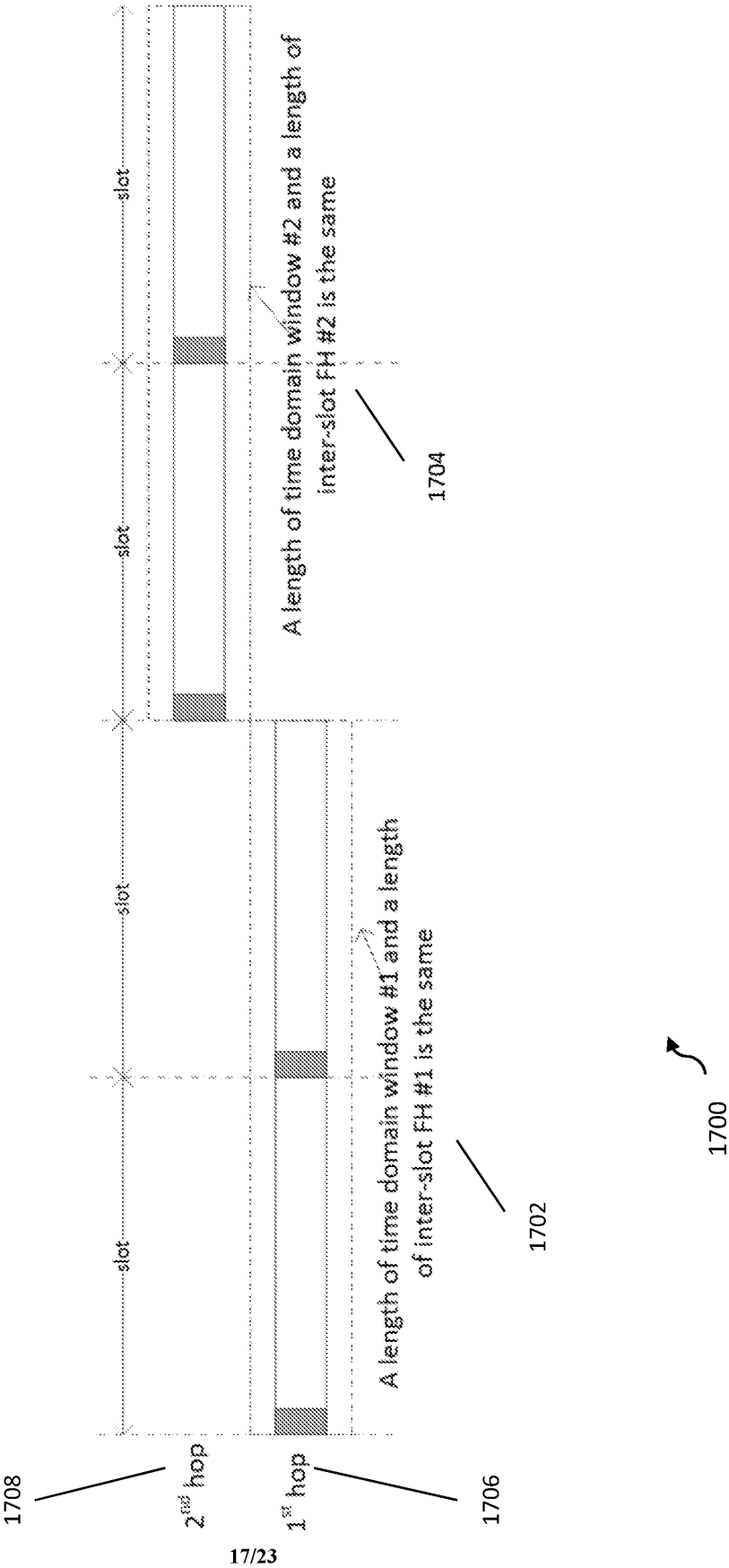


Fig. 17

Bit field mapped to index	PUSCH frequency hopping	Joint CE
0	Disabled	Disabled
1	Enabled	Enabled, the lengths of the one or more time domain windows and/or further parameters are configured via the enhanced TDRA table in Embodiment 1 by using at least DCI, MAC CE, or RRC

1800

Fig. 18

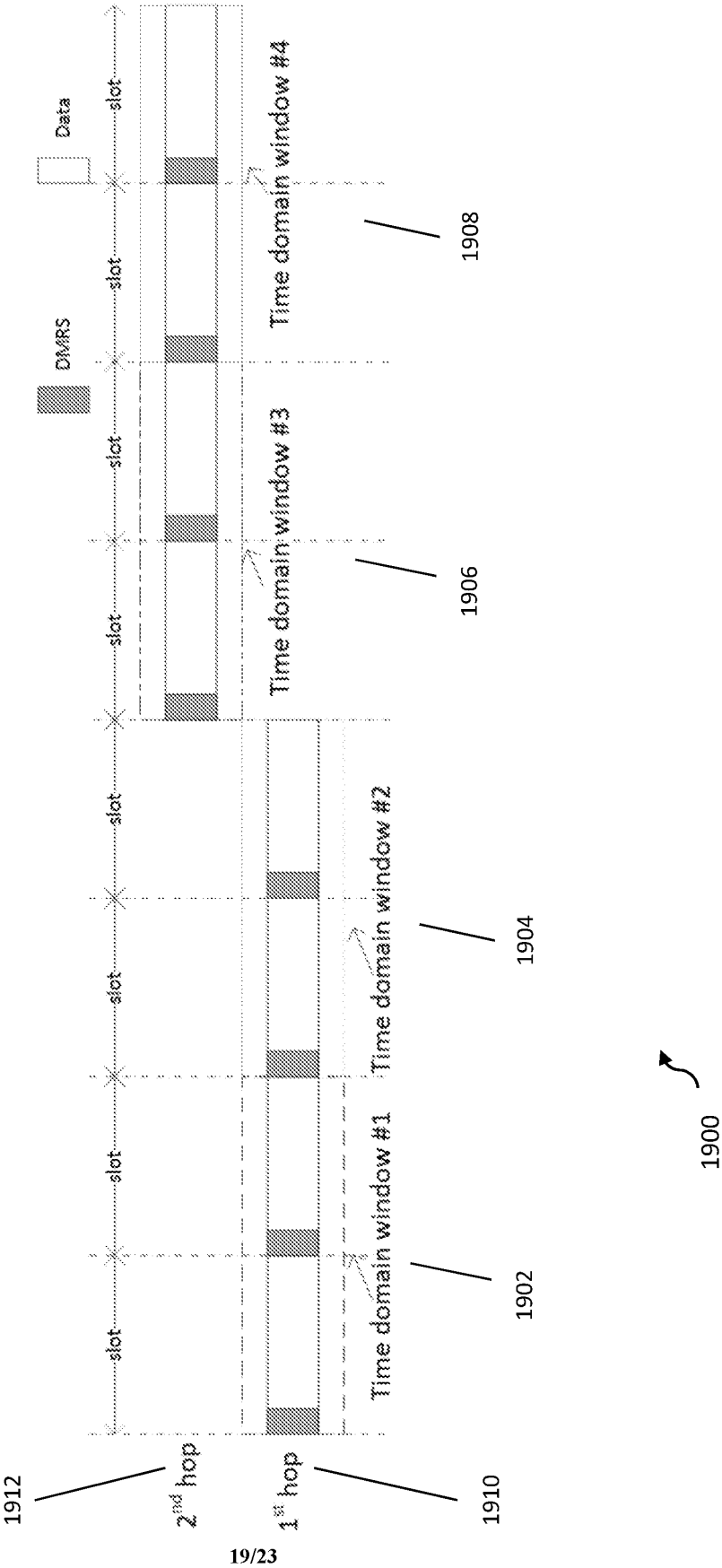


Fig. 19

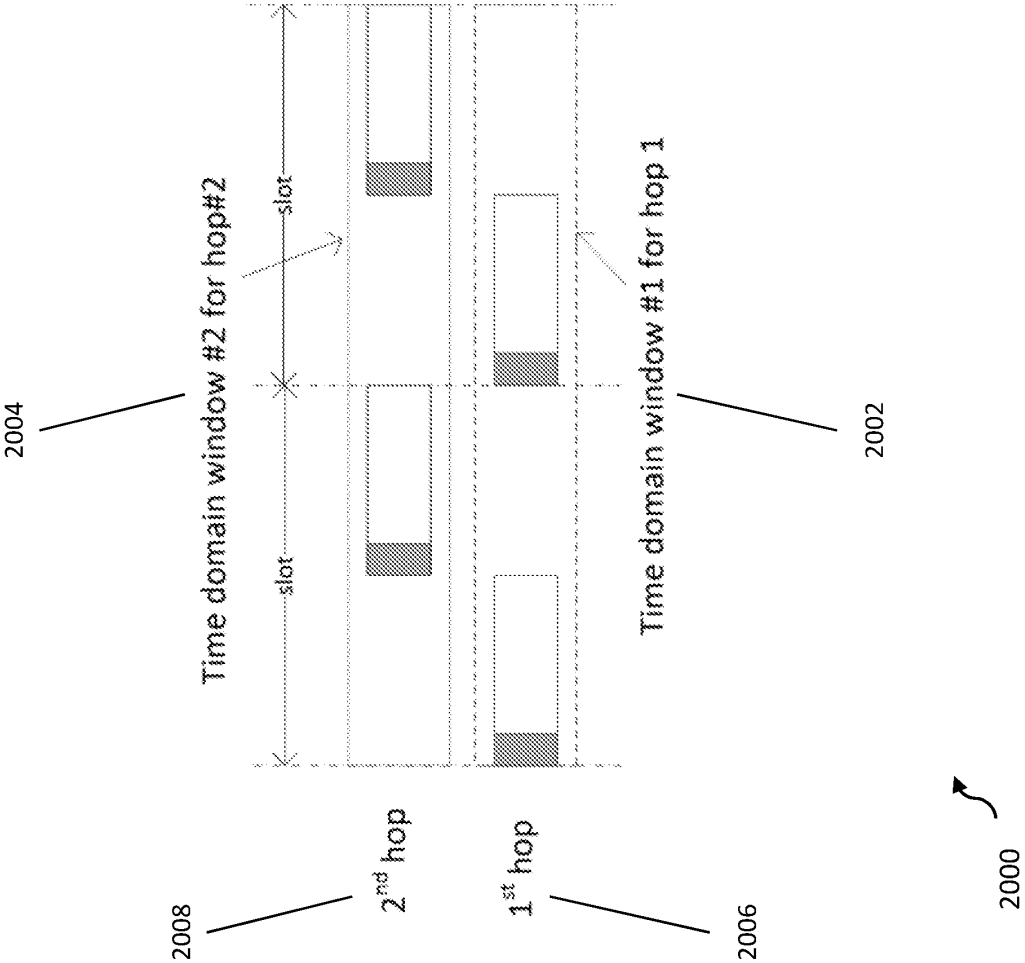


Fig. 20

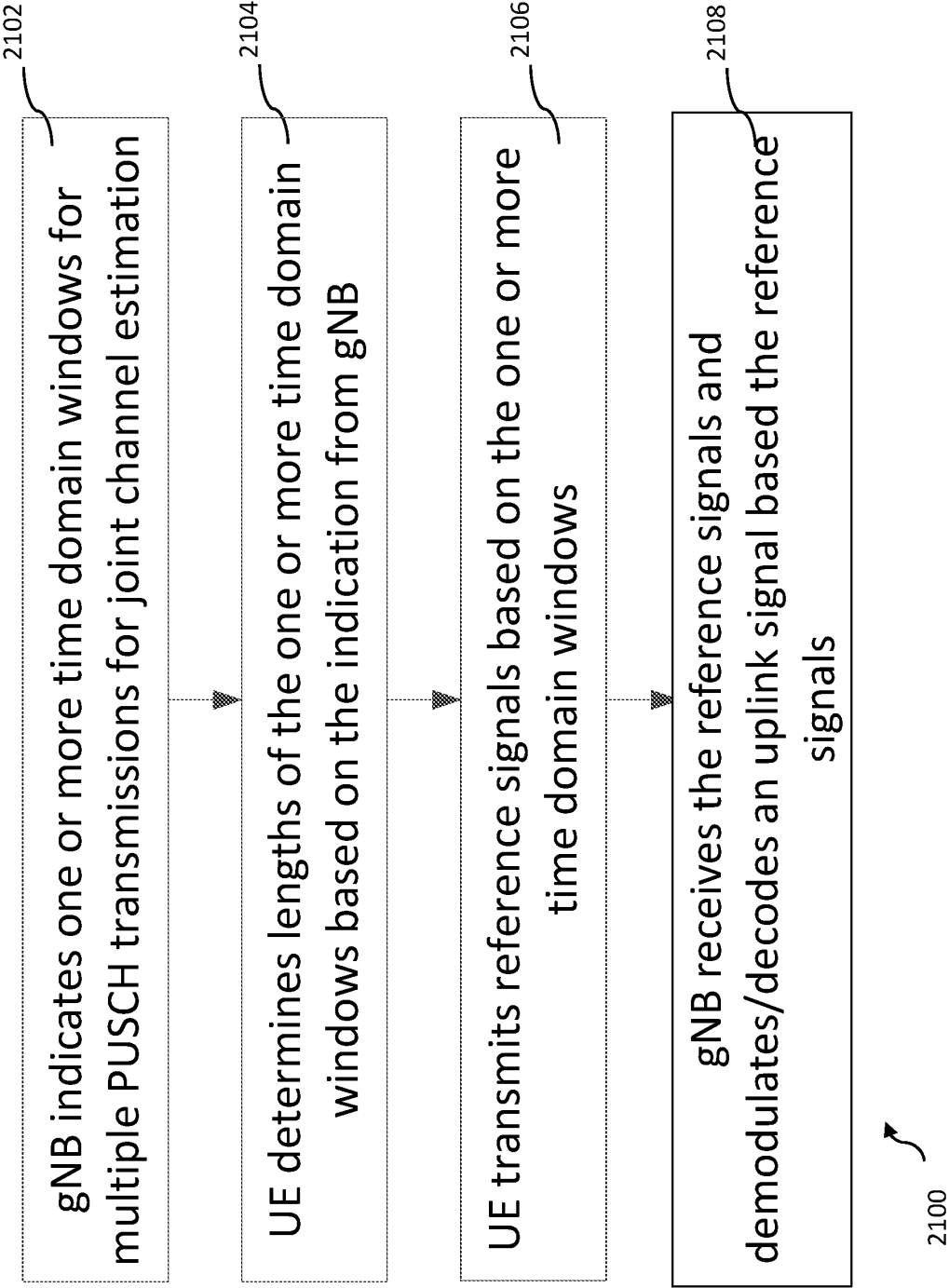
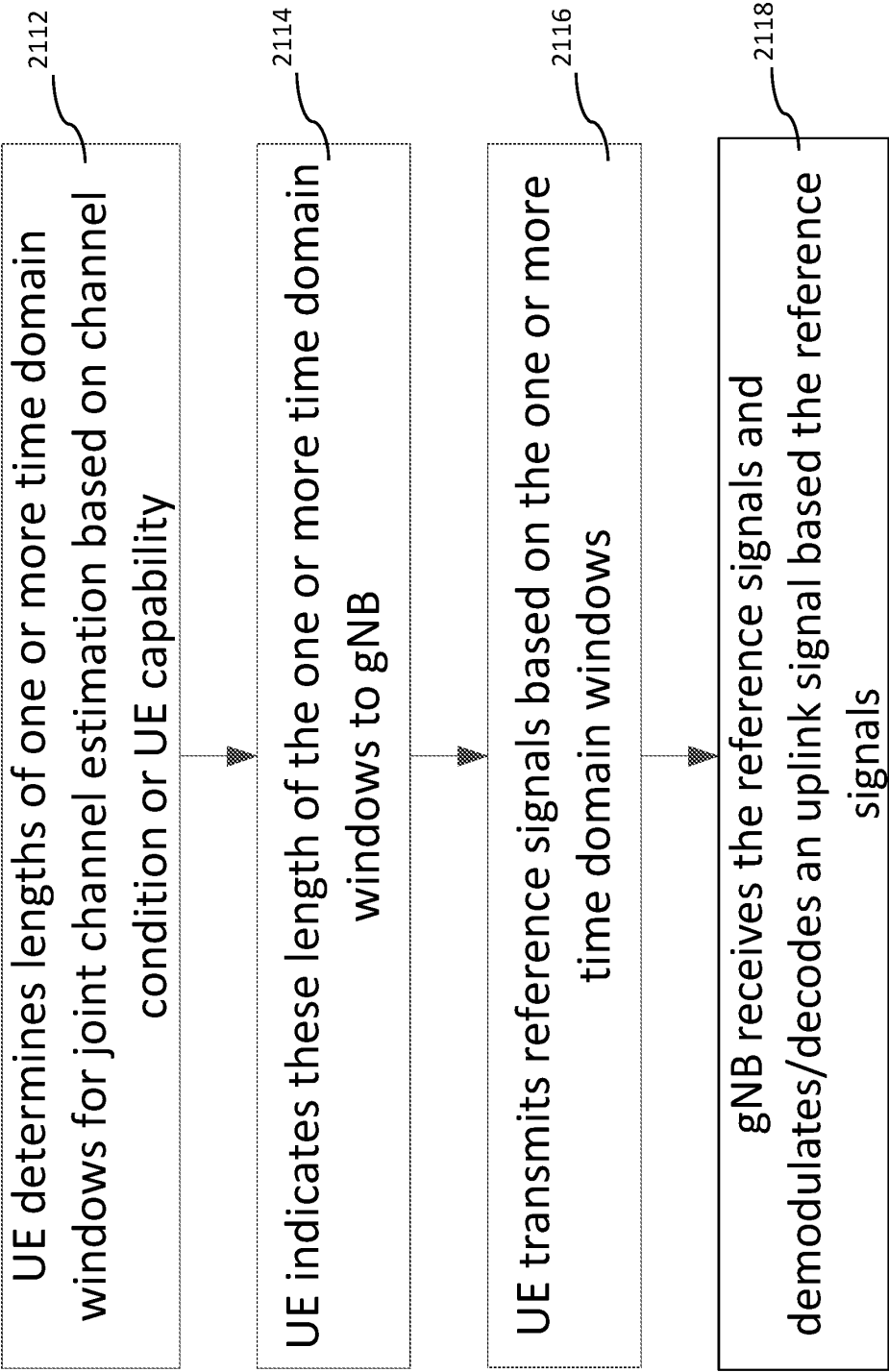


Fig. 21A



2110 ↗

Fig. 21B

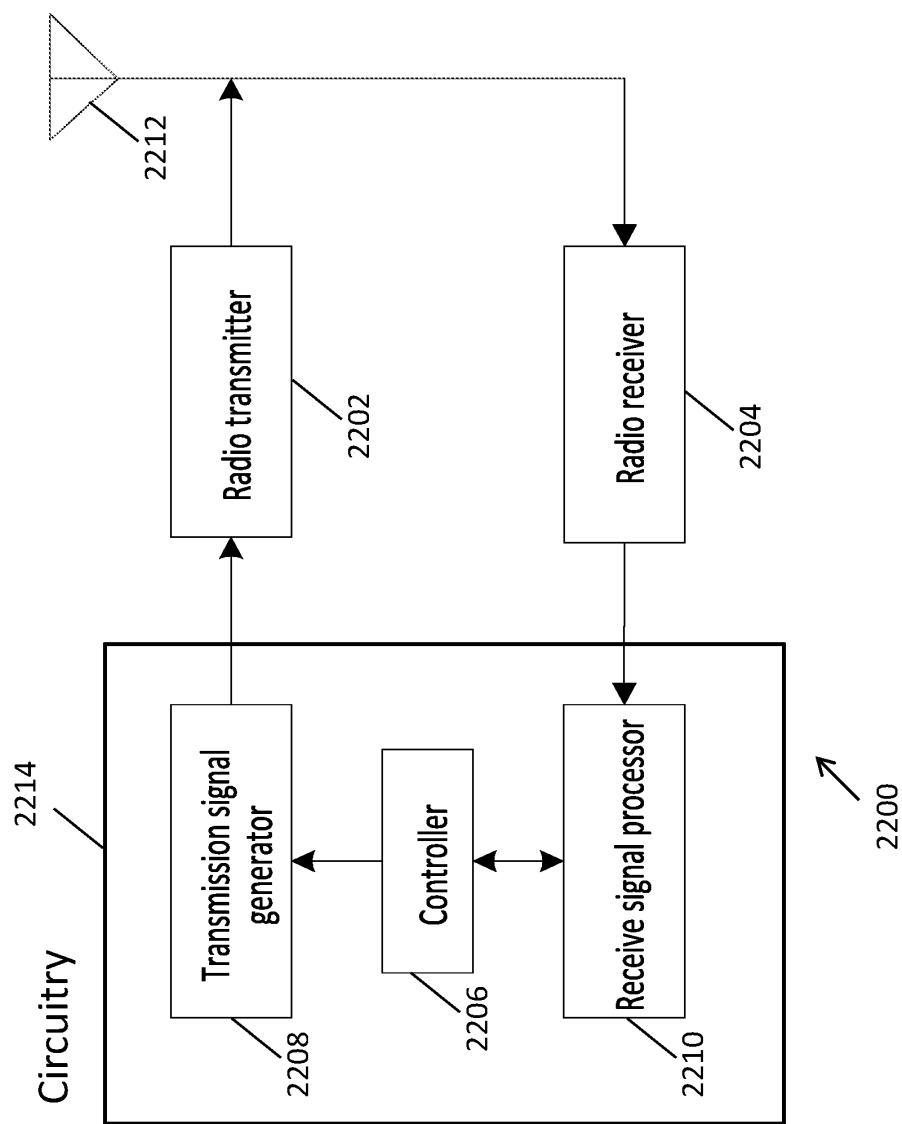


Fig. 22