

ABSTRACT

A video information reproduction system includes first and second video information reproduction devices (10, 20). A first synchronization signal processor (150) of the video information reproduction device (10) as a master device generates a phase synchronization signal (Psync) including a first vertical synchronization signal (Vsync1) and a first counter value (Vcount1) and sends it to the second video information reproduction device (20). A second synchronization signal processor (250) of the second video information reproduction device (20) as a slave device compares the phase synchronization signal (Psync) with a second vertical synchronization signal (Vsync2) on the basis of a second video clock signal (Vclk2), and a second clock generator (240) changes the frequency of a second decode reference clock signal (Dclk2) on the basis of a result of the comparison.