What is claimed is:

1. A method of setting a power of a secondary device-to-device synchronization signal, SD2DSS, by a first wireless device to enable a second wireless device to synchronize timing of the second wireless device to a timing derived from the first wireless device, the method comprising:

determining power of a primary device to device synchronization signal, PD2DSS (S104) transmitted by the first wireless device (S100); and

setting the power of the SD2DSS to be less than the power of the PD2DSS (S102).

2. The method of Claim 1, wherein setting the power of the SD2DSS to be less than the power of the PD2DSS comprises setting the power of the SD2DSS to be less than the power of the PD2DSS by a predetermined or configurable power offset.

3. The method of Claim 1 or 2, wherein the SD2DSS is transmitted with a transmission power less than PD2DSS, as determined by a power offset compared to the PD2DSS.

4. The method of Claims 2 or 3, wherein the power offset is -4 dB.

5. The method of Claim 1, wherein the PD2DSS includes a Zadoff-Chu, ZC, sequence and the SD2DSS includes an M sequence.

6. The method of Claim 2, further comprising determining the power offset autonomously by the first wireless device.

7. The method of Claim 1, wherein the set power of the SD2DSS is adjusted only when the power of the PD2DSS exceeds a predetermined amount (S108).

8. The method of Claim 1, wherein a same circuitry generates the SD2DSS and a secondary synchronization signal, SSS,

9. A wireless device (32) configured to set a power of a secondary device-to-device synchronization signal, SD2DSS, to enable a second wireless device to synchronize timing of the second wireless device to a timing of the wireless device, the wireless device comprising:

a processor (40); and

a memory (38), the memory (38) containing instructions executable by the processor, the instructions when executed configure the processor to:

determine power of a primary device to device synchronization signal, PD2DSS (S104) transmitted by the wireless device (48); and

set the power of the SD2DSS to be less than the power of the PD2DSS.

10. The wireless device (32) of Claim 9, wherein the power of the SD2DSS is set to be less than the power of the PD2DSS wherein the power of the SD2DSS is set to be less than the power of the PD2DSS by a predetermined or configurable power offset.

11. The wireless device (32) of Claim 9, wherein the PD2DSS includes a Zadoff-Chu, ZC, sequence and the SD2DSS includes an M sequence.

12. The wireless device of Claim 10, wherein the power offset is determined autonomously by the device.

13. A method of determining and transmitting one of a power offset and a power threshold to a wireless device, the method comprising:

determining at least one of a power offset and a power threshold for setting a power of a secondary device to device synchronization signal, SD2DSS, by a wireless device; and

transmitting at least one of the power offset and the power threshold to the wireless device.

14. A network node, comprising:

a processor;

a memory containing instructions that when executed by the processor configure the processor to determine at least one of a power offset and a power threshold for setting a power of secondary device to device synchronization signal, SD2DSS by a wireless device;

a communication interface configured to transmit at least one of the power offset and the power threshold to the wireless device; and

the memory configured to store the at least one of the power offset and the power threshold.