

FIG. 1

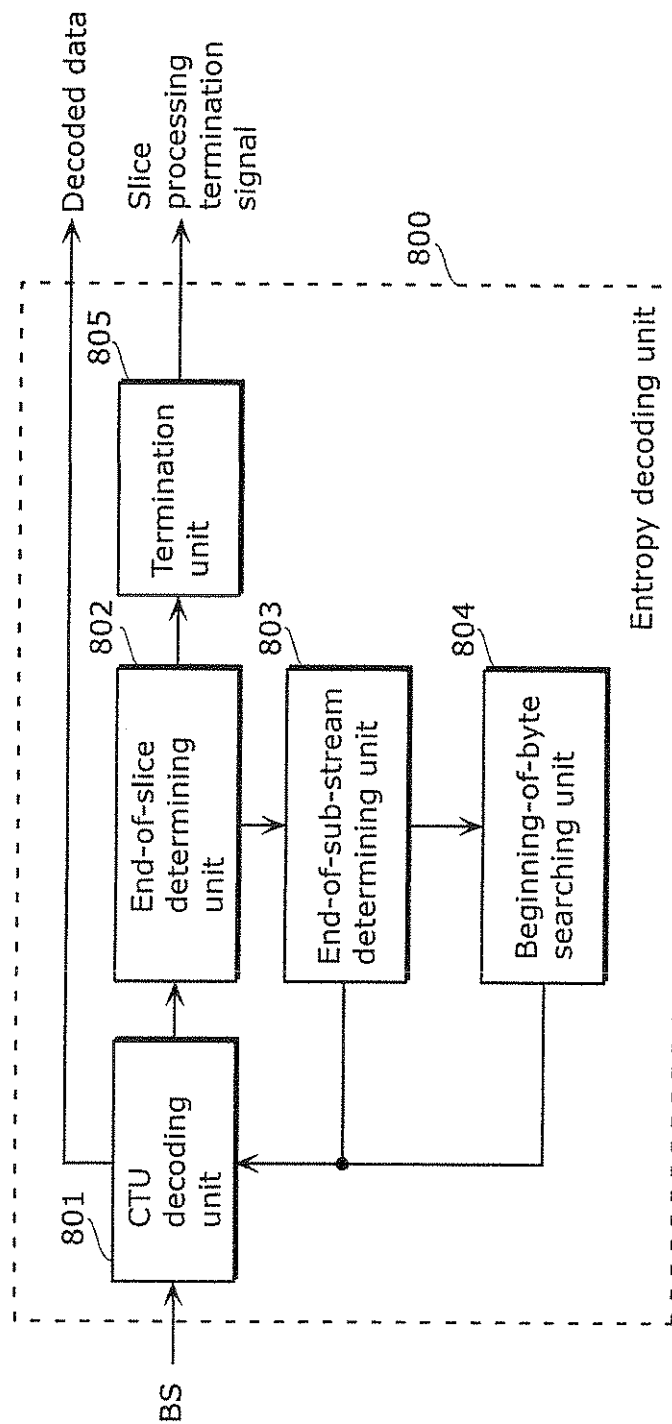


FIG. 2

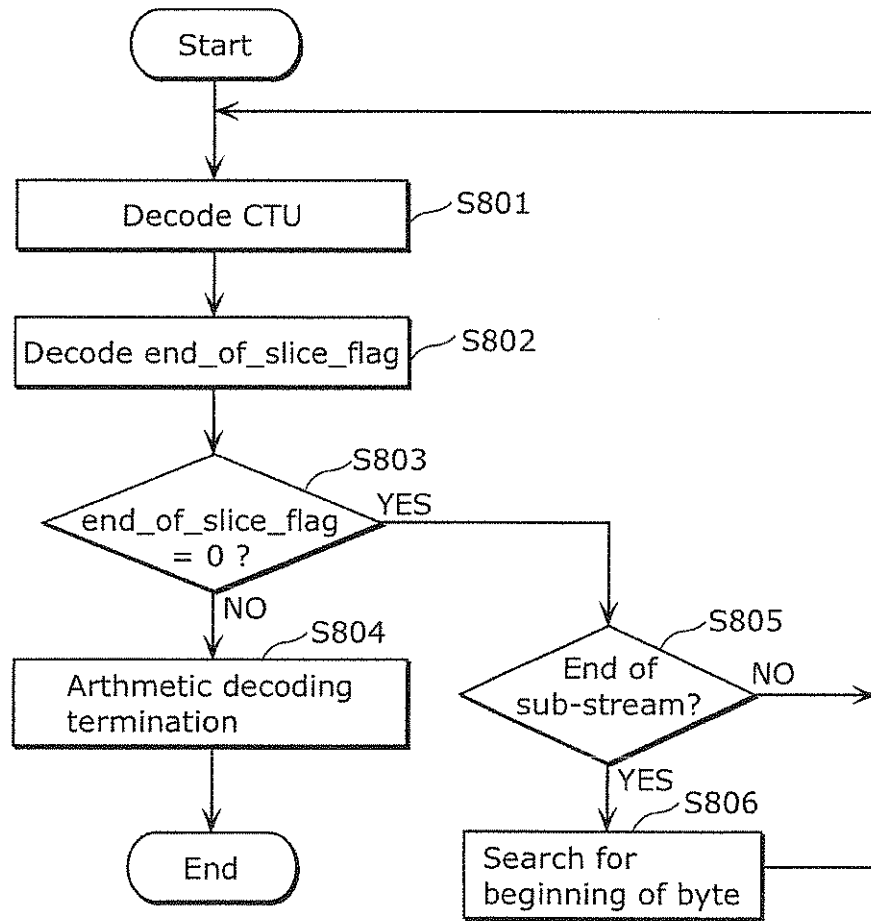


FIG. 3A

slice_data() {		Descriptor
do {		
coding_tree_unit()	851	
end_of_slice_flag	852	ae(v)
CtbAddrTS++	853	
CtbAddrRS = CtbAddrTStoRS[CtbAddrTS]		
if(!end_of_slice_flag && (tiles_enabled_flag && TileId[CtbAddrTS] != TileId[CtbAddrTS - 1]) (entropy_coding_sync_enabled_flag && CtbAddrTS % PicWidthInCtbsY == 0)))		
byte_alignment()	854	
while(!end_of_slice_flag)		
}		

FIG. 3B

854	
byte_alignment()	Descriptor
{bit_equal to one /* equal to 1 */ 855	f(1)
while(!byte_aligned())	
{bit_equal to zero /* equal to 0 */ 856	f(1)
}	

FIG. 4

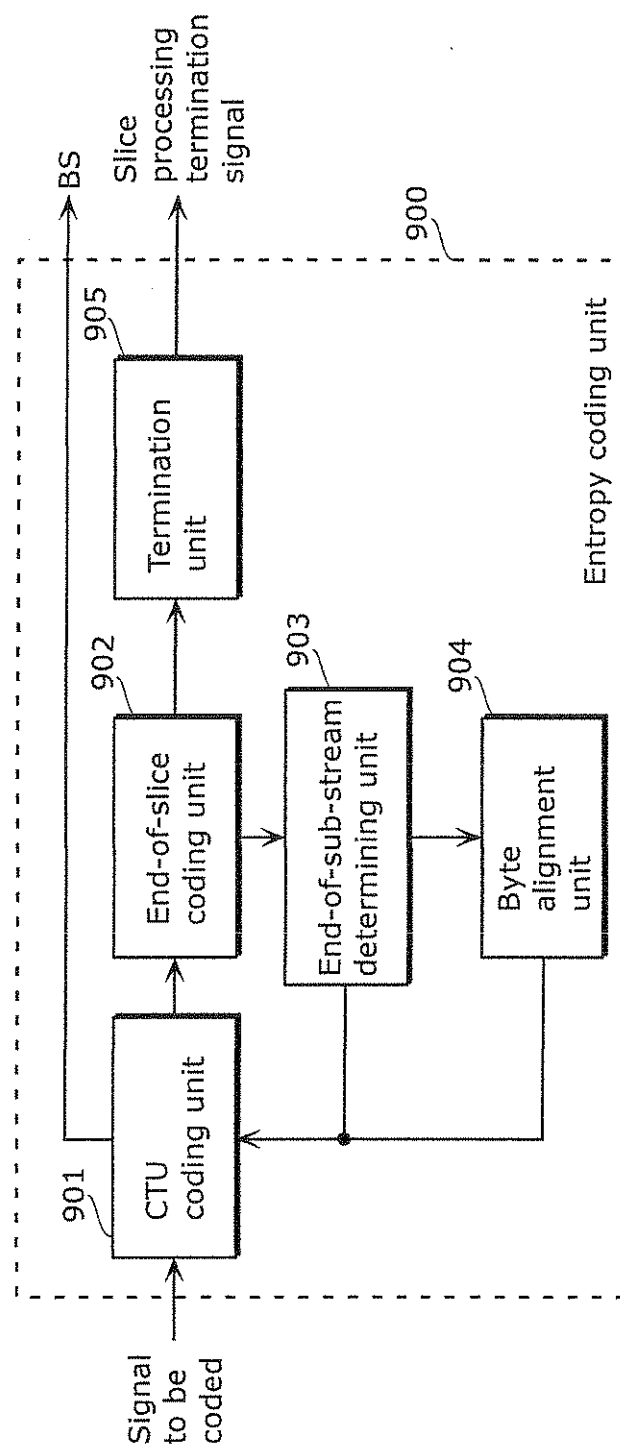


FIG. 5

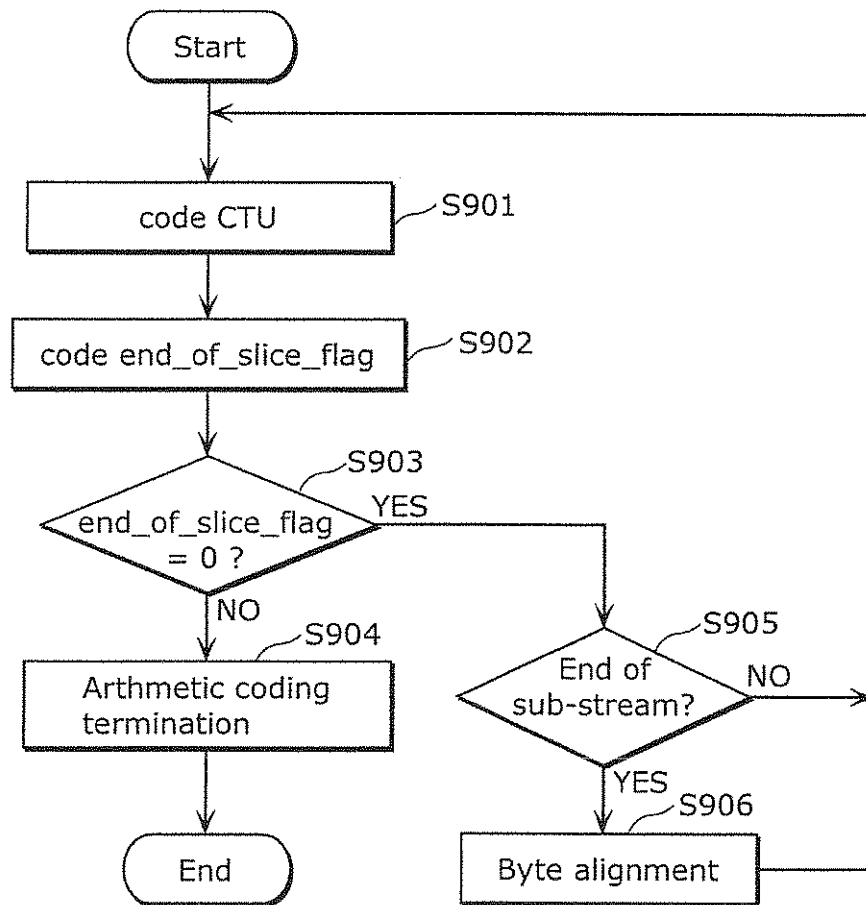


FIG. 6

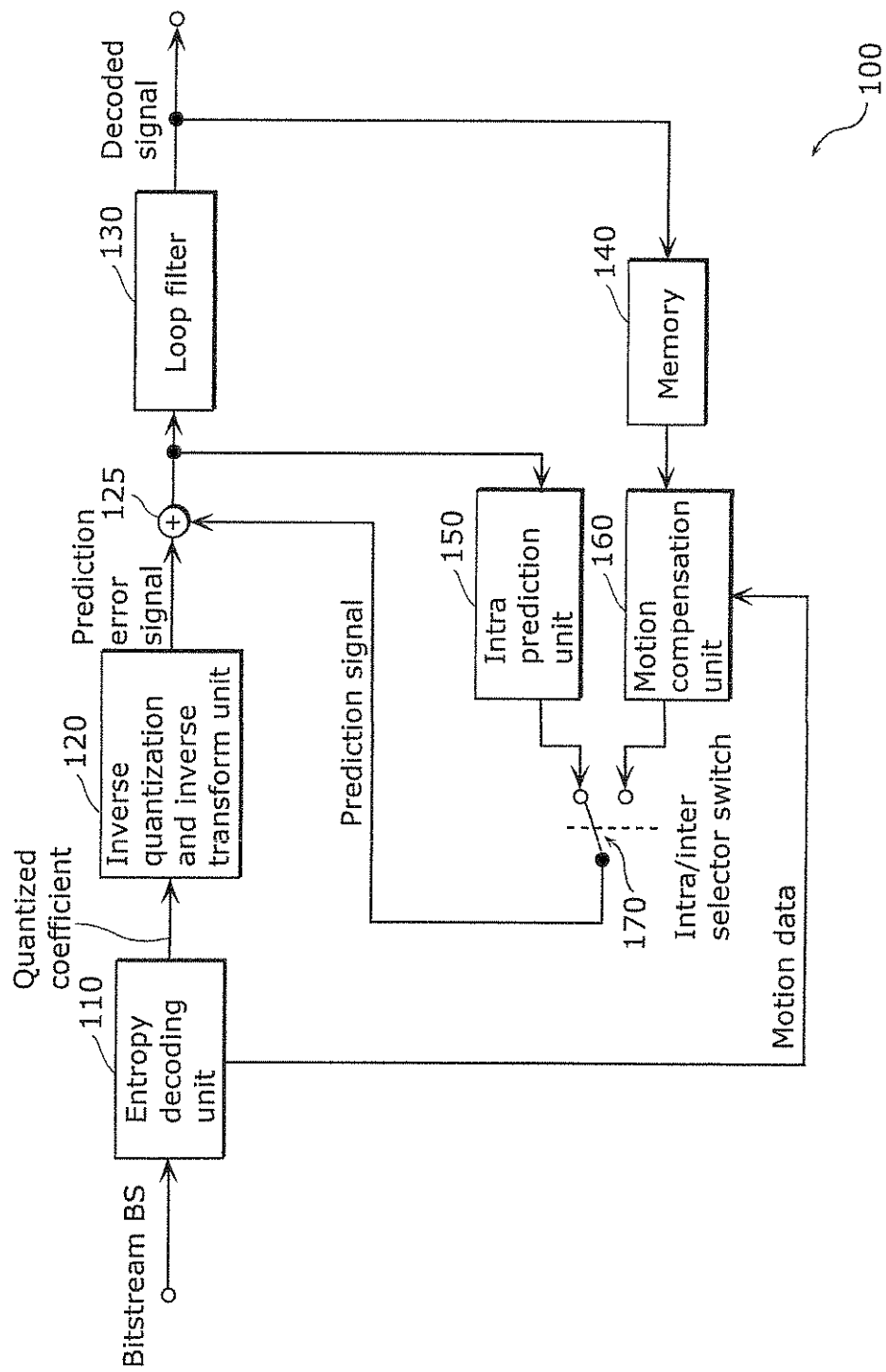


FIG. 7

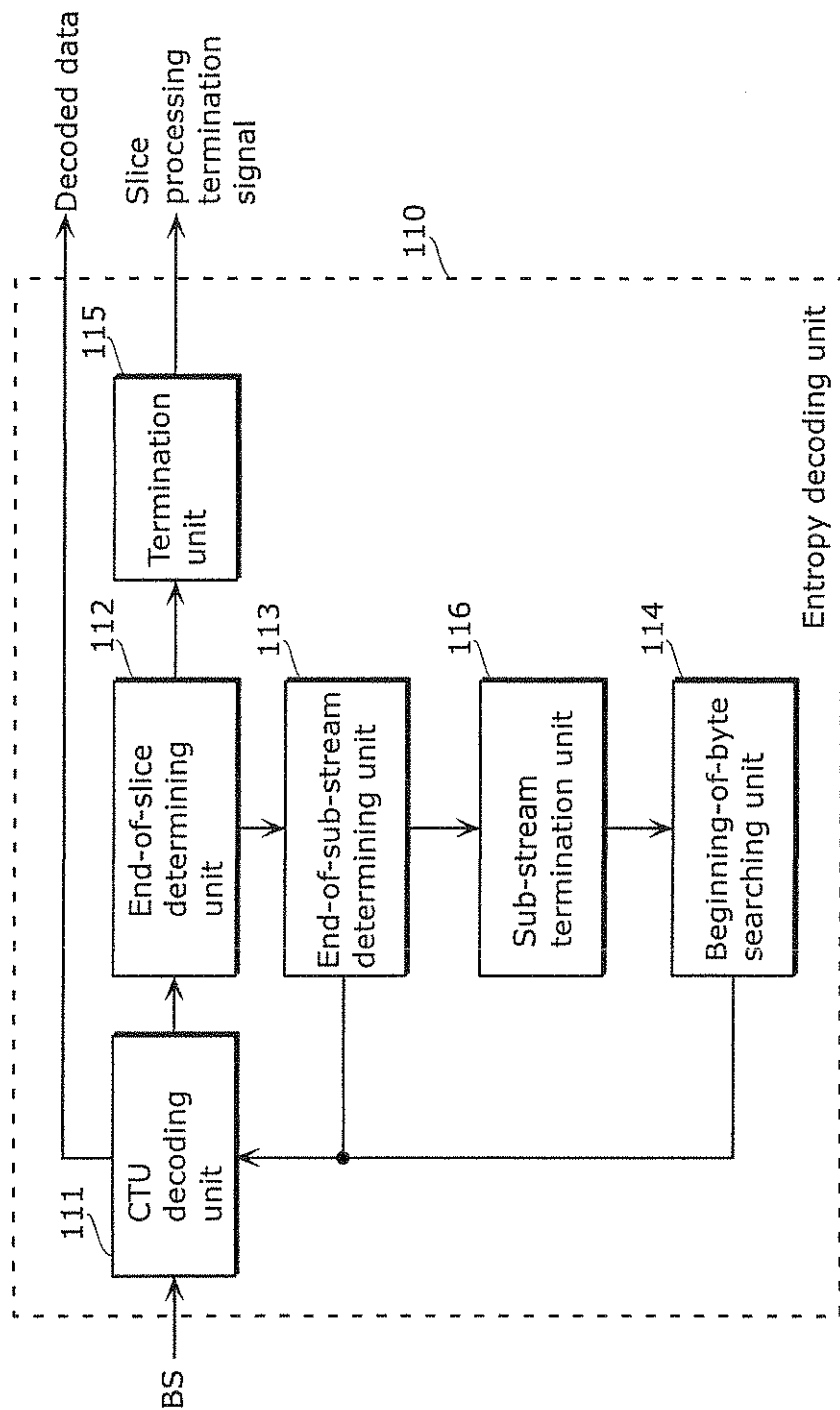


FIG. 8

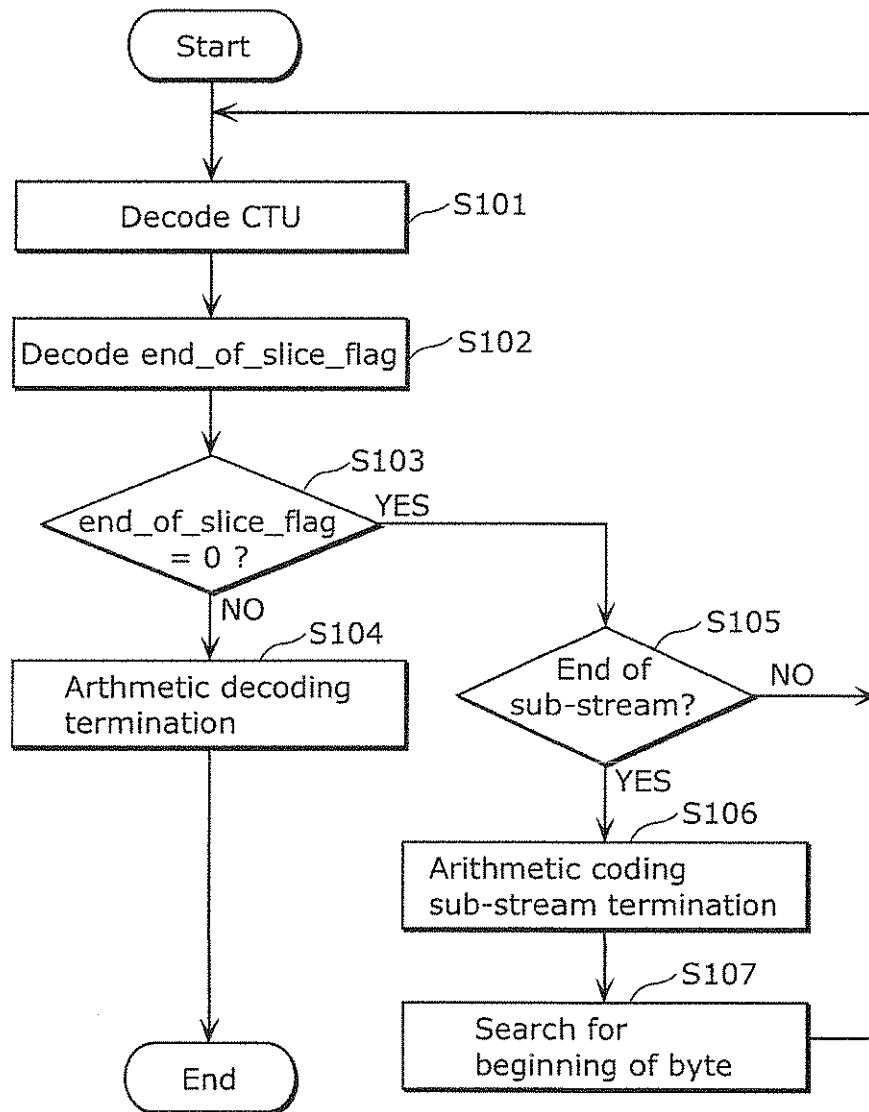


FIG. 9

slice_data() {	Descriptor
do {	
coding_tree_unit() 181	
end_of_slice_flag 182	ae(v)
CtbAddrTS++	
CtbAddrRS = CtbAddrTStoRS[CtbAddrTS]	
if(!end_of_slice_flag && ((tiles_enabled_flag && TileId[CtbAddrTS] != TileId[CtbAddrTS - 1]) (entropy_coding_sync_enabled_flag && CtbAddrTS % PicWidthInCtbsY == 0)))	
{	
end_of_sub_stream_one_bit /* equal to 1 */ 184	ae(v)
byte_alignment() 185	
}	
while(!end_of_slice_flag)	
}	

FIG. 10

byte_alignment()	Descriptor
bit_equal_to_one /* equal to 1 */ 185a	ae(v)
while(!byte_aligned())	
bit_equal_to_zero /* equal to 0 */ 185b	f(1)
}	

FIG. 11

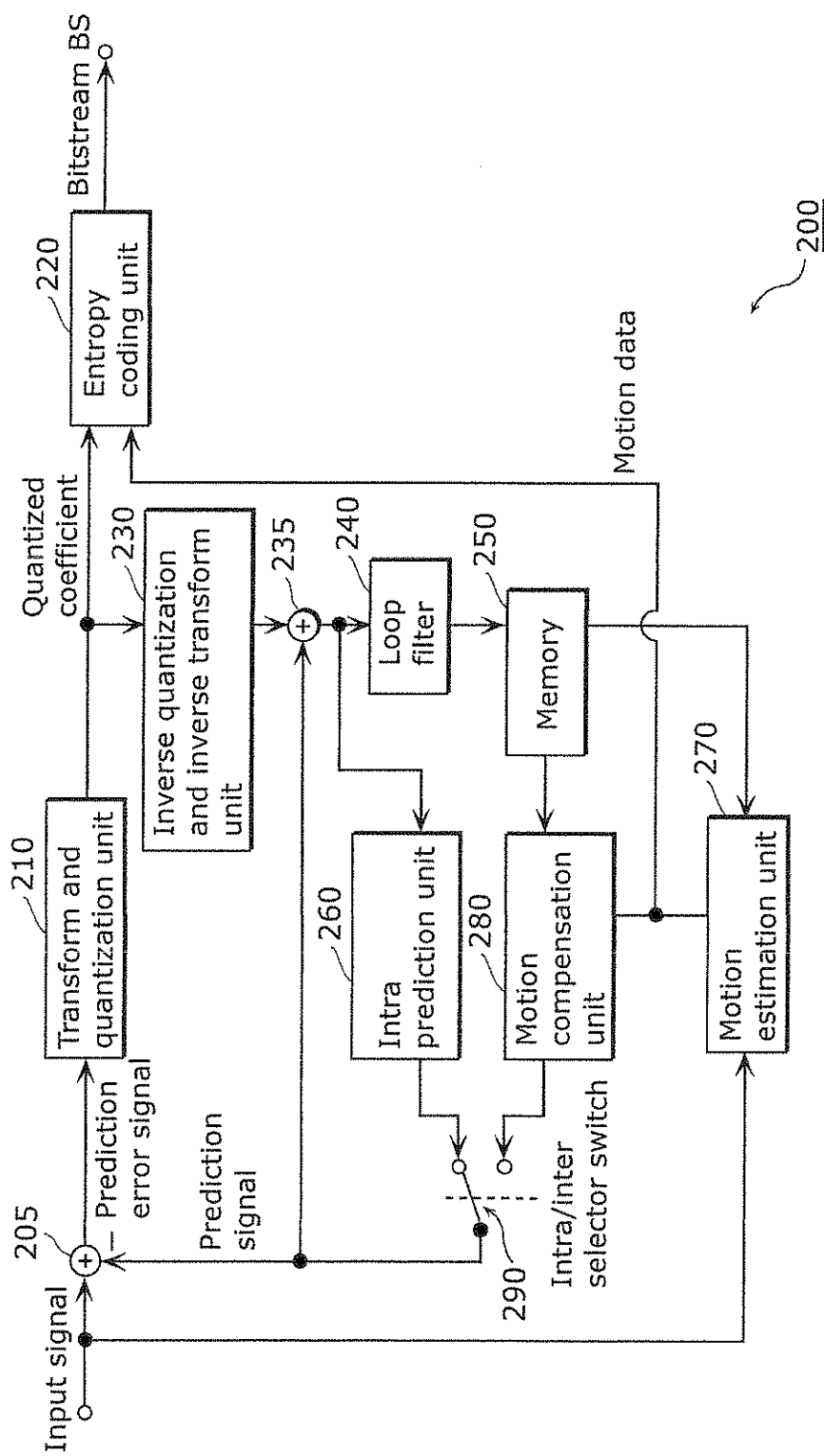


FIG. 12

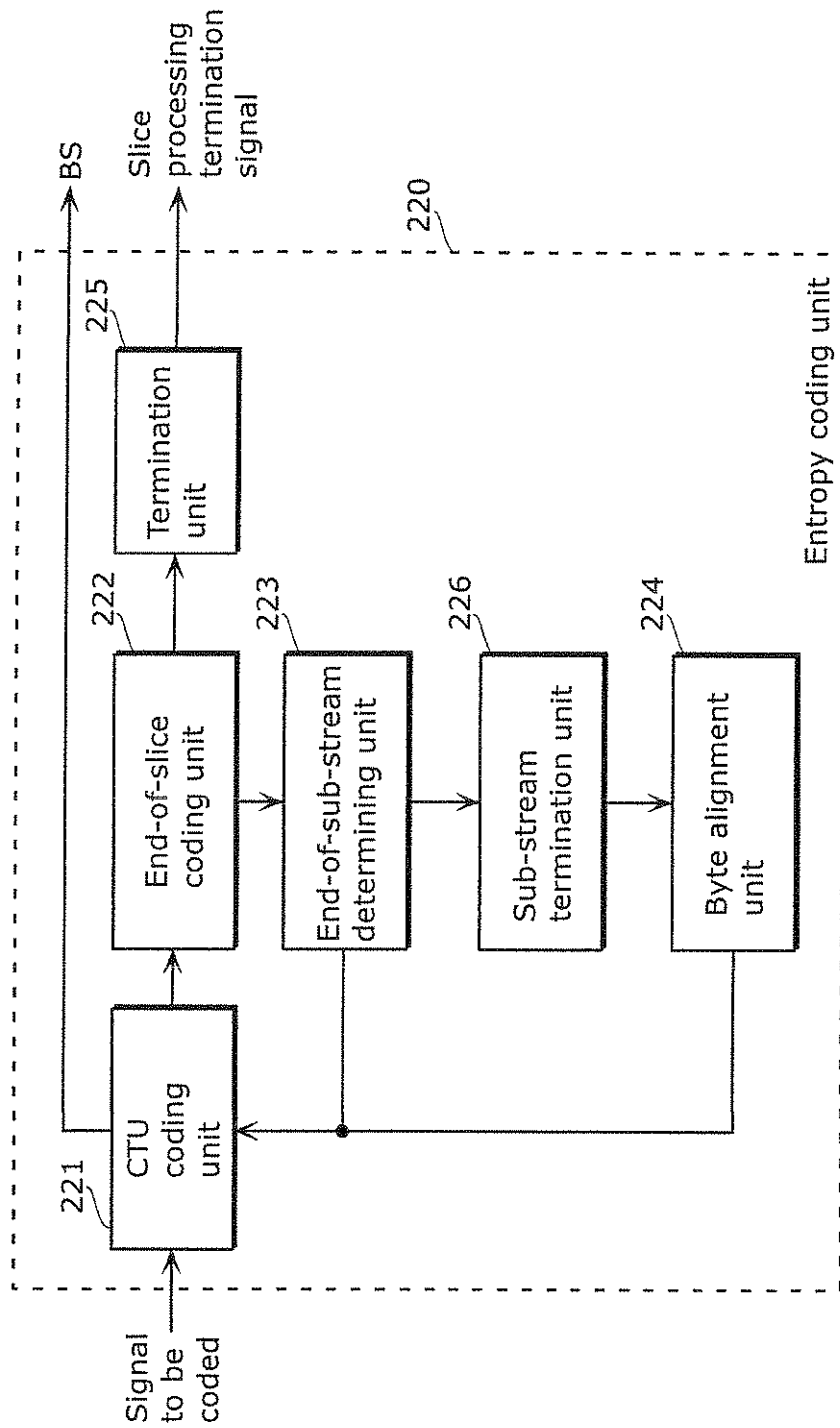


FIG. 13

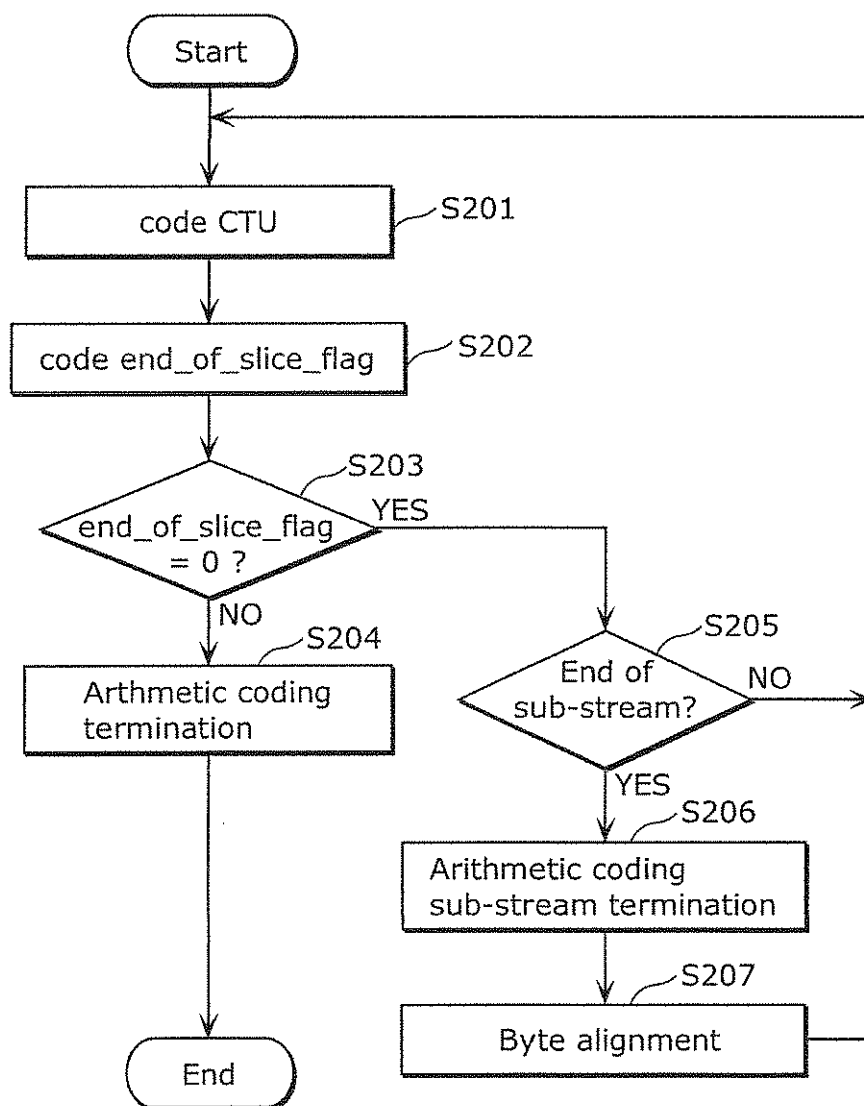


FIG. 14A

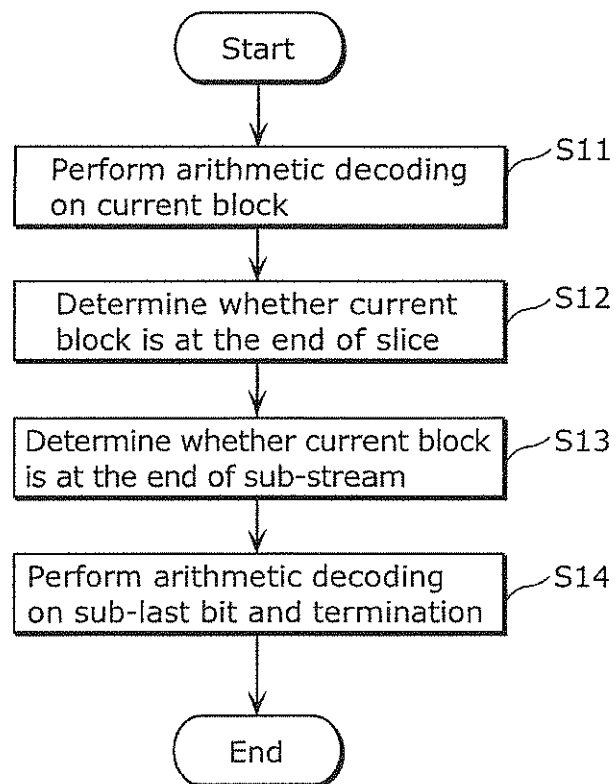


FIG. 14B

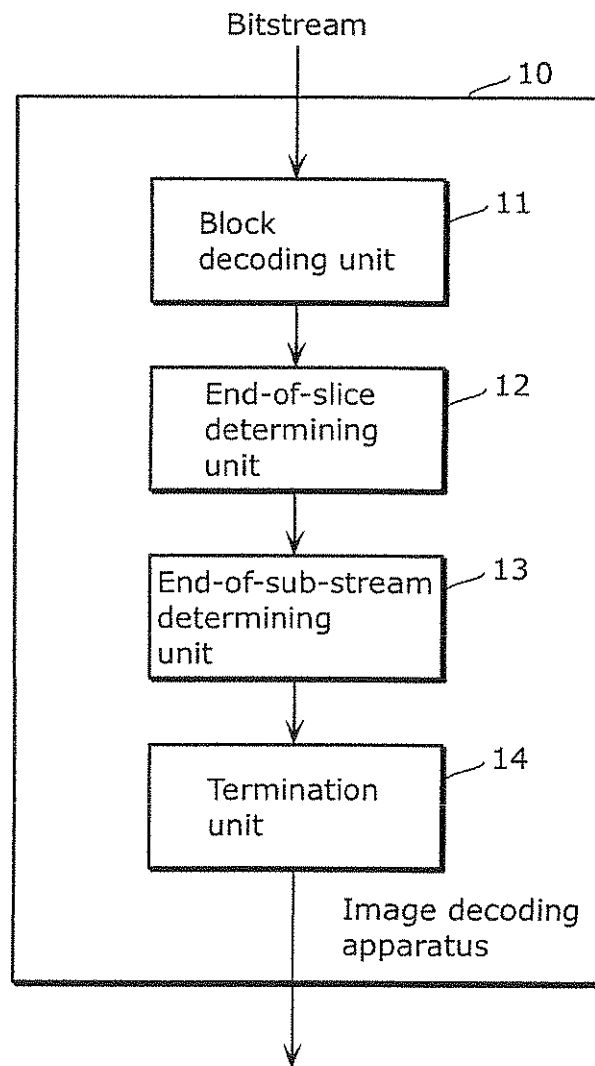


FIG. 15A

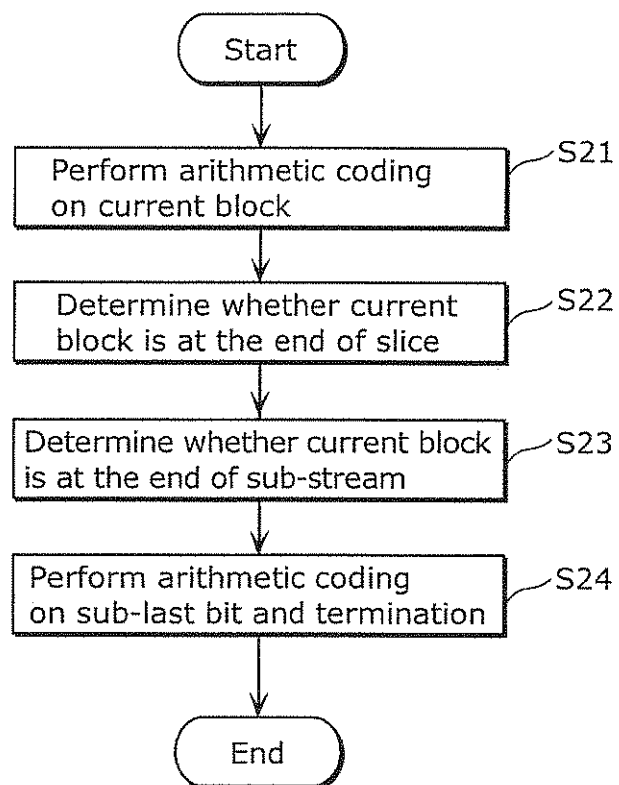


FIG. 15B

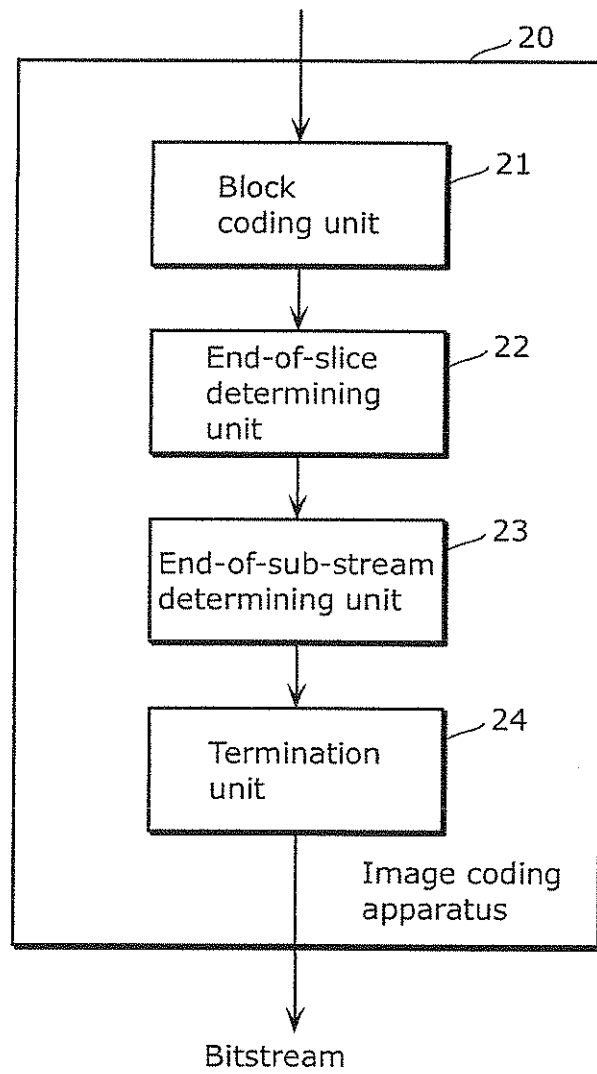


FIG. 16

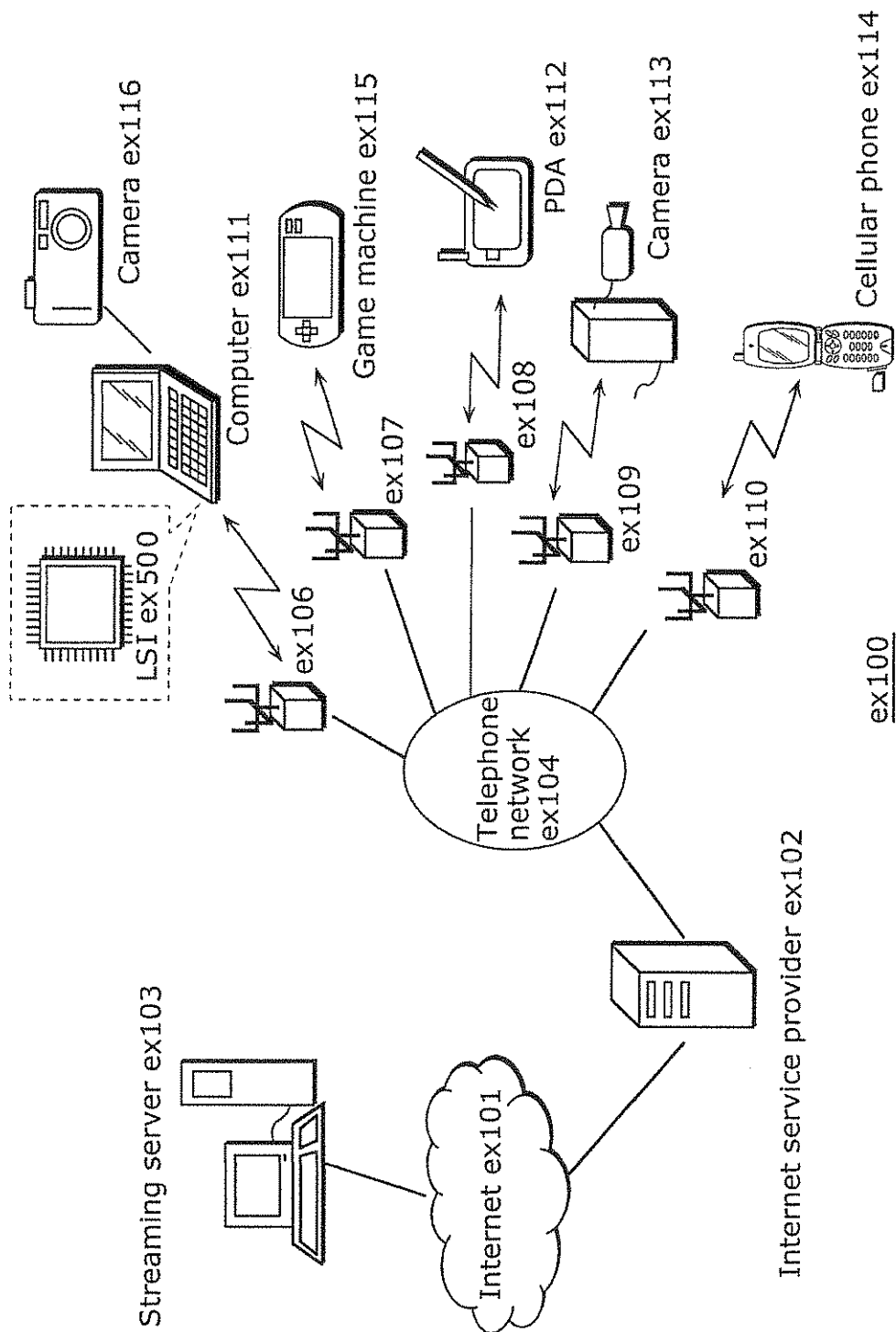


FIG. 17

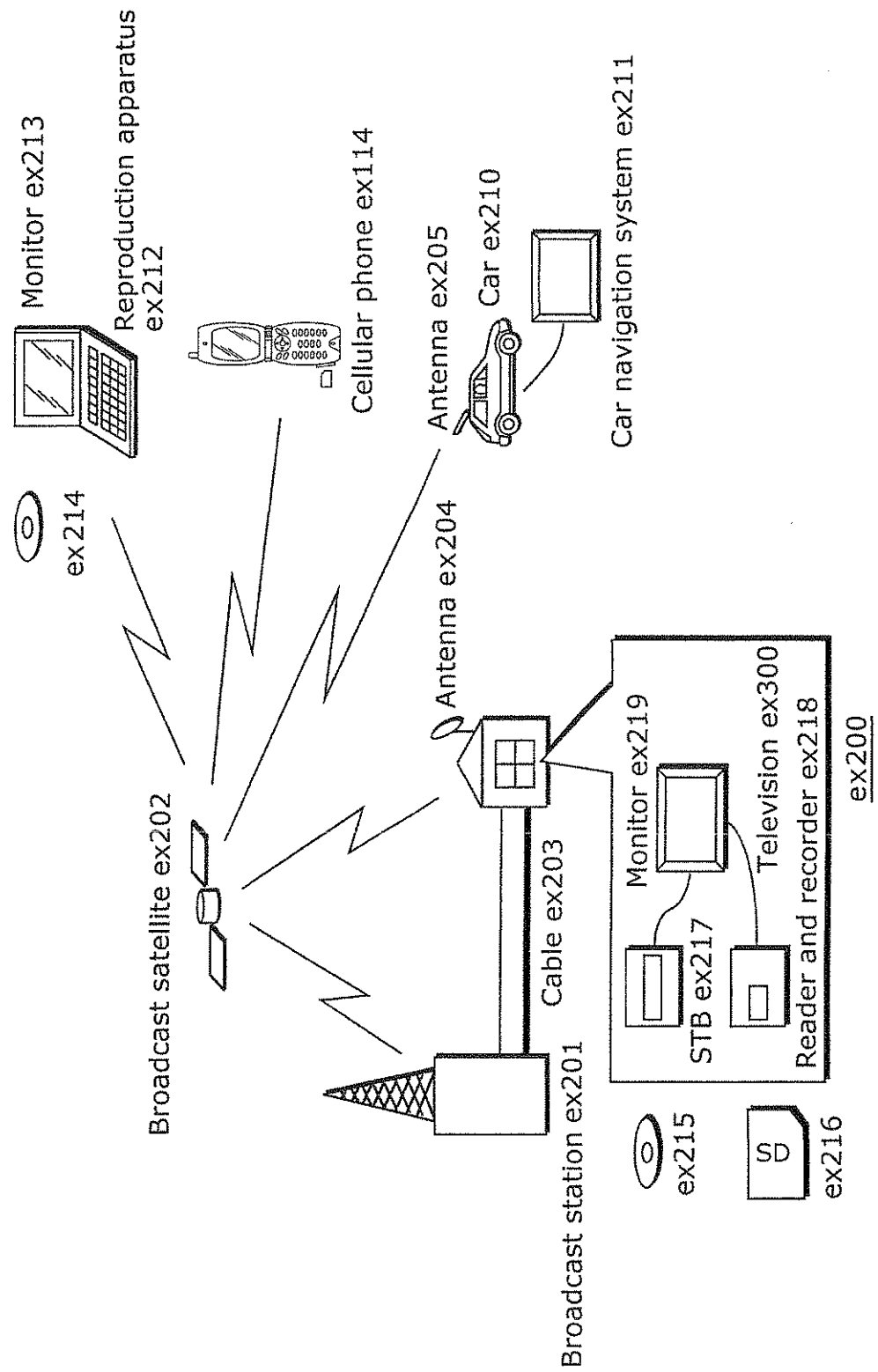


FIG. 18

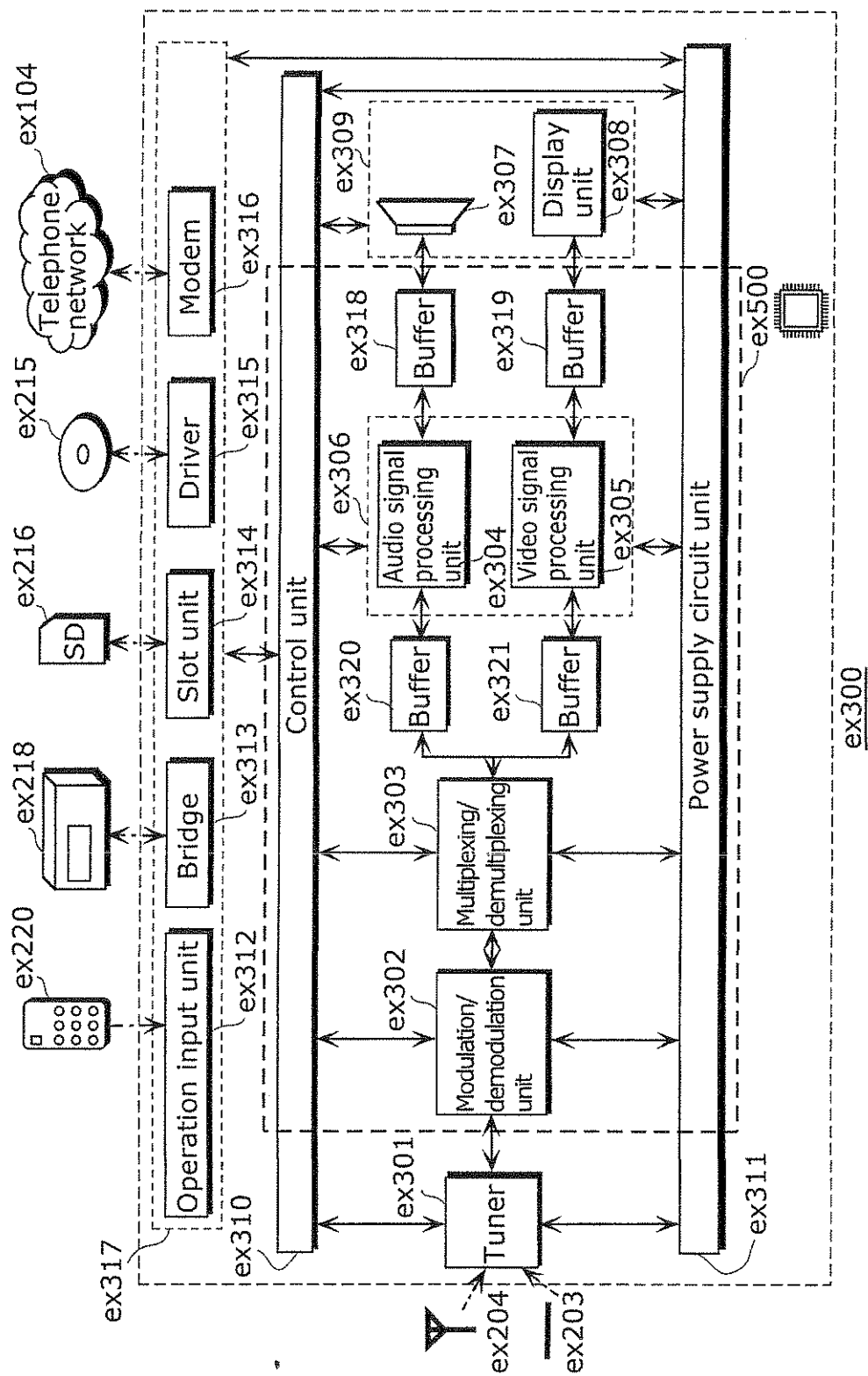


FIG. 19

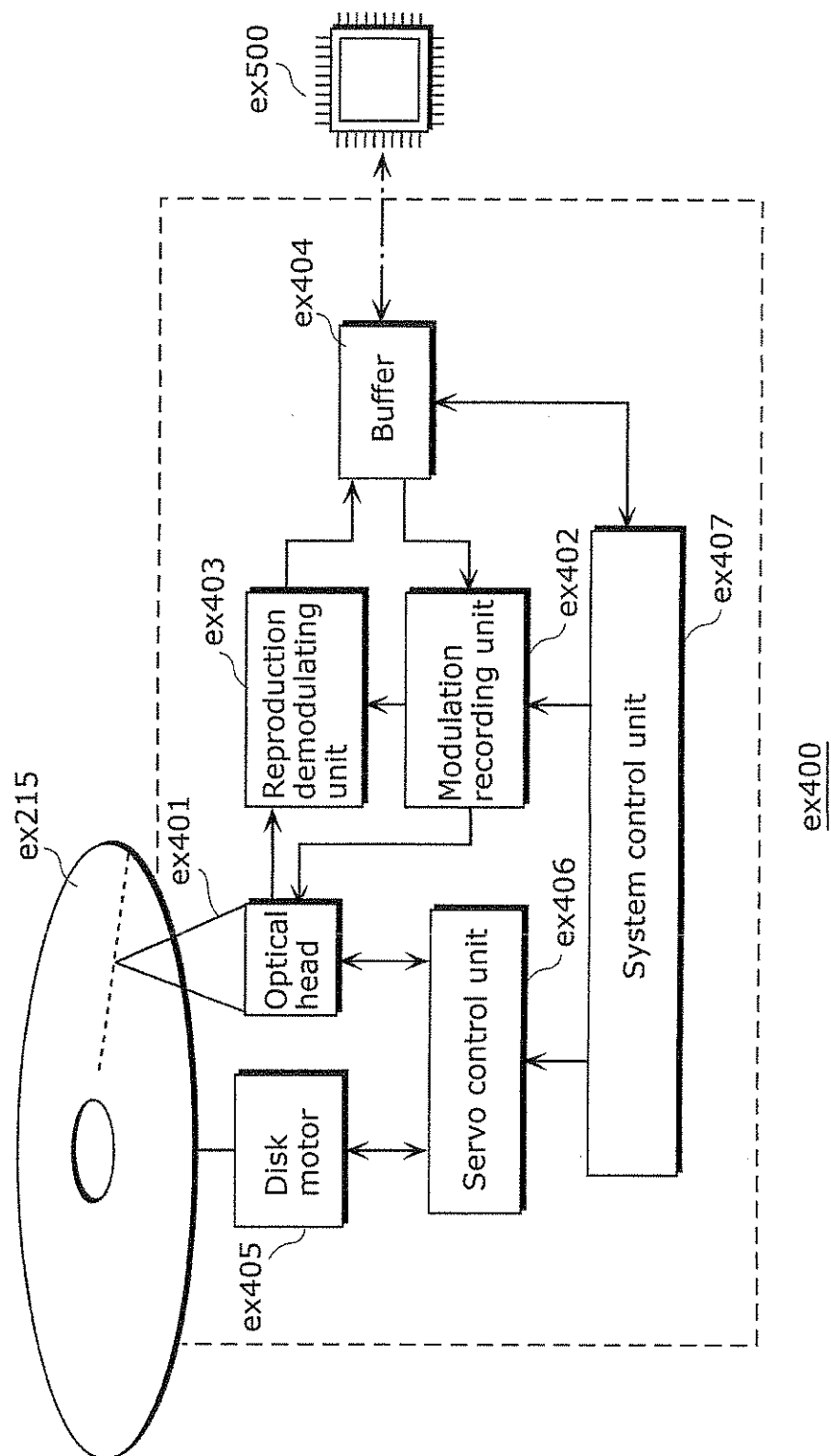


FIG. 20

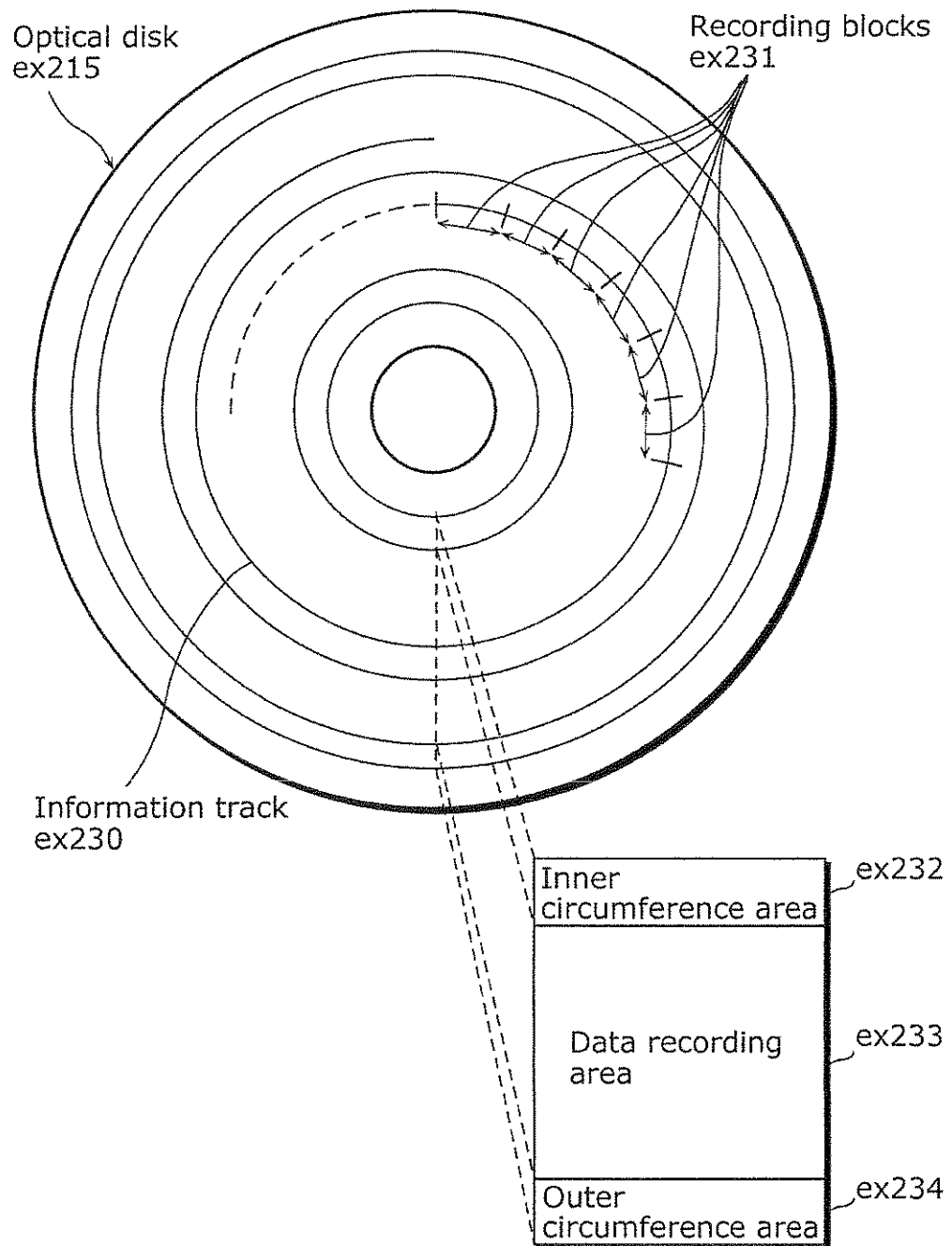


FIG. 21A

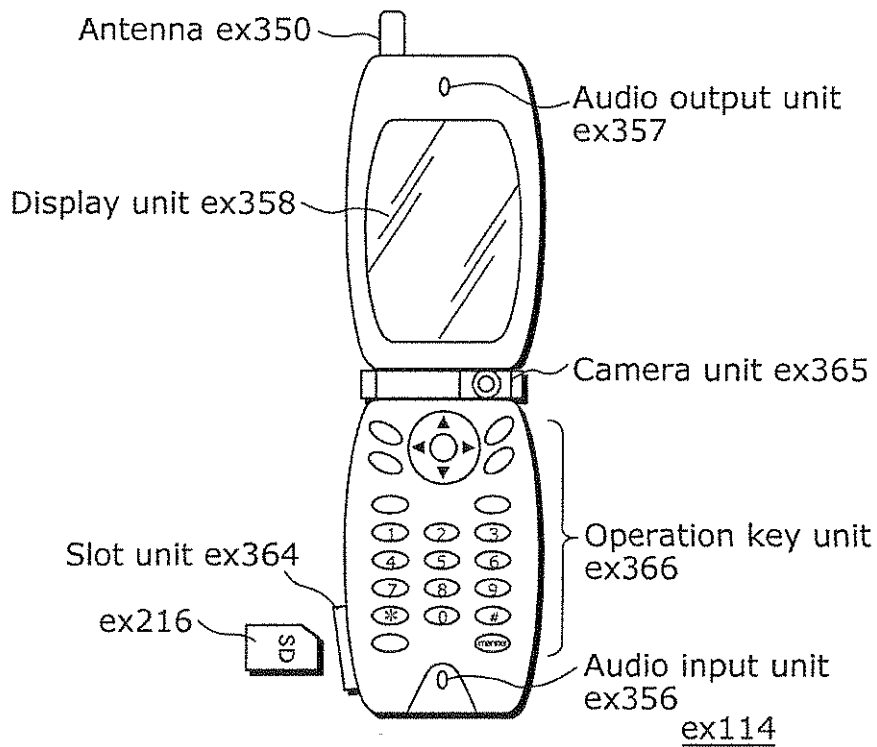


FIG. 21B

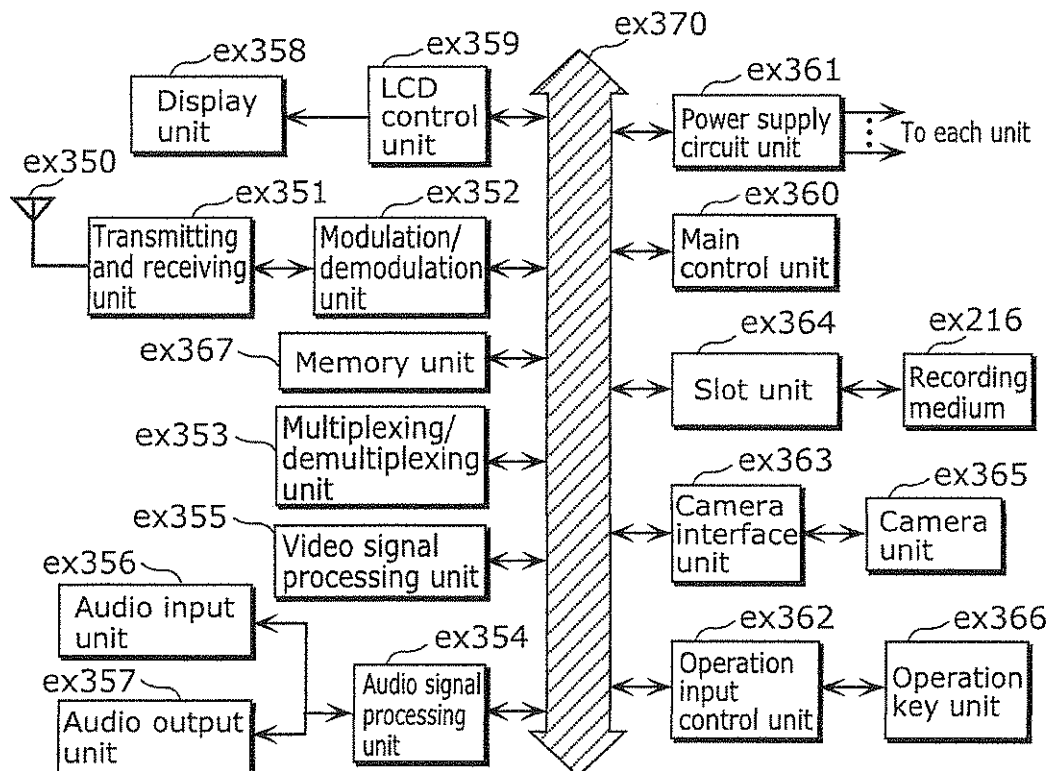


FIG. 22

Video stream (PID=0x1011, Primary video)
Audio stream (PID=0x1100)
Audio stream (PID=0x1101)
Presentation graphics stream (PID=0x1200)
Presentation graphics stream (PID=0x1201)
Interactive graphics stream (PID=0x1400)
Video stream (PID=0x1B00, Secondary video)
Video stream (PID=0x1B01, Secondary video)

FIG. 23

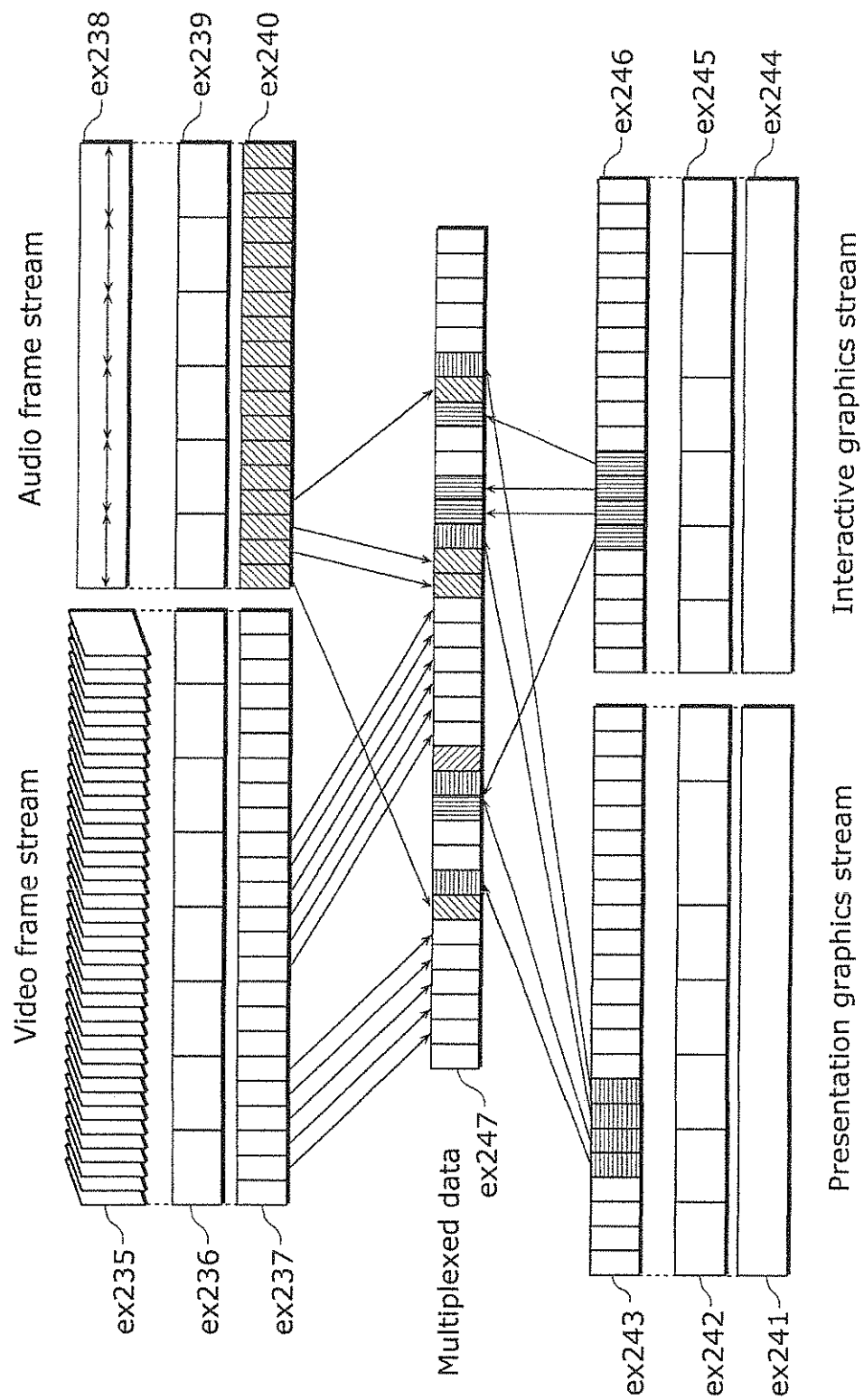


FIG. 24

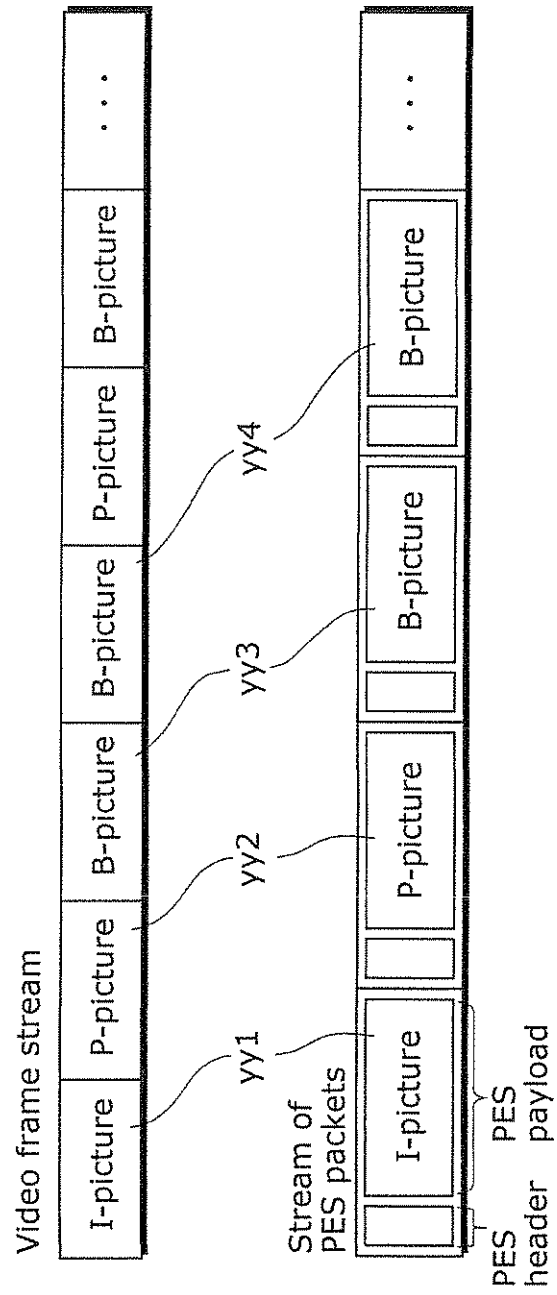


FIG. 25

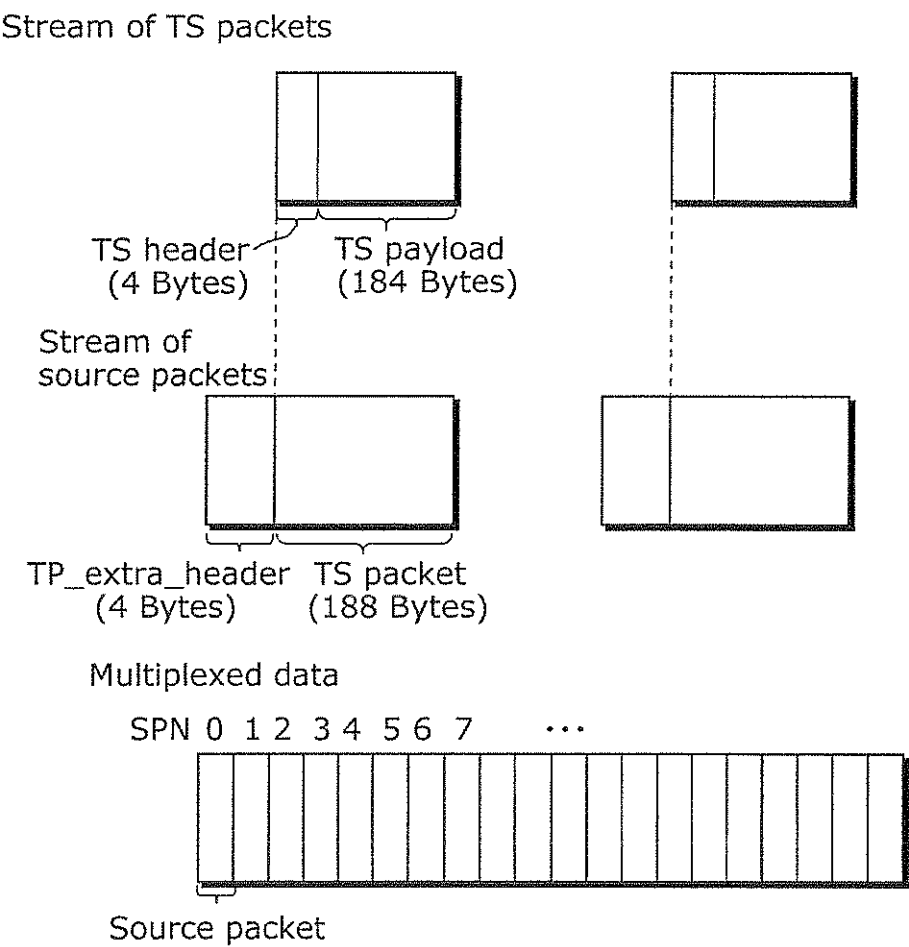


FIG. 26

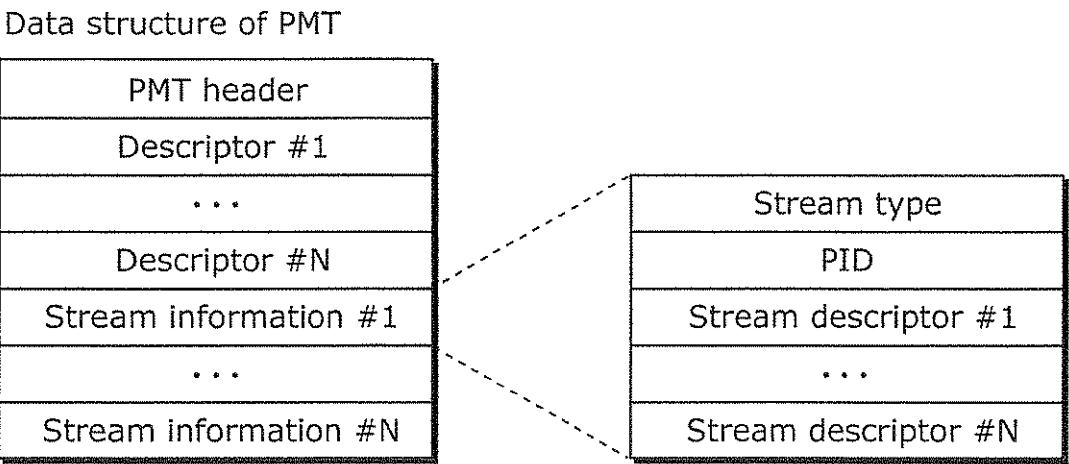


FIG. 27

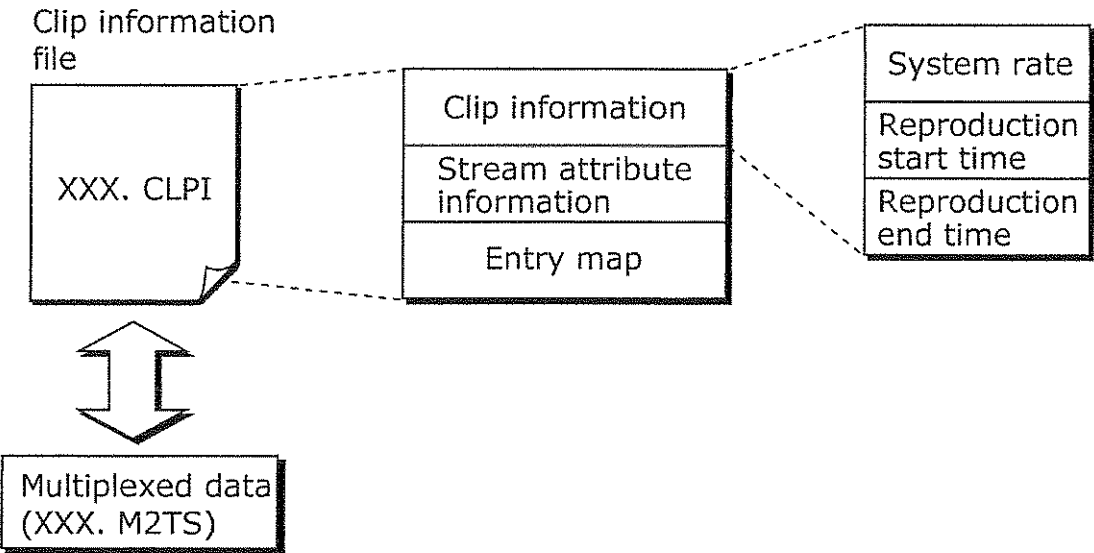


FIG. 28

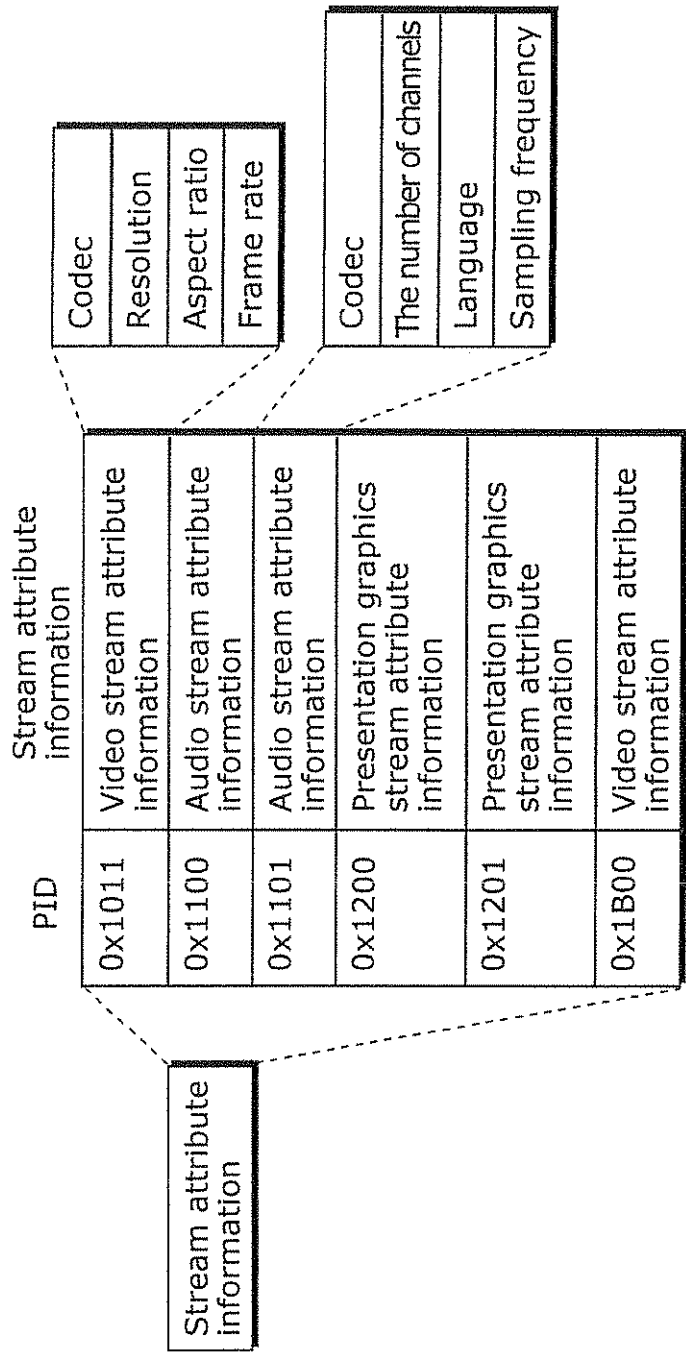


FIG. 29

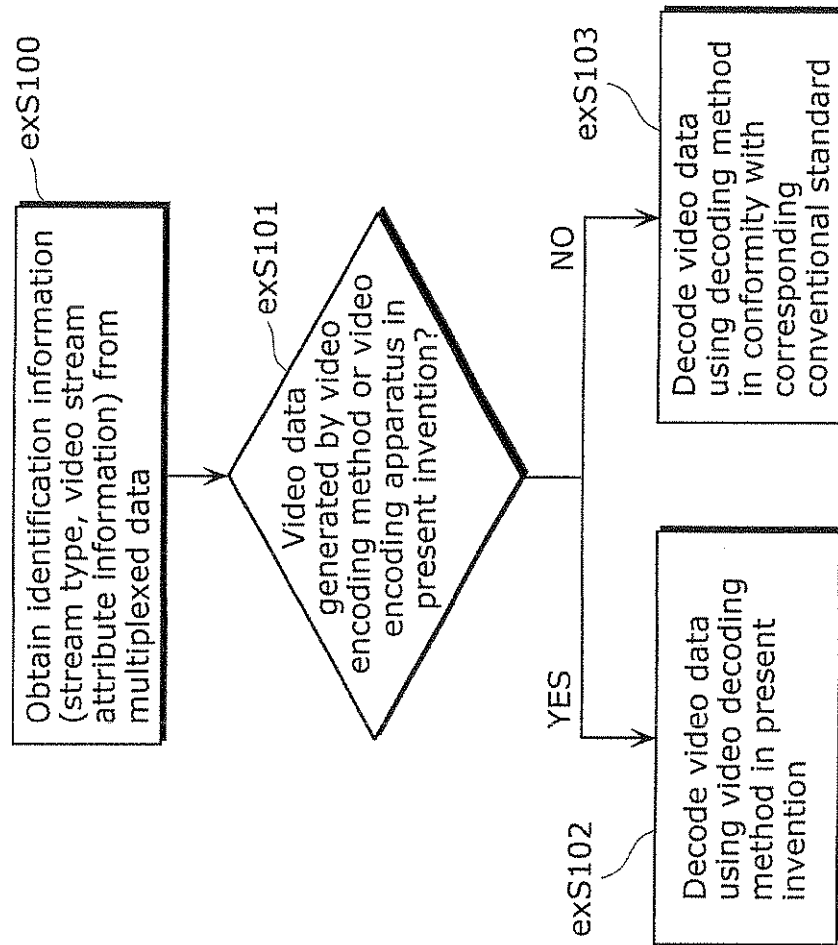


FIG. 30

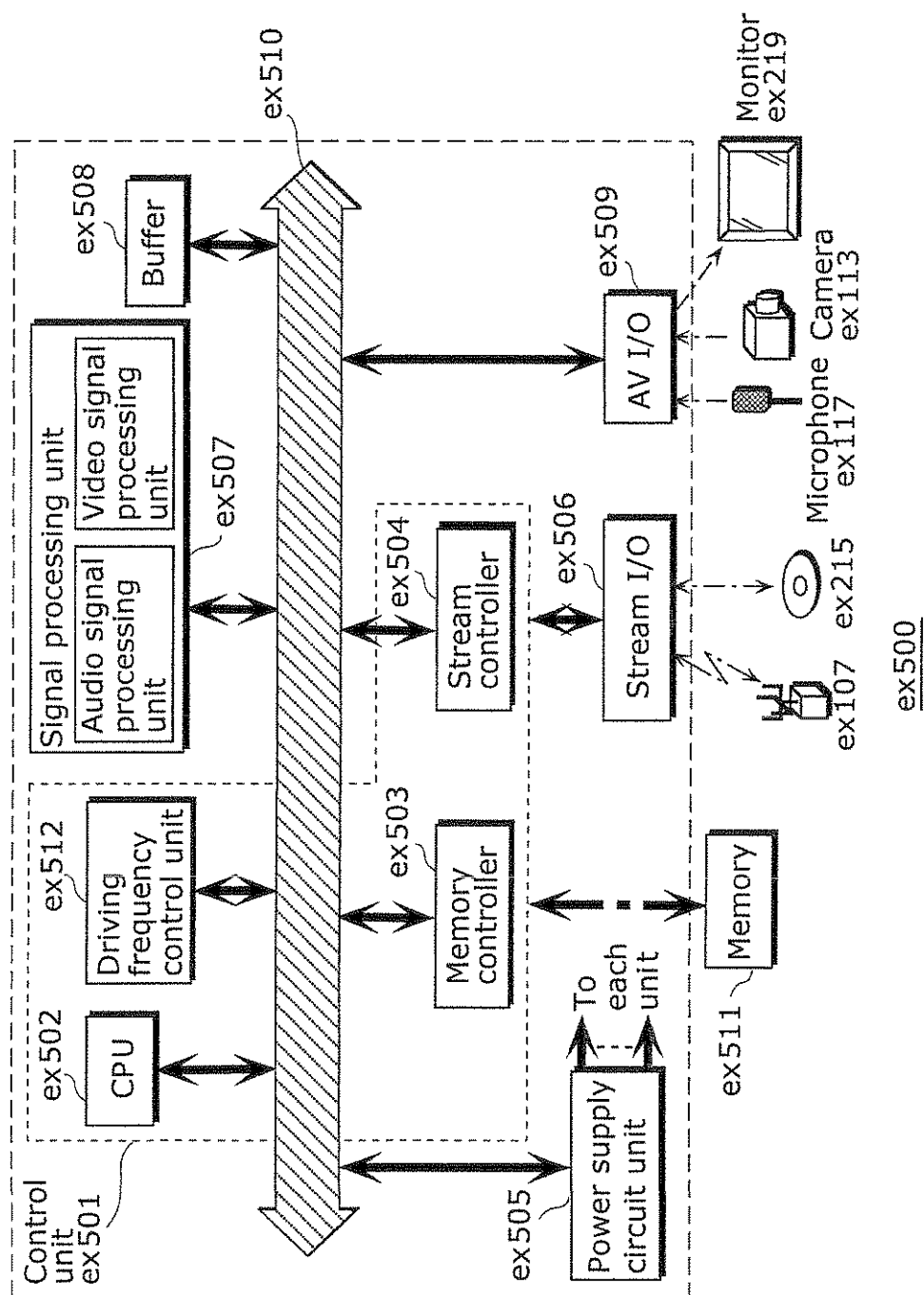


FIG. 31

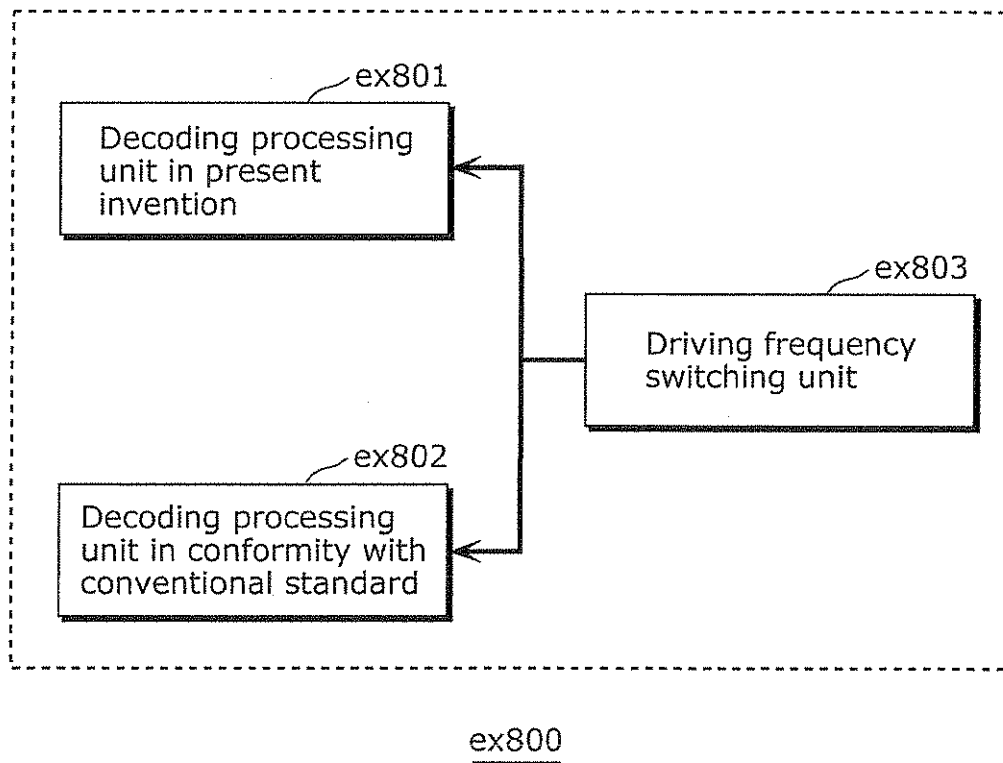


FIG. 32

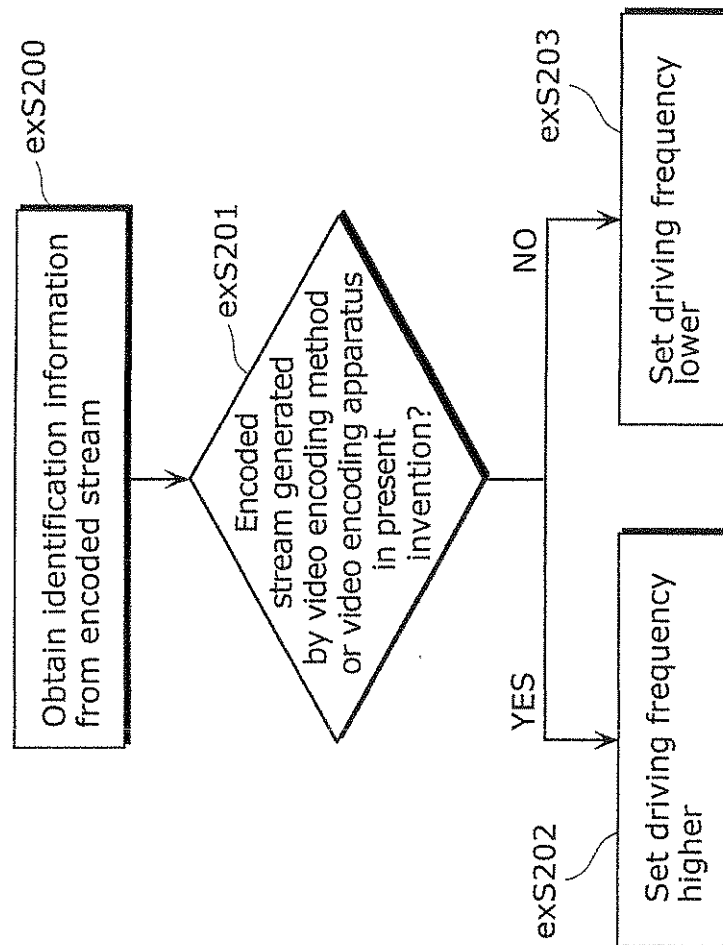
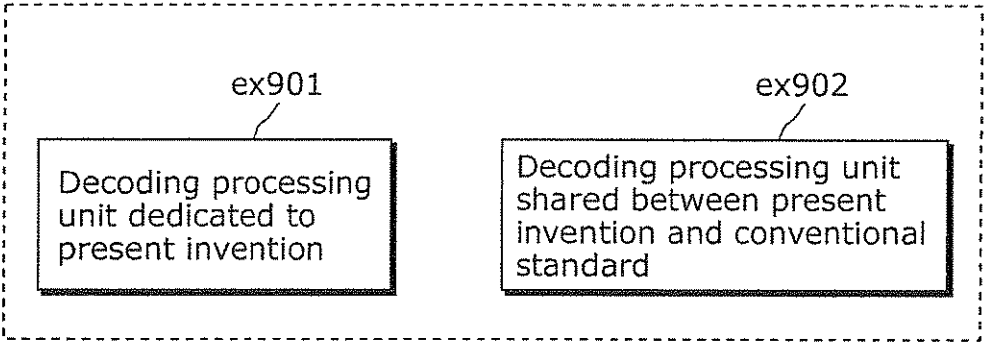


FIG. 33

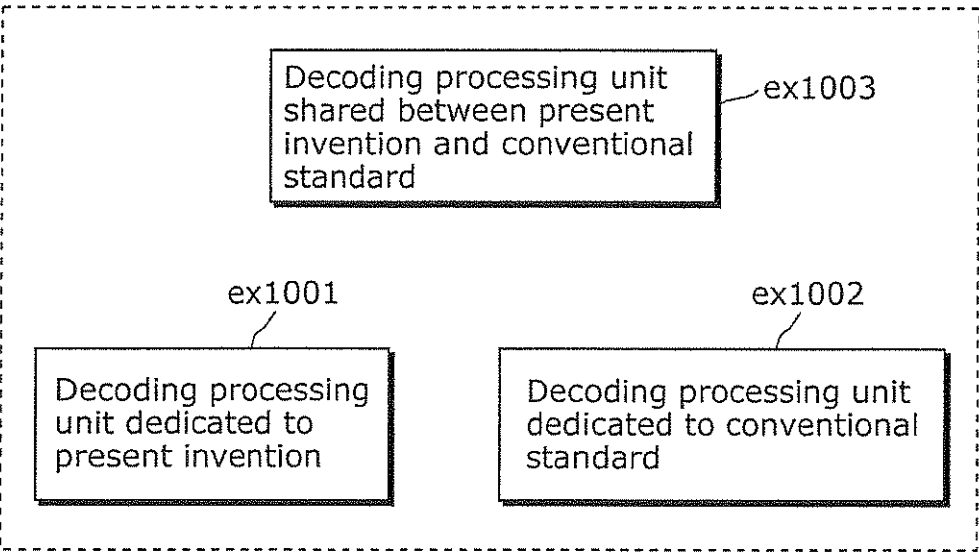
Corresponding standard	Driving frequency
MPEG-4 AVC	500 MHz
MPEG-2	350 MHz
⋮	⋮

FIG. 34A



ex900

FIG. 34B



ex1000
35/35