~~What is claimed is:~~ CLAIMS

1. A method ~~programmed in a non-transitory memory of~~ for information processing a device comprising:

~~a.~~ decoding ~~content; and~~ a bit stream based on video parameter set (VPS) syntax structure wherein a byte-alignment syntax is under a condition of a VPS extension flag in the VPS syntax structure.

~~b. accessing information related to the content, wherein the information comprises video parameter set data, further wherein the video parameter set data comprises mixed signaling information.~~

2. The method of claim 1 wherein a ~~video parameter set~~ VPS function ~~used in determining the video parameter set data~~ is under a condition of a ~~video parameter set~~ VPS extension flag in VPS syntax structure.

3. The method of claim 1 wherein VPS extension function is present after byte-alignment syntaxes in VPS syntax structure ~~used in determining the video parameter set data are under a condition of a video parameter set extension flag~~.

~~4. The method of claim 1 wherein the video parameter set data includes a raw byte sequence payload trailing bits value.~~

~~5. The method of claim 1 wherein the video parameter set data is determined without using byte alignment syntaxes.~~

~~6. The method of claim 1 wherein the video parameter set data is determined using two syntaxes for mixed sequence signaling support.~~

~~7. The method of claim 6 wherein the video parameter set data is determined using a source mixed codec flag syntax parameter.~~

~~8. The method of claim 7 wherein the video parameter set data is determined using a source mixed video present flag syntax parameter.~~