ABSTRACT OF THE DISCLOSURE

This invention has as its object to provide an image sensor chip and an image sensor using the image sensor chip capable of reducing random noise in the image sensor chip to obtain a high-quality image. An image sensor chip having a plurality of light-receiving element includes a noise reduction circuit for reducing random noise contained in an output signal from the light-receiving element in the ON state and outputting the random noise contained in the output signal from the light-receiving element without any processing in the OFF state, a first control circuit for controlling the ON/OFF state of the noise reduction circuit, a clamp circuit for clamping an output signal output through the noise reduction circuit to a reference potential, and a second control circuit for controlling an ON/OFF state of the clamp circuit. An image sensor chip and an image sensor formed by arraying a plurality of image sensor chips are also provided.

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