

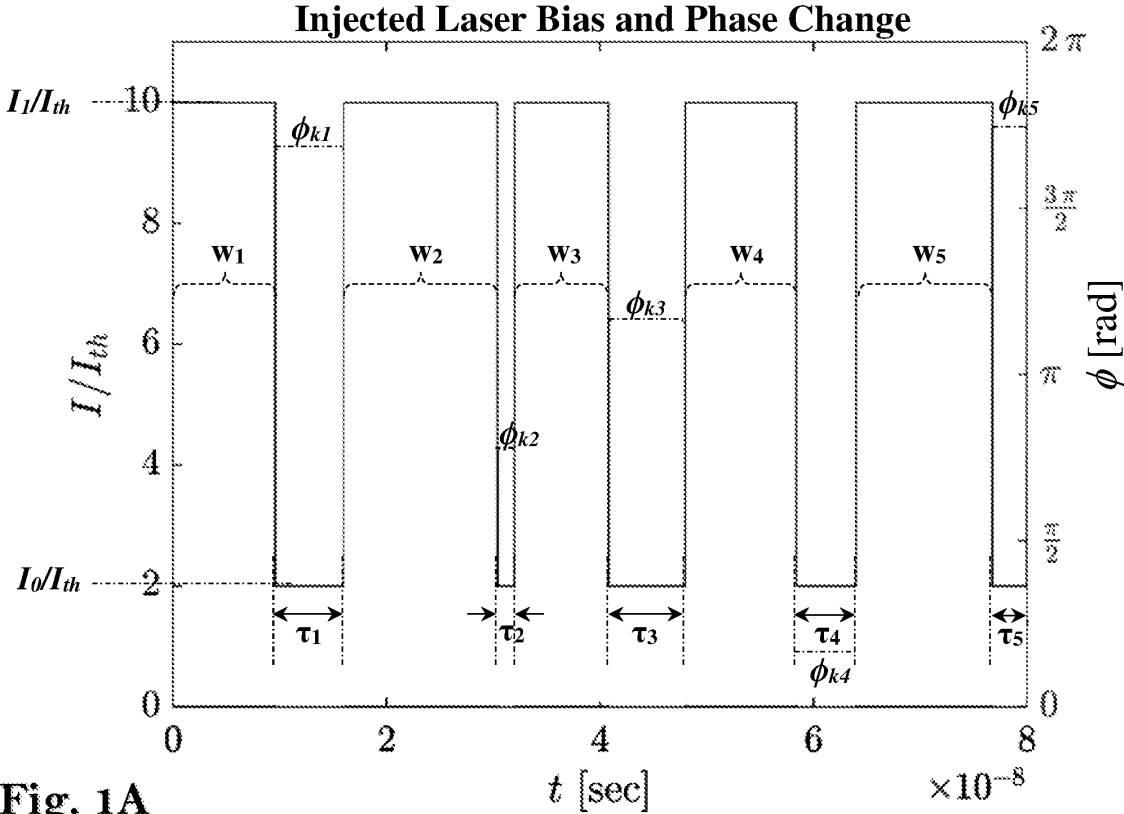


Fig. 1A

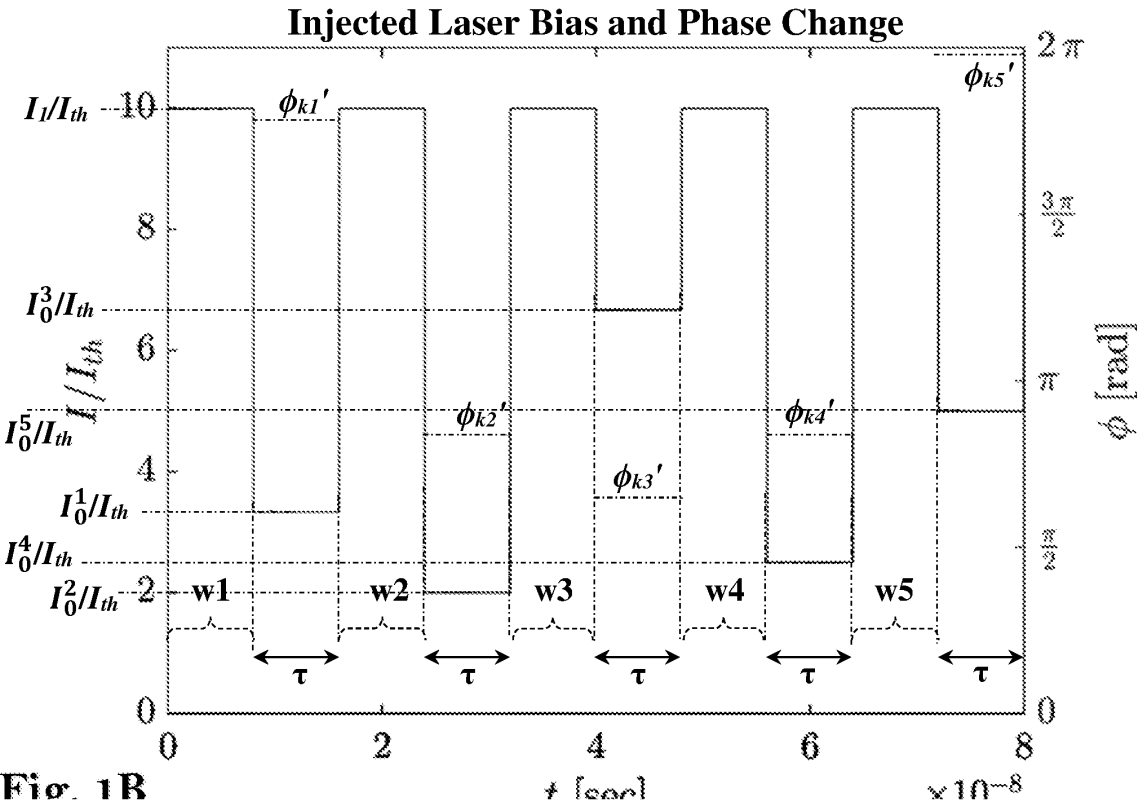
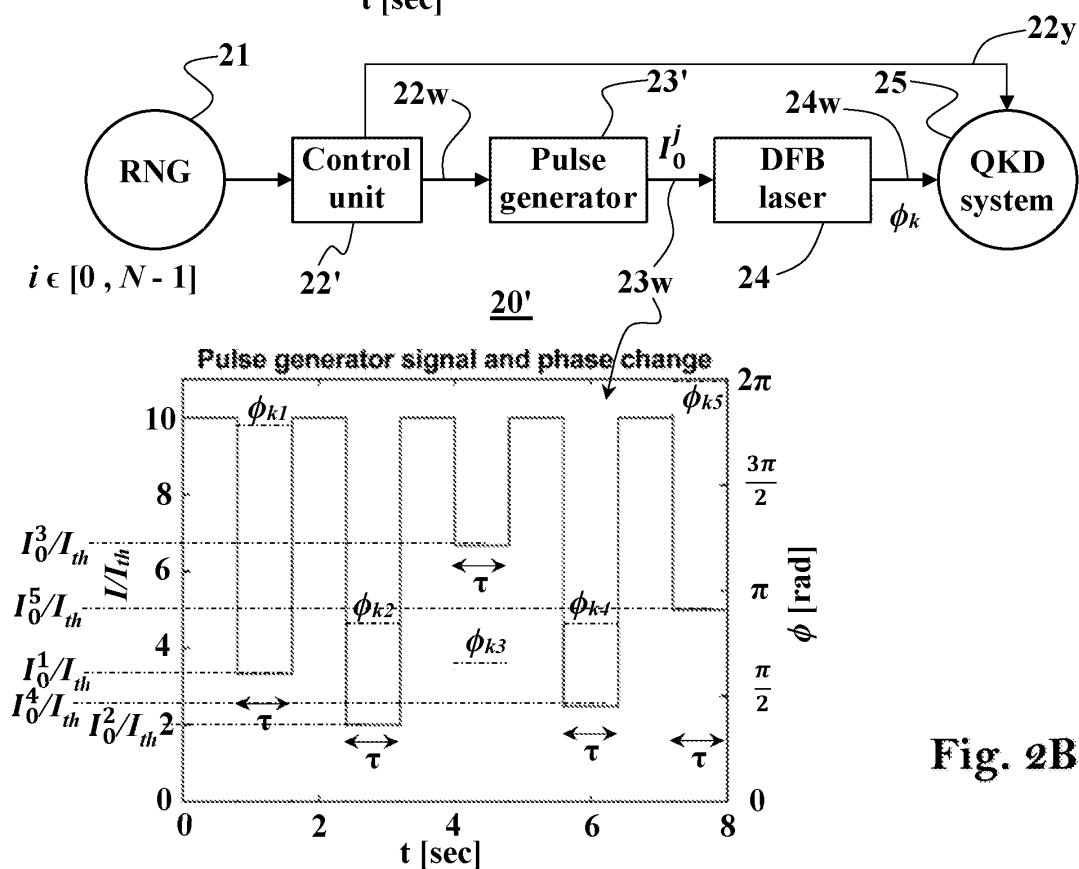
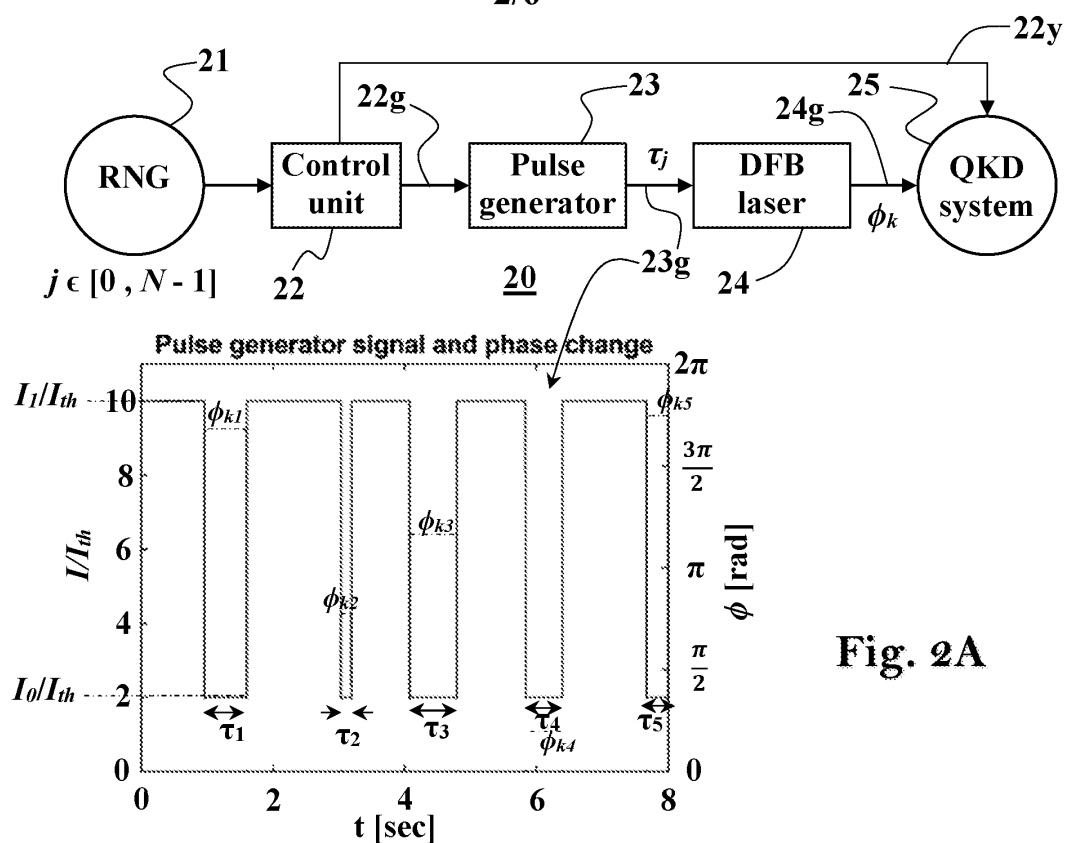


Fig. 1B

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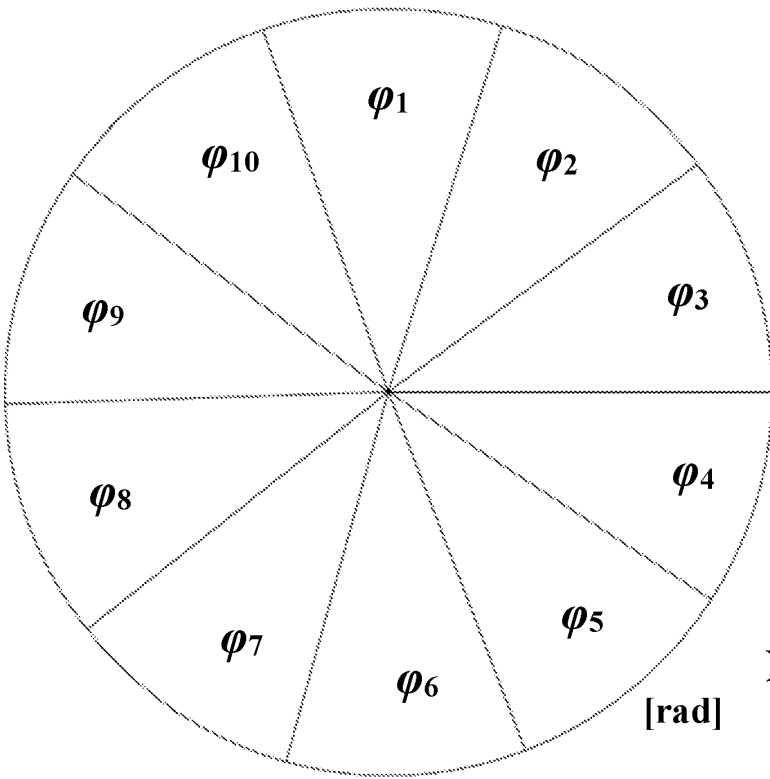


Fig. 2C

[rad]

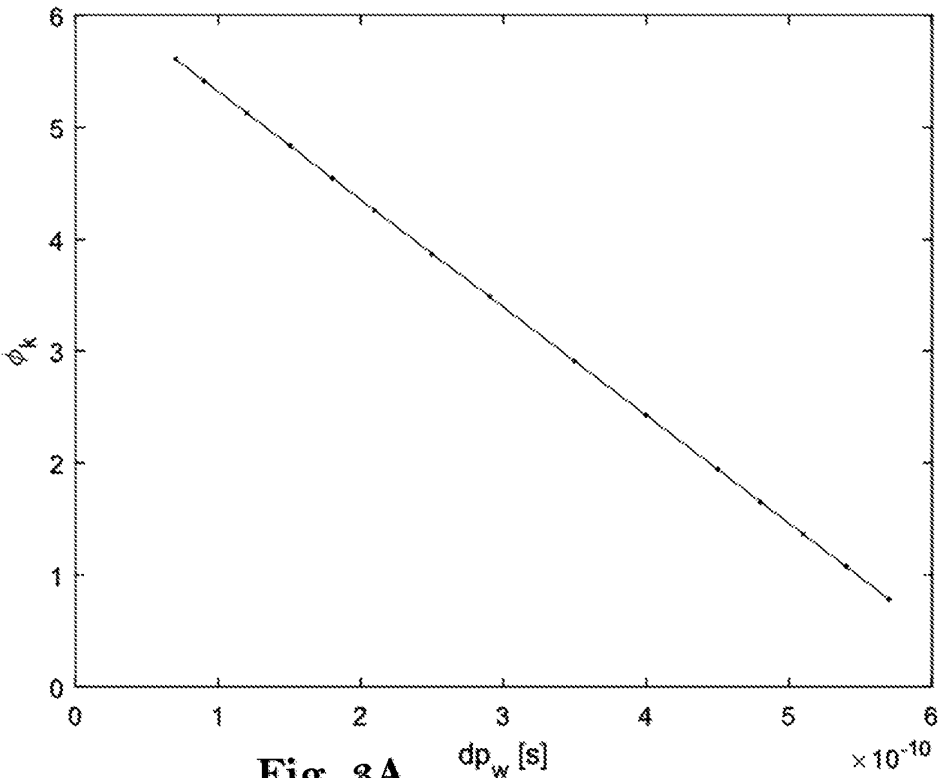


Fig. 3A

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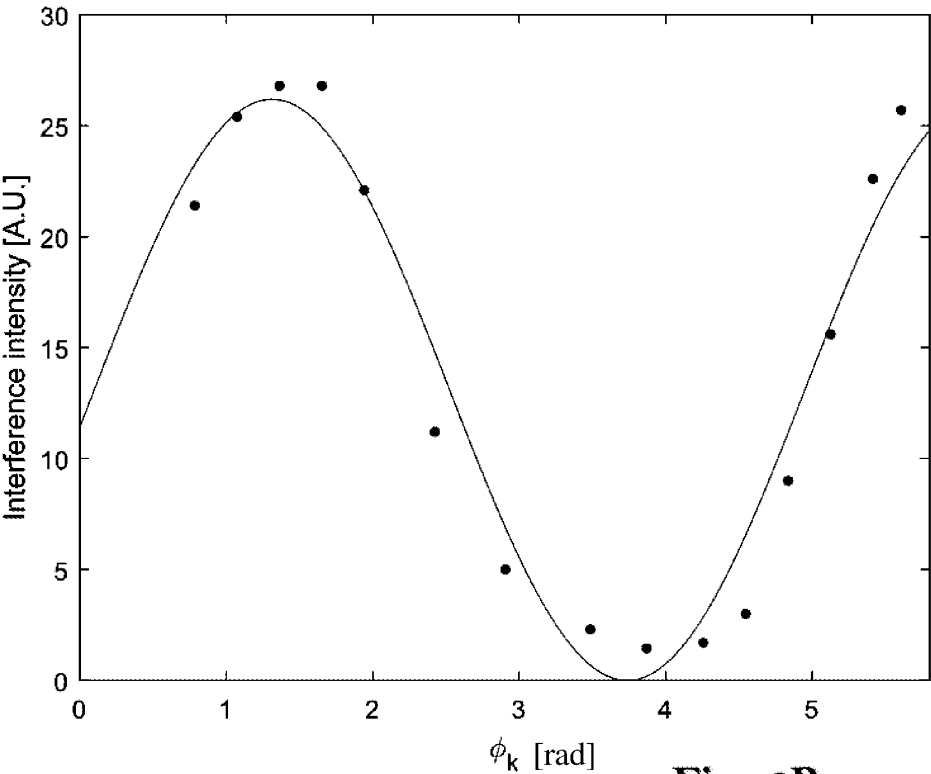


Fig. 3B

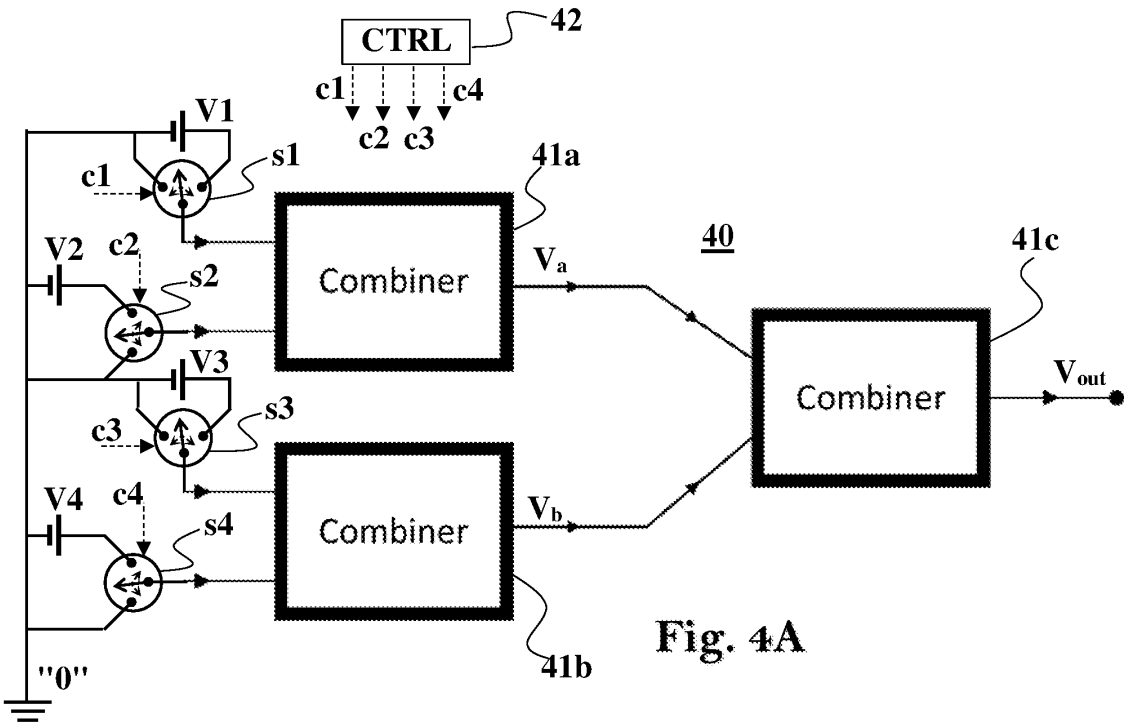
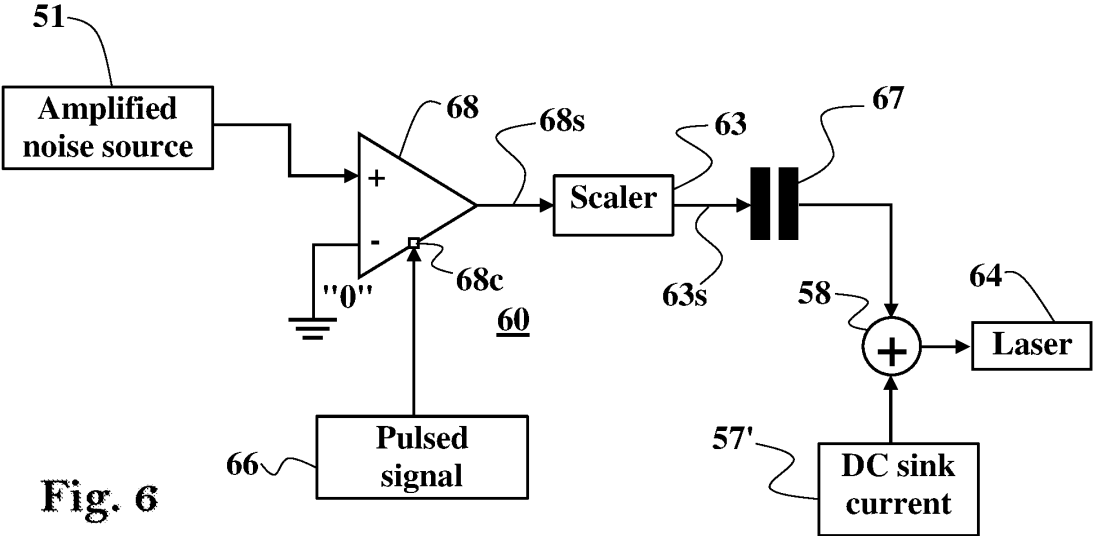
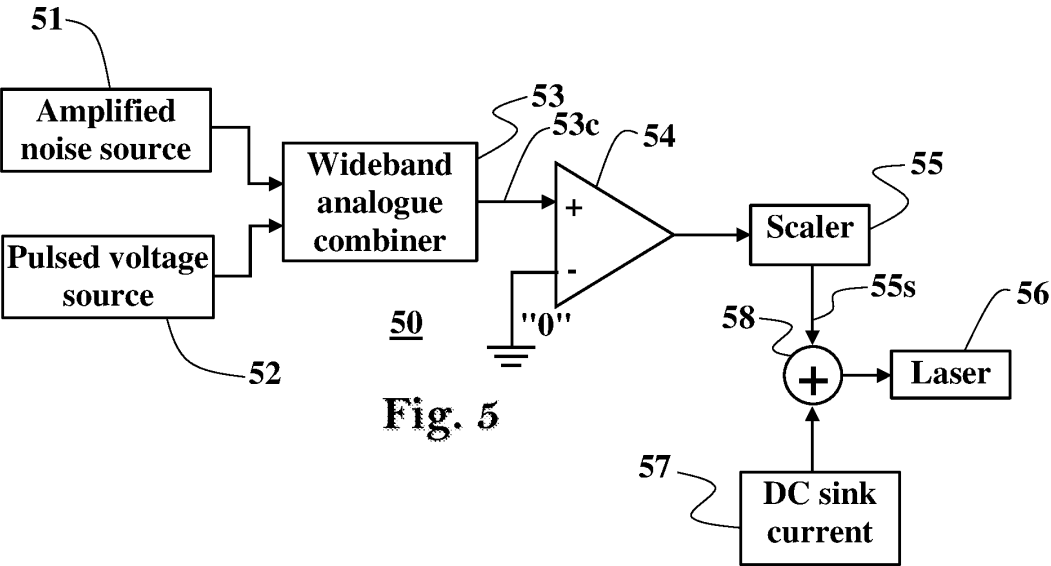
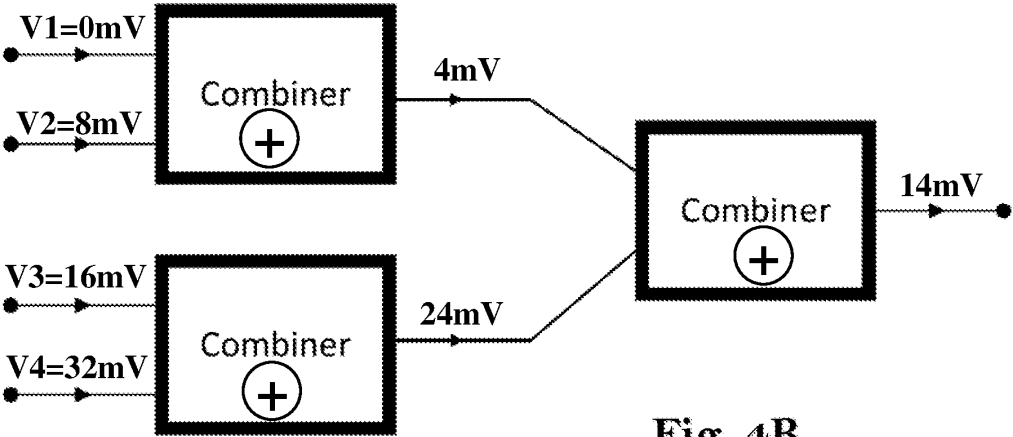


Fig. 4A

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Ideal system with 4 bit dedicated for phase randomization

