

BYTE WRITE CAPABILITY FOR MEMORY ARRAY

ABSTRACT

A method and device for selectively writing to portions of a memory word (22) which is cleared during the write operation. Information from all bytes in a word (22) are stored in a cache (24) prior to clearing the memory word, and thereafter, a portion of the stored information is written to at least one of the bytes in the word, and new information (26) is written to at least one other byte in the memory word. The writing is accomplished using a multiplexer (28) which writes either stored information or new information. The cache includes a latch for each memory cell. The memory word can be one of a plurality of memory words in the memory device (RAM), and the memory word is accessed using an addressable word line.

Figure 2

This Sheet Has Been Replaced

