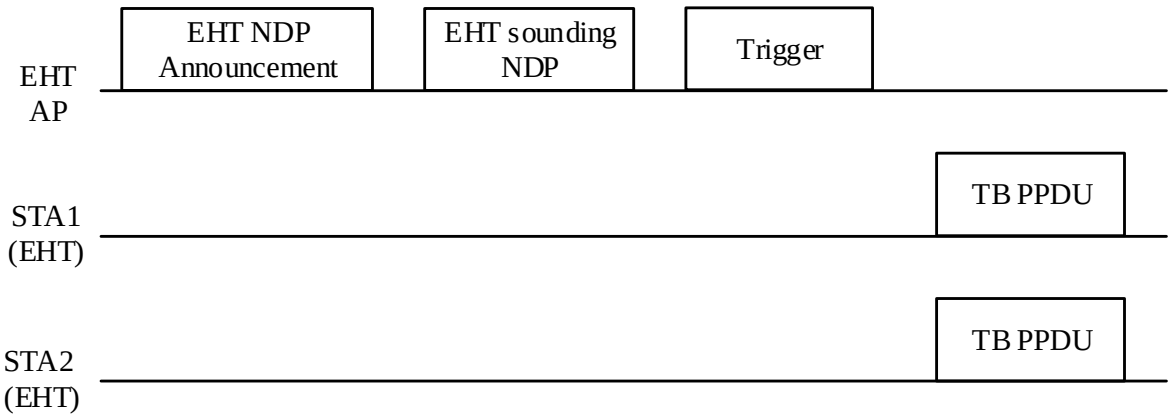



FIG. 1

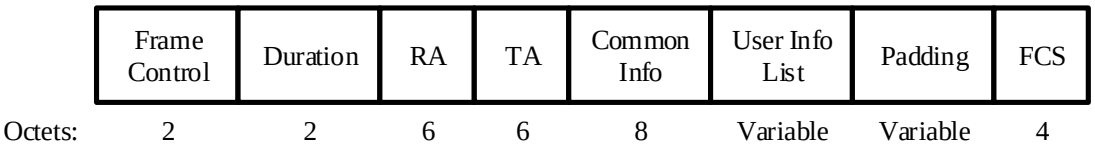



FIG. 2

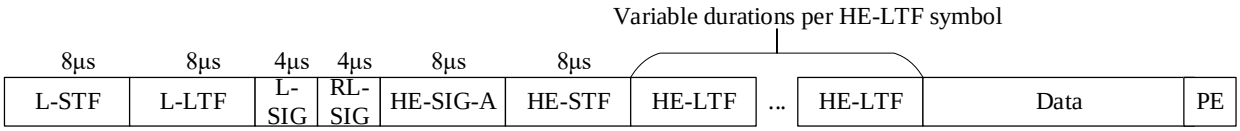



FIG. 3A

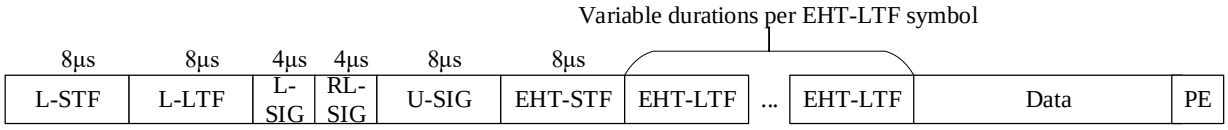



FIG. 3B

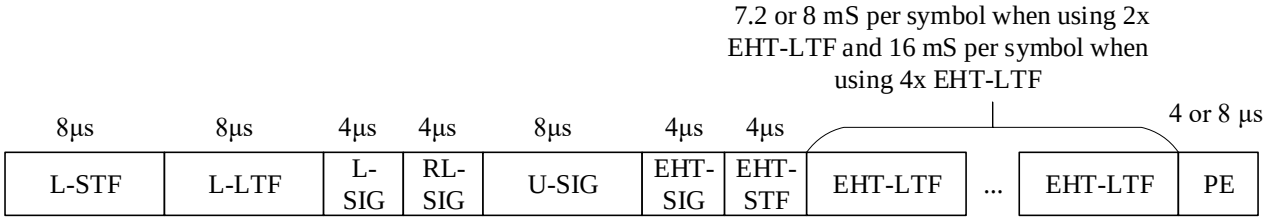



FIG. 4

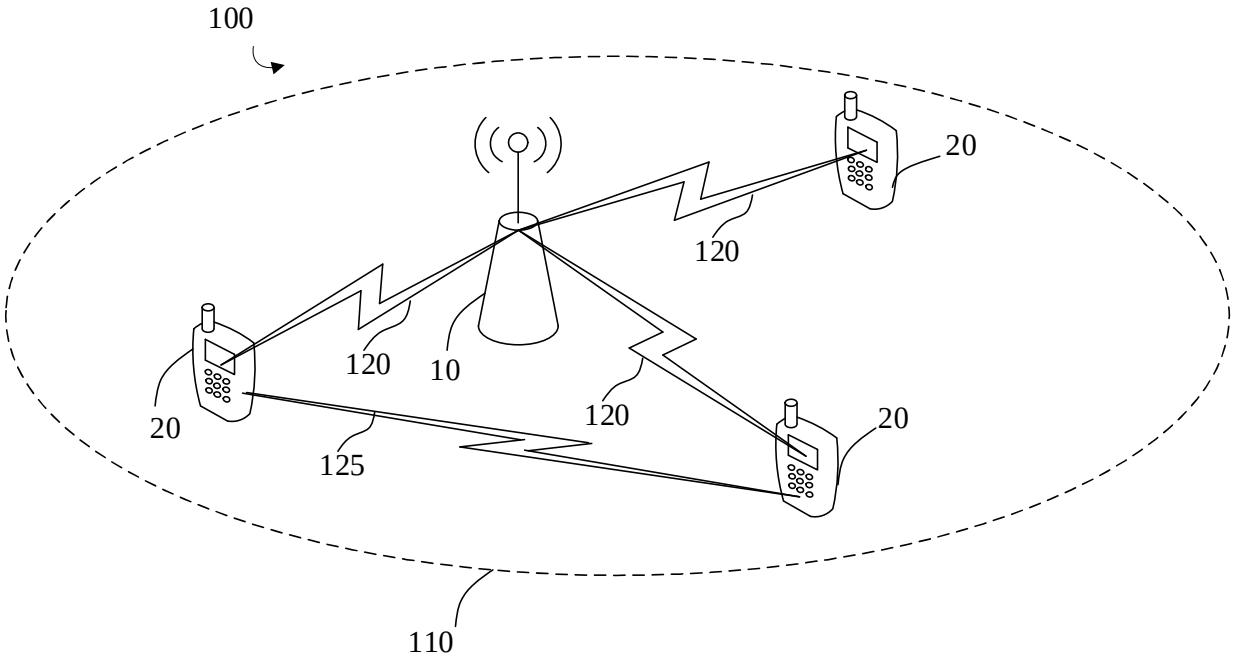

100
20
120
120
10
20
120
20
125
110

FIG. 5

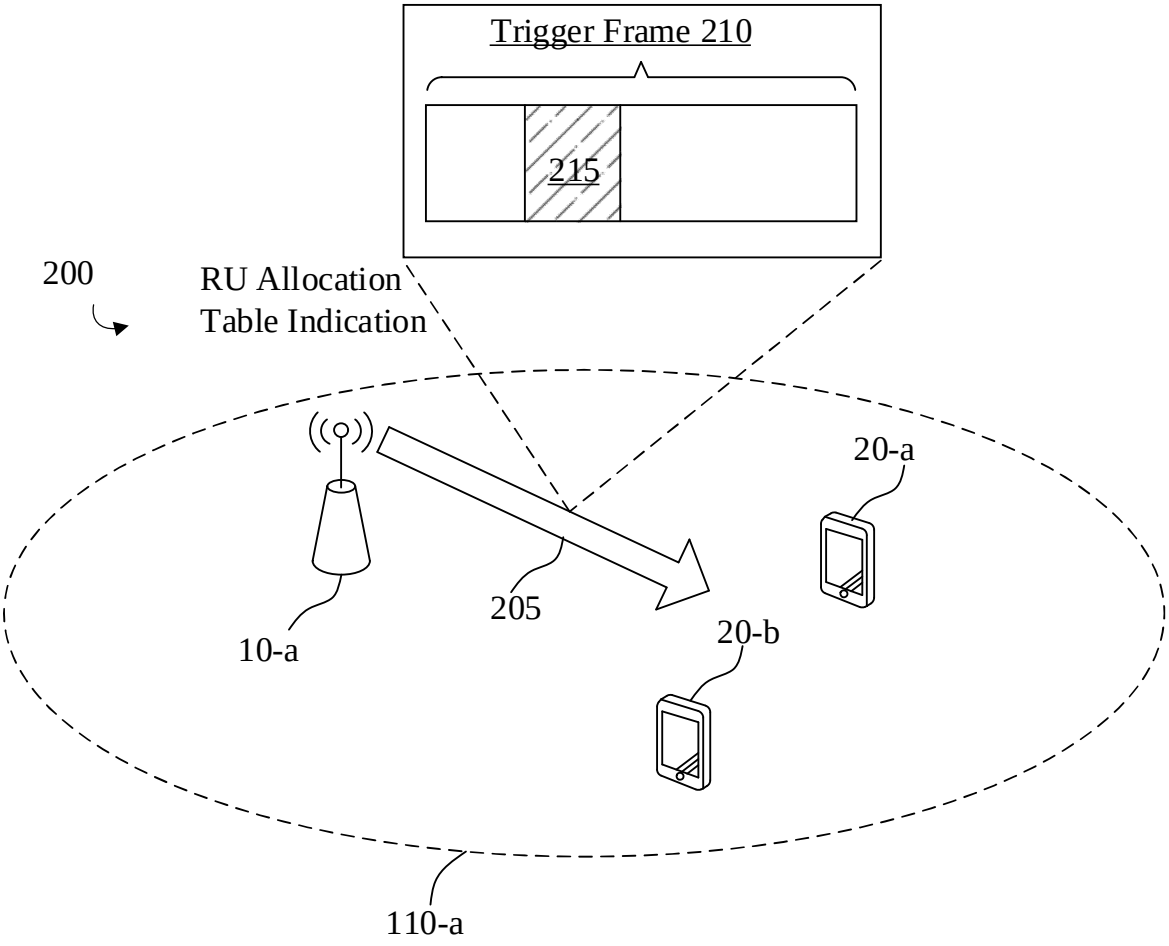

Trigger Frame 210
215
200
RU Allocation
Table Indication
20-a
205
10-a
20-b
110-a

FIG. 6

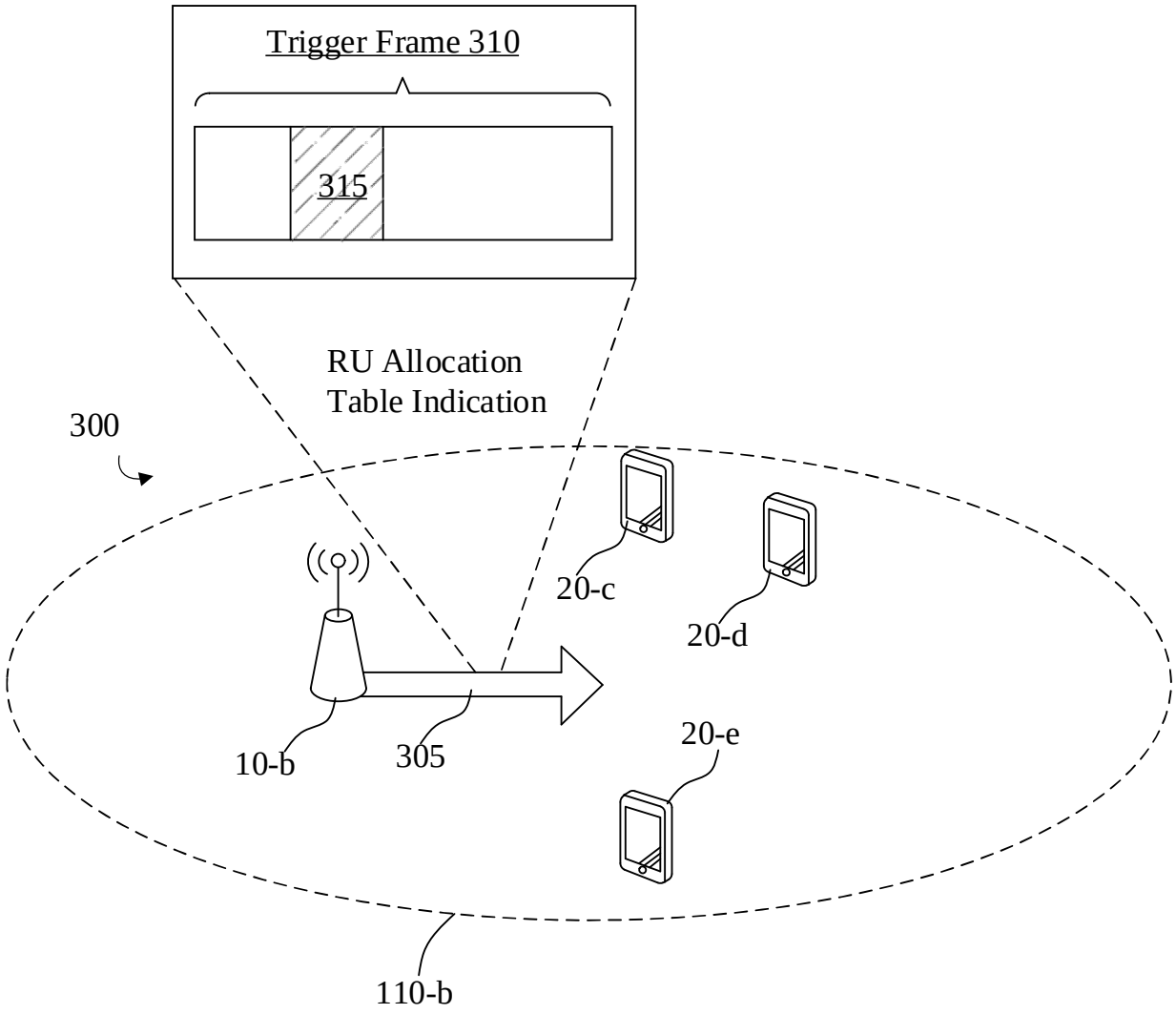

Trigger Frame 310
315
RU Allocation
Table Indication
300
20-c
20-d
20-e
10-b
305
110-b

FIG. 7

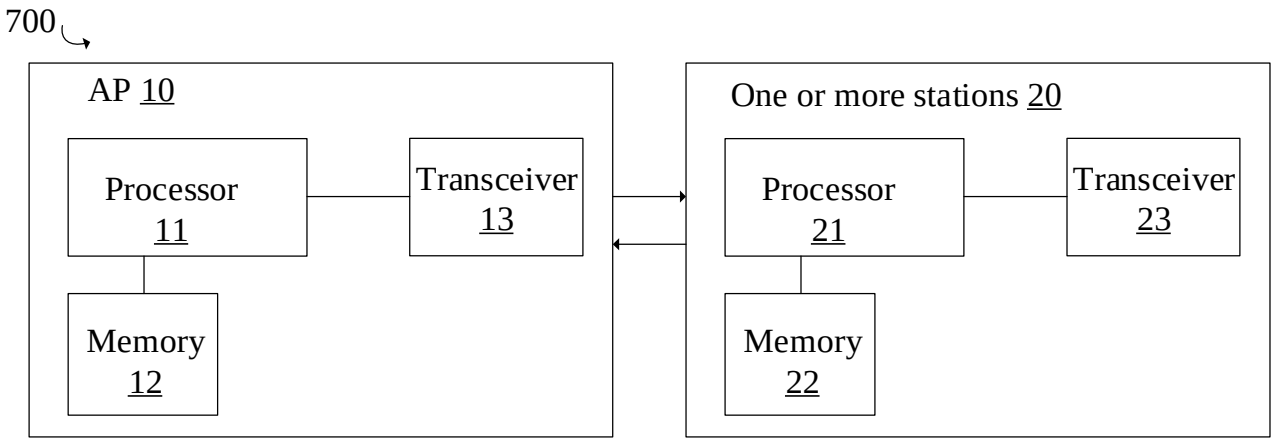

700
AP 10
Processor
11
Transceiver
13
Memory
12
One or more stations 20
Processor
21
Transceiver
23
Memory
22

FIG. 8

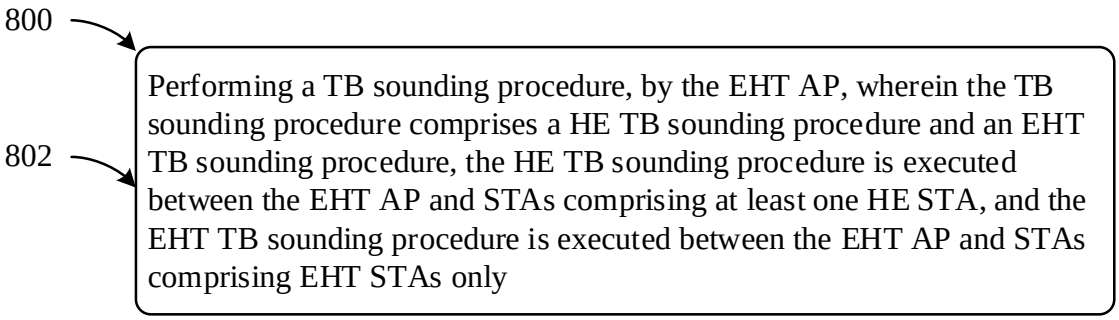



FIG. 9

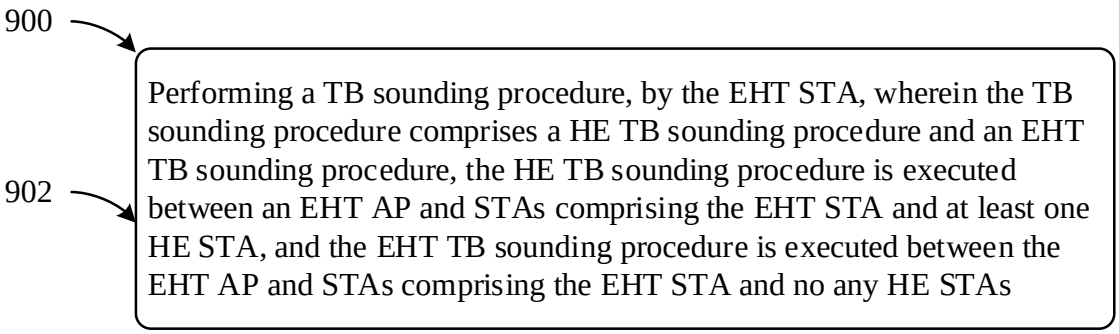



FIG. 10

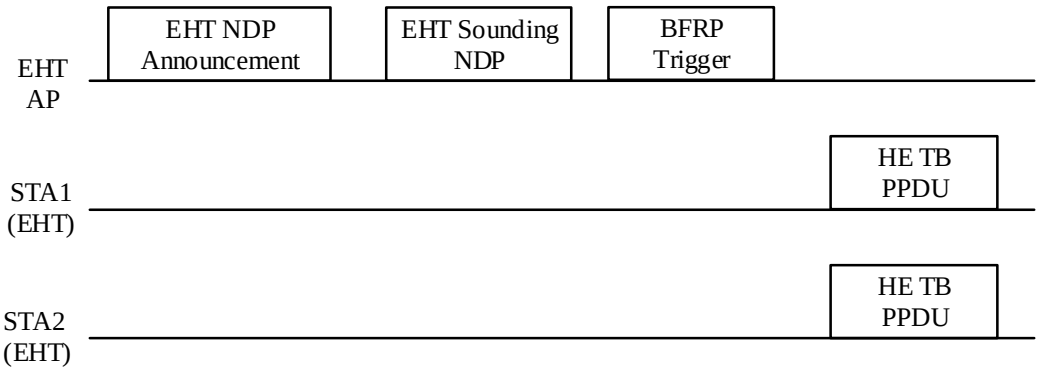



FIG. 11

| Trigger Type (1) | UL Length | More TF | CS Required | UL BW | GI And EHT-LTF Type | MU-MIMO HE-LTF Mode | Number Of HE-LTF Symbols And Mid-amble Periodicity |
|---|---|---|---|---|---|---|---|
| Bits: 4 | 12 | 1 | 1 | 2 | 2 | 1 | 3 |

| UL STBC | LDPC Extra Symbol Segment | AP TX Power | Pre-FEC Padding Factor | PE Disambiguity | UL Spatial Reuse | Doppler | UL HE-SIG-A2 Reserved | Reserved |
|---|---|---|---|---|---|---|---|---|
| Bits: 1 | 1 | 1 | 2 | 1 | 16 | 1 | 9 | 1 |

FIG. 12A

| AID12 | RU Allocation | UL FEC Coding Type | UL HE-MCS | UL DCM | SS Allocation/ RA-RU Information | UL Target RSSI | Reserved | Feedback Segment Retransmission Bitmap (all ones) |
|---|---|---|---|---|---|---|---|---|
| Bits: 12 | 8 | 1 | 4 | 1 | 6 | 7 | 1 | 8 |

FIG. 12B

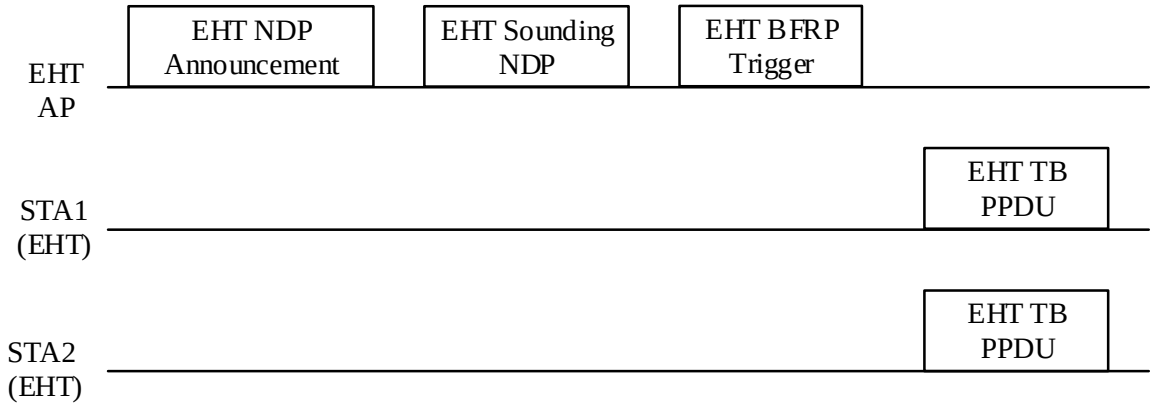



FIG. 13

| Trigger Type (8) | UL Length | More TF | CS Required | Reserved | GI And EHT-LTF Type | MU-MIMO EHT-LTF Mode (0) | Number Of EHT-LTF Symbols And Mid-amble Periodicity |
|---|---|---|---|---|---|---|---|
| Bits: 4 | 12 | 1 | 1 | 2 | 2 | 1 | 3 |

| UL STBC (0) | LDPC Extra Symbol Segment | AP TX Power | Pre-FEC Padding Factor | PE Disambiguity | Reserved | Doppler | Reserved | Reserved |
|---|---|---|---|---|---|---|---|---|
| Bits: 1 | 1 | 1 | 2 | 1 | 16 | 1 | 9 | 1 |

FIG. 14A

| AID12 | RU Allocation | UL FEC Coding Type | UL EHT-MCS | Reserved | SS Allocation/ RA-RU Information | UL Target RSSI | PS160 |
|---|---|---|---|---|---|---|---|
| Bits: 12 | 8 | 1 | 4 | 1 | 6 | 7 | 1 |

FIG. 14B

| AID12 | PHY Version | PPDU Bandwidth | Spatial Reuse 1 | Spatial Reuse 2 | U-SIG Reserved | Reserved |
|---|---|---|---|---|---|---|
| Bits: 12 | 3 | 3 | 4 | 4 | 12 | 2 |

FIG. 14C

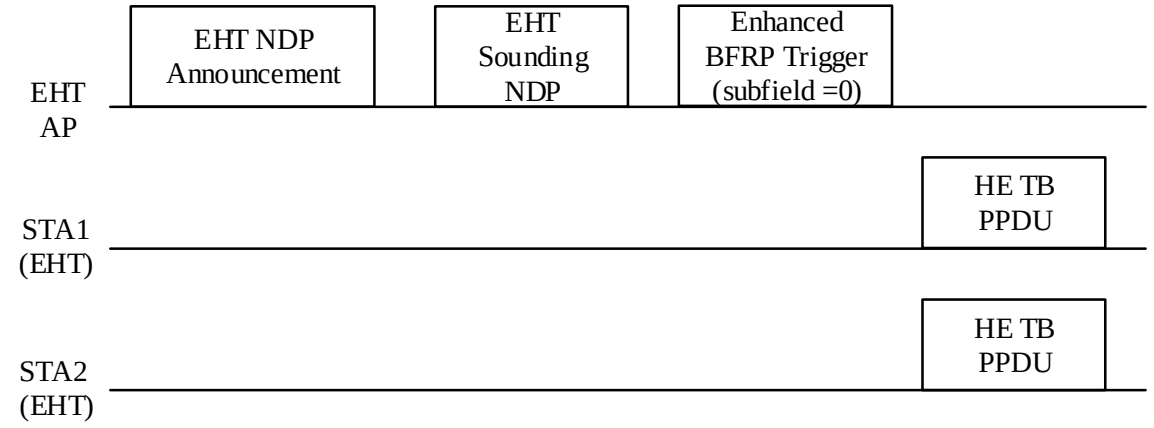



FIG. 15

| Trigger Type (1) | UL Length | More TF | CS Required | UL BW | GI And HE-LTF Type | MU-MIMO HE-LTF mode | Number Of HE-LTF Symbols And Midamble Periodically |
|---|---|---|---|---|---|---|---|
| Bits: 4 | 12 | 1 | 1 | 2 | 2 | 1 | 3 |

| UL STBC | LDPC Extra Symbol Segment | AP TX Power | Pre-FEC Padding Factor | PE Disambiguity | UL Spatial Reuse | Doppler | UL HE-SIG-A2 Reserved | Enhanced Trigger Frame Flag (0) |
|---|---|---|---|---|---|---|---|---|
| Bits: 1 | 1 | 1 | 2 | 1 | 16 | 1 | 9 | 1 |

FIG. 16

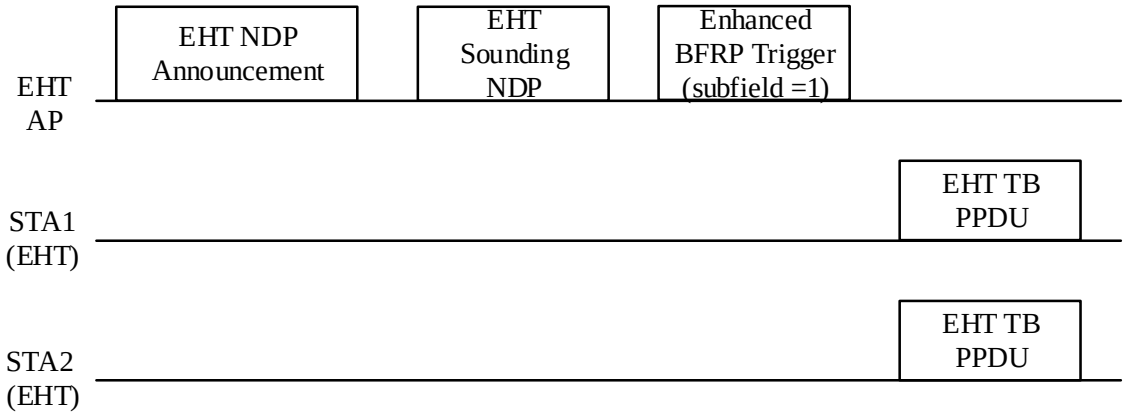



FIG. 17

| Trigger Type (1) | UL Length | More TF | CS Required | Reserved | GI And EHT-LTF Type | MU-MIMO EHT-LTF mode (0) | Number Of EHT-LTF Symbols And Midamble Periodically |
|---|---|---|---|---|---|---|---|
| Bits: 4 | 12 | 1 | 1 | 2 | 2 | 1 | 3 |

| UL STBC (0) | LDPC Extra Symbol Segment | AP TX Power | Pre-FEC Padding Factor | PE Disambiguity | Reserved | Doppler | Reserved | Enhanced Trigger Frame Flag (1) |
|---|---|---|---|---|---|---|---|---|
| Bits: 1 | 1 | 1 | 2 | 1 | 16 | 1 | 9 | 1 |

FIG. 18

| AID12 | RU Allocation | UL FEC Coding Type | UL EHT-MCS | Reserved | SS Allocation/ RA-RU Information | UL Target RSSI | PS160 |
|---|---|---|---|---|---|---|---|
| Bits: 12 | 8 | 1 | 4 | 1 | 6 | 7 | 1 |

FIG. 19A

| AID12 | RU Allocation | UL FEC Coding Type | UL EHT-MCS | Reserved | SS Allocation/ RA-RU Information | UL Target RSSI | PS160 | Reserved |
|---|---|---|---|---|---|---|---|---|
| Bits: 12 | 8 | 1 | 4 | 1 | 6 | 7 | 1 | 8 |

FIG. 19B

| AID12 | RU Allocation | UL FEC Coding Type | UL EHT-MCS | Reserved | SS Allocation/ RA-RU Information | UL Target RSSI | PS160 | Feedback Segment Retransmission Bitmap (all ones) |
|---|---|---|---|---|---|---|---|---|
| Bits: 12 | 8 | 1 | 4 | 1 | 6 | 7 | 1 | 8 |

FIG. 19C

| AID12 | PHY Version Identifier | PPDU Bandwidth | Spatial Reuse 1 | Spatial Reuse 2 | U-SIG Reserved | Reserved |
|---|---|---|---|---|---|---|
| Bits: 12 | 3 | 3 | 4 | 4 | 12 | 2 |

FIG. 20A

| AID12 | PHY Version | PPDU Bandwidth | Spatial Reuse 1 | Spatial Reuse 2 | U-SIG Reserved | Reserved | Reserved |
|---|---|---|---|---|---|---|---|
| Bits: 12 | 3 | 3 | 4 | 4 | 12 | 2 | 8 |

FIG. 20B

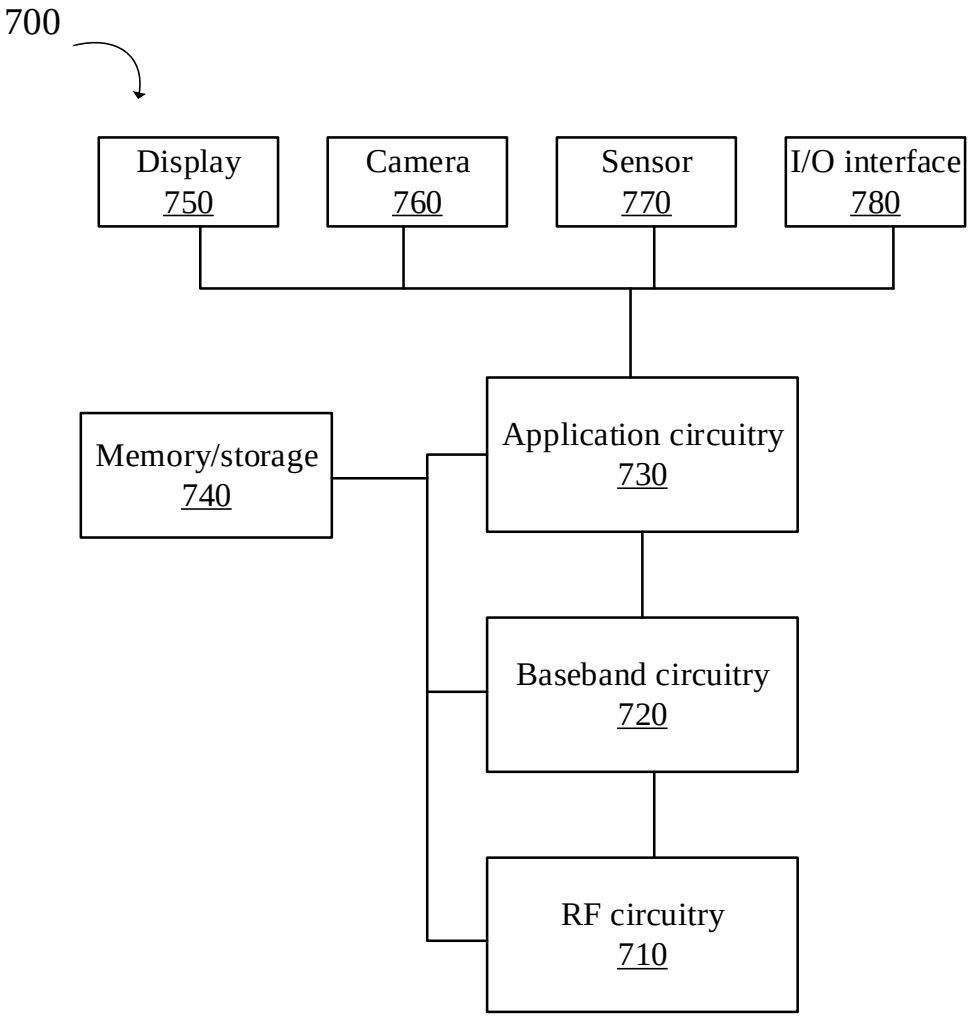



FIG. 21