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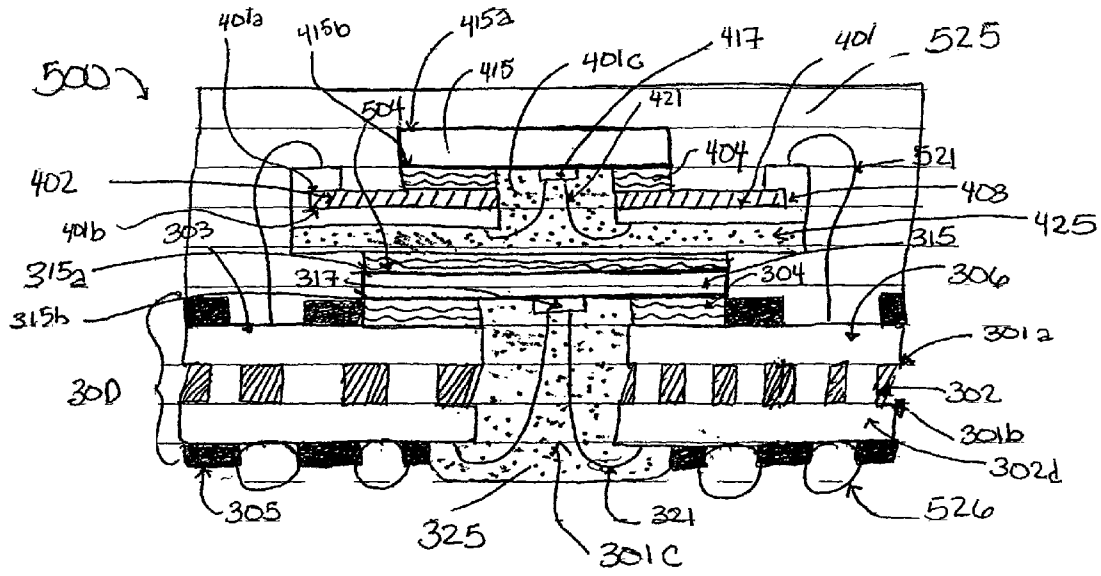
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(54) Title: MULTI-CHIP BALL GRID ARRAY PACKAGE AND METHOD OF MANUFACTURE



(57) Abstract: A BGA package is disclosed including a base IC structure having a base substrate, with an opening running lengthwise therethrough. A first semiconductor chip is mounted face-down on the base substrate so that the bond pads thereof are accessible through the opening. The package also includes a secondary IC structure including a secondary substrate, having an opening running there through, and a second semiconductor chip. The second chip is mounted face-down on the secondary substrate so that the bond pads thereof are accessible through the opening in the secondary substrate. An encapsulant fills the opening in the secondary substrate and forms a substantially planar surface over the underside of the secondary substrate. The substantially planar surface is mounted to the first chip of the base IC structure through an adhesive. Wires connect a conductive portion of the secondary IC structure to a conductive portion of the base IC structure.

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