

EQUALIZER CIRCUIT, METHOD FOR SAMPLING DATA AND MEMORY

ABSTRACT

An equalizer circuit, a method for sampling data and a memory are provided. The equalizer circuit includes a first input buffer circuit, a second input buffer circuit and a selecting and sampling circuit. The first input buffer circuit and the second input buffer circuit are respectively connected with the selecting and sampling circuit, and reference voltages used in the first input buffer circuit and the second input buffer circuit are different from each other. The selecting and sampling circuit selects to perform data sampling on a data signal outputted by the first input buffer circuit or the second input buffer circuit according to data outputted previously by the equalizer circuit, and takes sampled data as data outputted currently by the equalizer circuit. That is, the equalizer circuit includes two different input buffer circuits. When sampling data, the selecting and sampling circuit performs data acquisition on a data signal outputted by one of the two input buffer circuits according to the data outputted previously by the equalizer circuit, thereby improving the receiving quality of the data signal more effectively.

Fig. 1