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Abstract

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Method for fabricating an integrated pin diode and associated circuit arrangement

An explanation is given of, inter alia, a method for fabricating an integrated pin photodiode which contains a buried region (20) and a terminal region (32) leading to the buried region (20). This fabrication method enables the pin photodiode (14) to be integrated in a simple manner. Moreover, there is the possibility that process steps for fabricating the pin diode can also be utilized for fabricating shielding wells (22, 56).

(Figure 1)

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