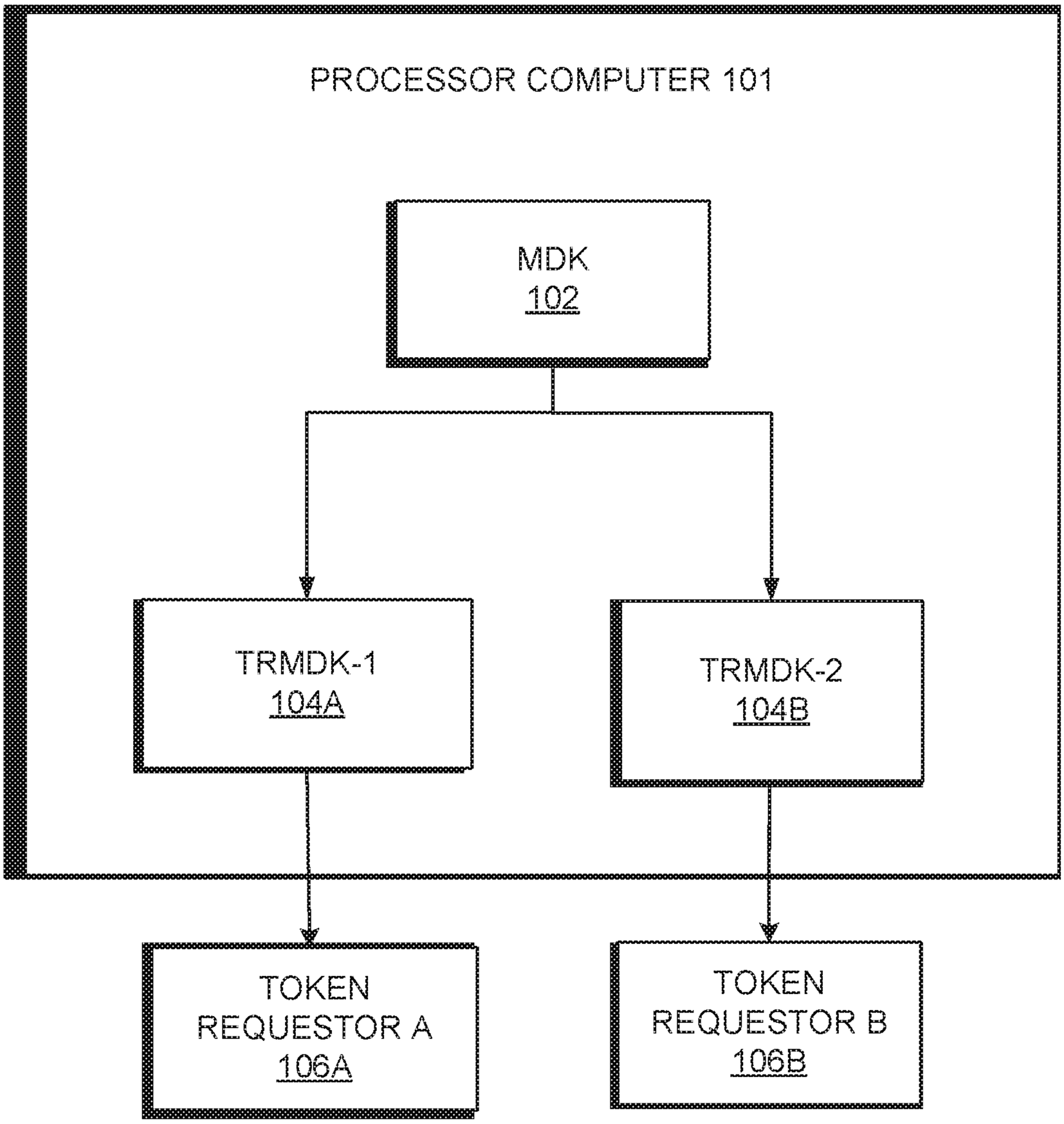
PROCESSOR COMPUTER 101
MDK
102
TRMDK-1
104A
TRMDK-2
104B
TOKEN
REQUESTOR A
106A
TOKEN
REQUESTOR B
106B

FIG. 1

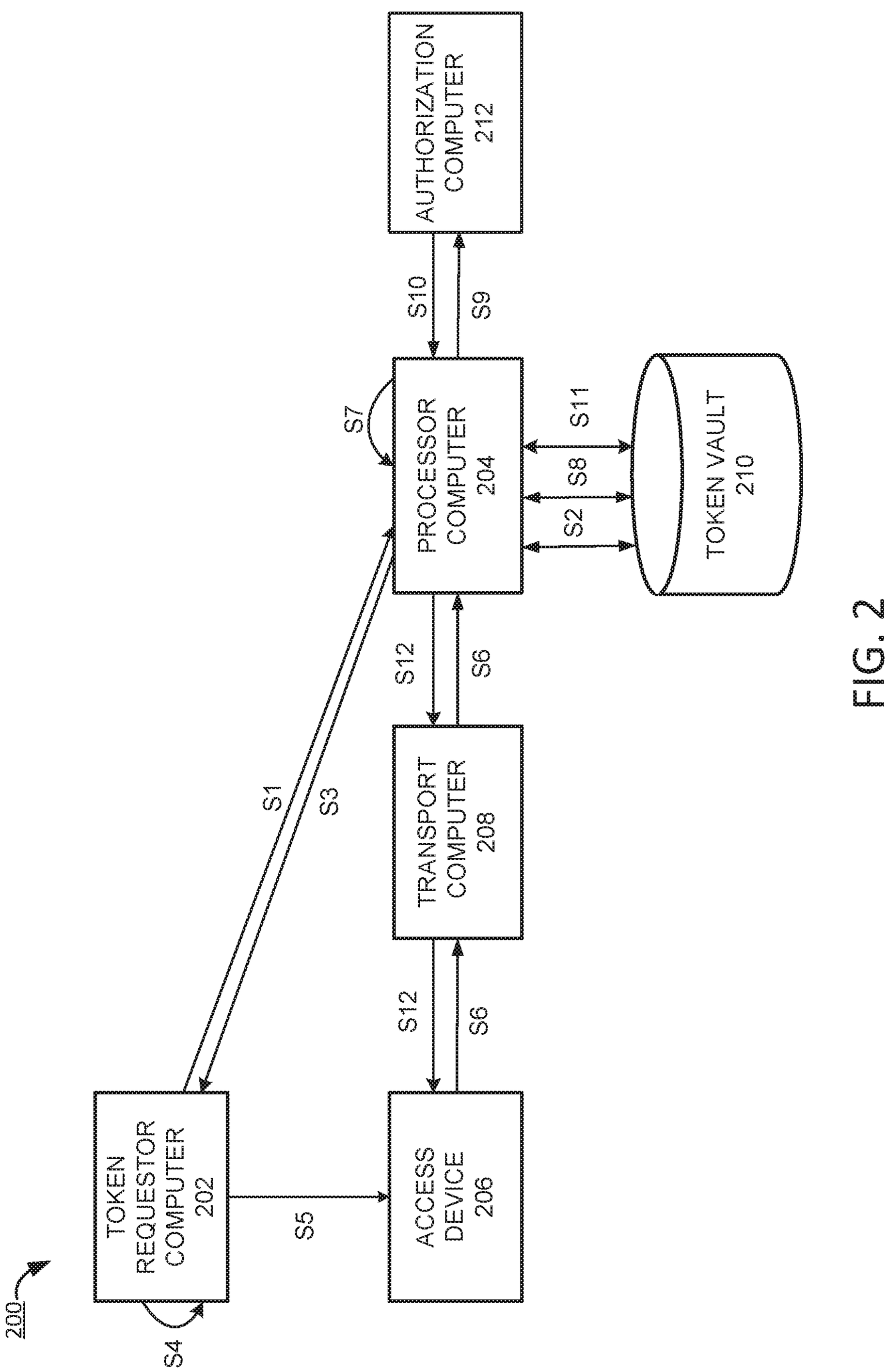
200
TOKEN REQUESTOR COMPUTER 202
TRANSPORT COMPUTER 208
PROCESSOR COMPUTER 204
AUTHORIZATION COMPUTER 212
TOKEN VAULT 210
ACCESS DEVICE 206
S1
S2
S3
S4
S5
S6
S7
S8
S9
S10
S11
S12

FIG. 2

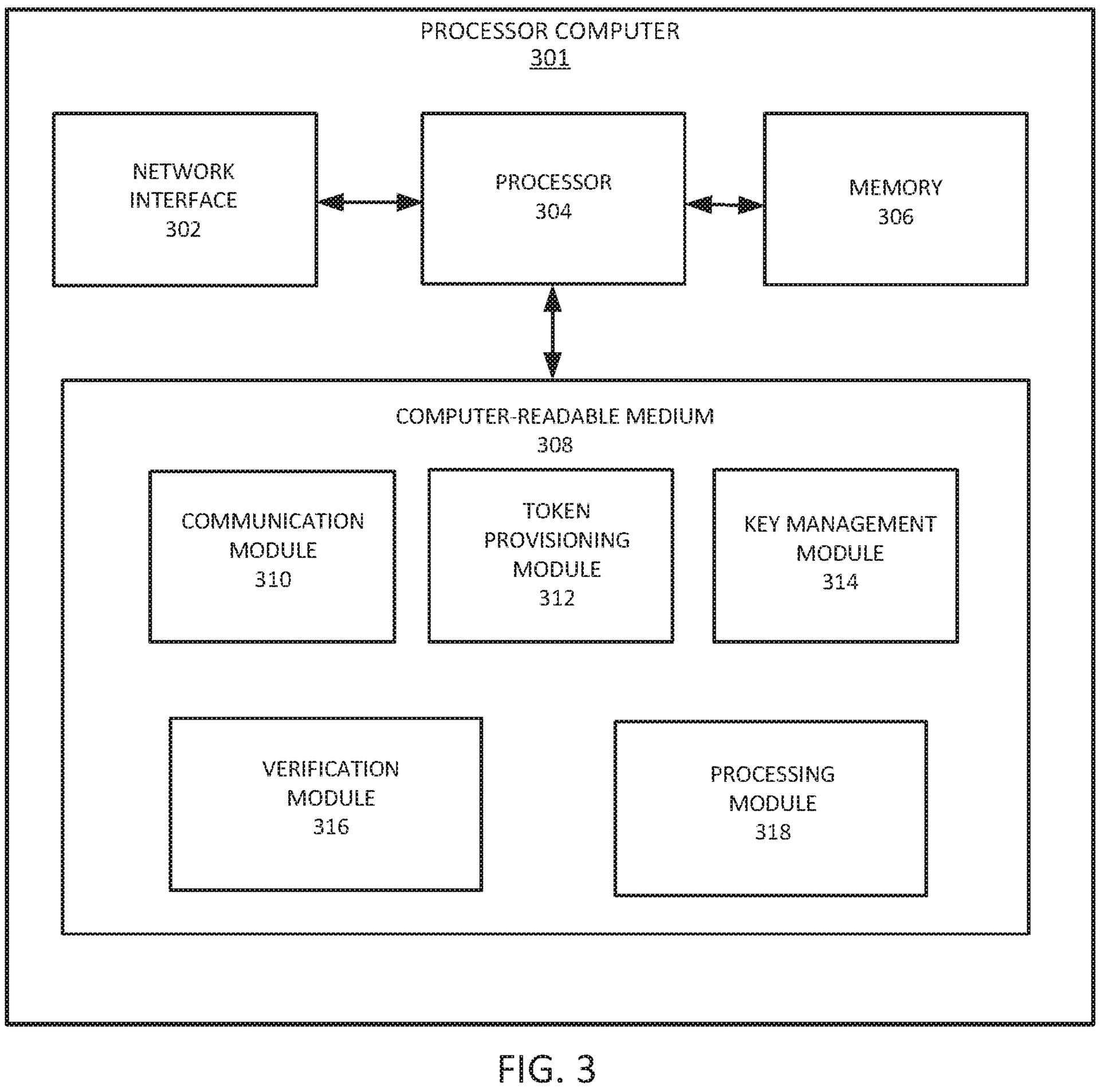
PROCESSOR COMPUTER
301
NETWORK INTERFACE 302
PROCESSOR 304
MEMORY 306
COMPUTER-READABLE MEDIUM
308
COMMUNICATION MODULE 310
TOKEN PROVISIONING MODULE 312
KEY MANAGEMENT MODULE 314
VERIFICATION MODULE 316
PROCESSING MODULE 318

FIG. 3

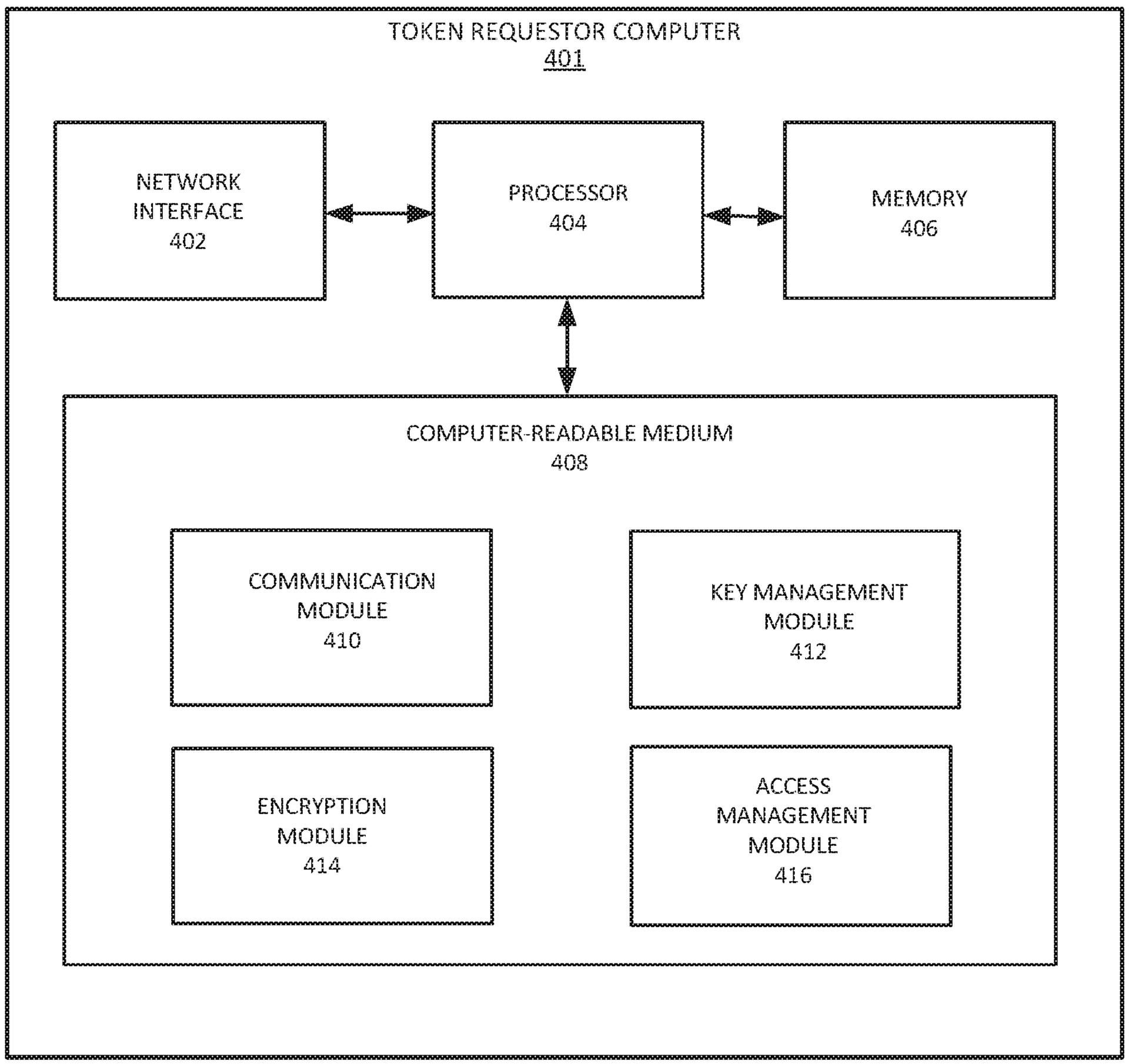
TOKEN REQUESTOR COMPUTER
401
NETWORK INTERFACE 402
PROCESSOR 404
MEMORY 406
COMPUTER-READABLE MEDIUM
408
COMMUNICATION MODULE 410
KEY MANAGEMENT MODULE 412
ENCRYPTION MODULE 414
ACCESS MANAGEMENT MODULE 416

FIG. 4

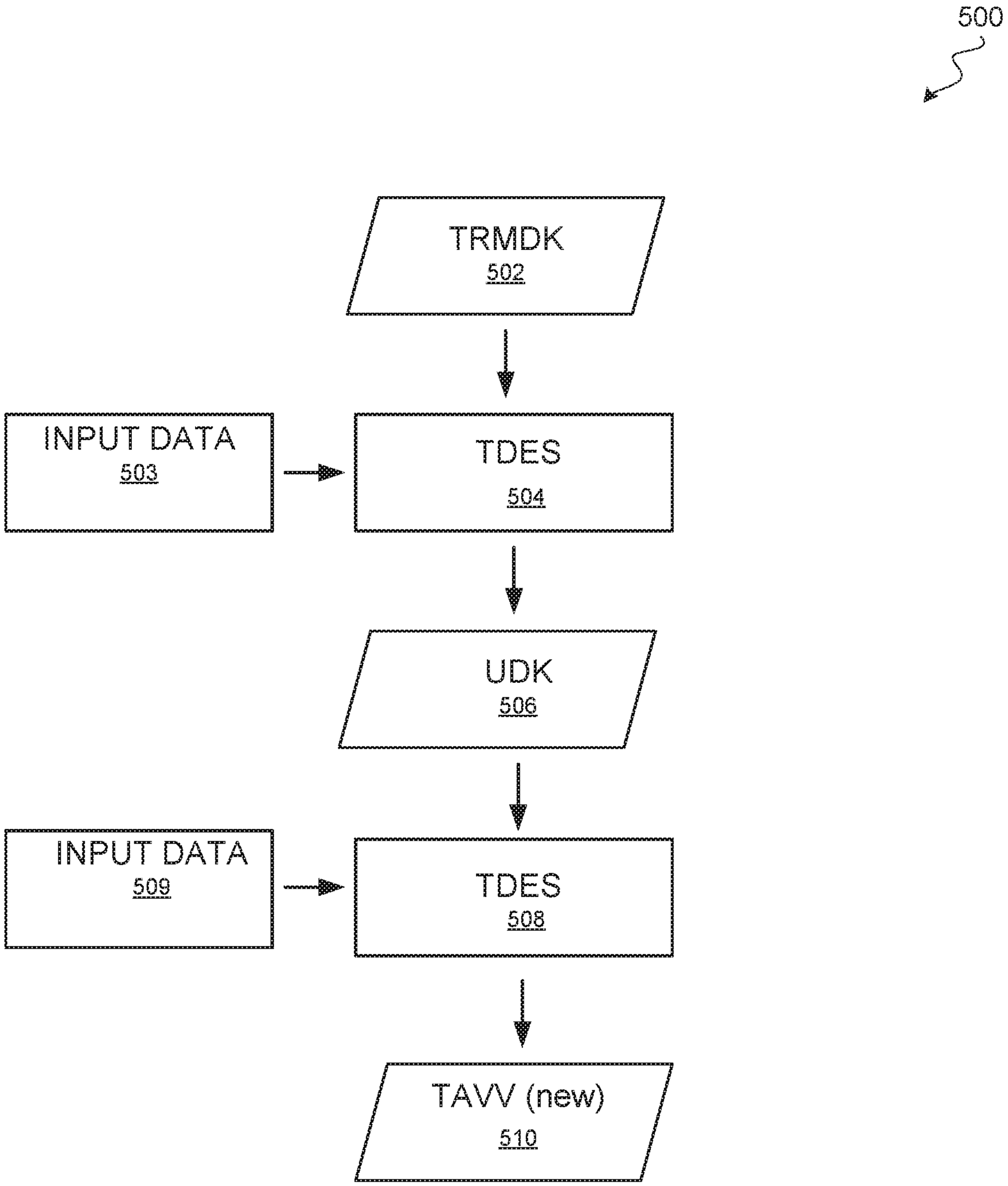
500
TRMDK
502
INPUT DATA
503
TDES
504
UDK
506
INPUT DATA
509
TDES
508
TAVV (new)
510

FIG. 5

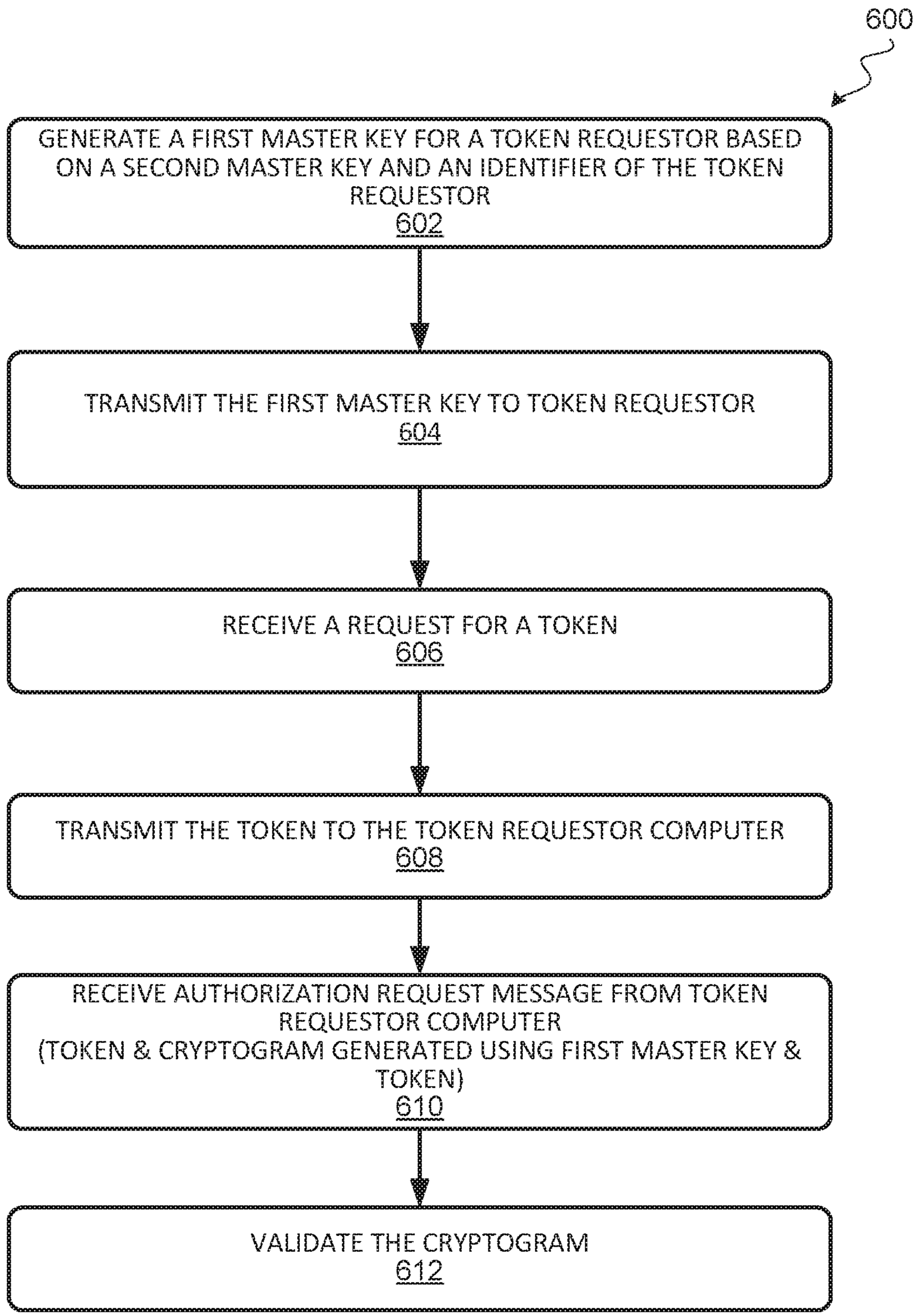
600
GENERATE A FIRST MASTER KEY FOR A TOKEN REQUESTOR BASED ON A SECOND MASTER KEY AND AN IDENTIFIER OF THE TOKEN REQUESTOR
602
TRANSMIT THE FIRST MASTER KEY TO TOKEN REQUESTOR
604
RECEIVE A REQUEST FOR A TOKEN
606
TRANSMIT THE TOKEN TO THE TOKEN REQUESTOR COMPUTER
608
RECEIVE AUTHORIZATION REQUEST MESSAGE FROM TOKEN REQUESTOR COMPUTER
(TOKEN & CRYPTOGRAM GENERATED USING FIRST MASTER KEY & TOKEN)
610
VALIDATE THE CRYPTOGRAM
612

FIG. 6