

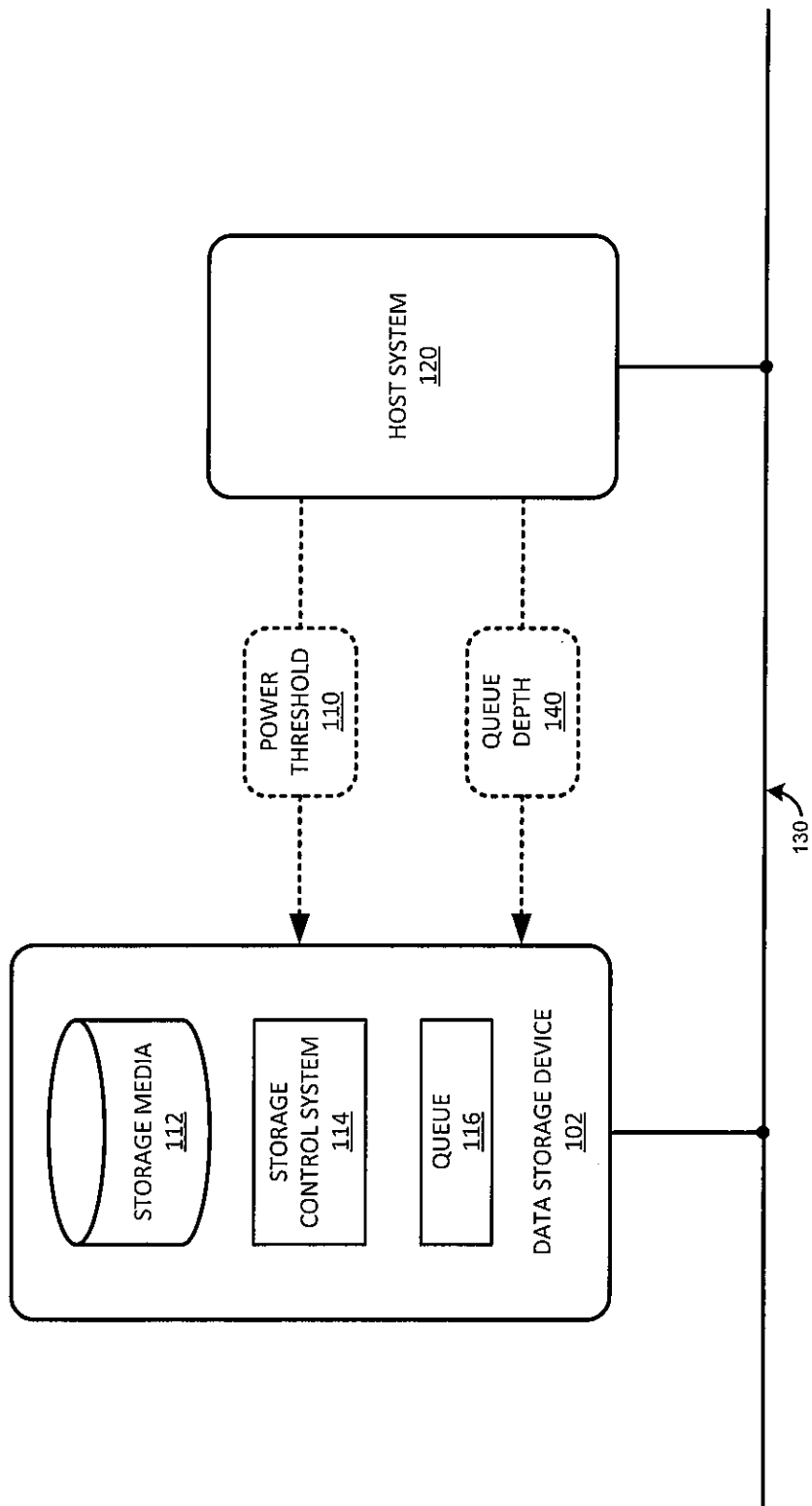
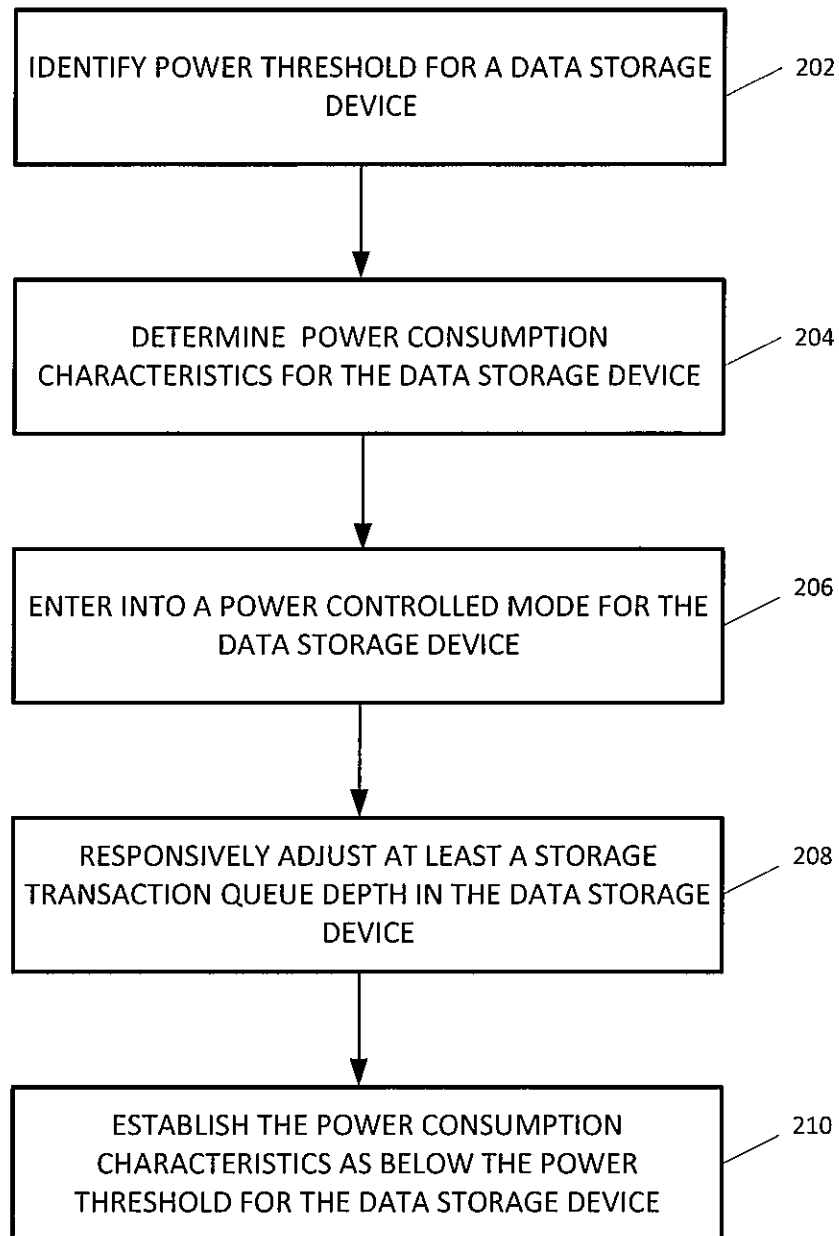


FIGURE 1

**FIGURE 2**

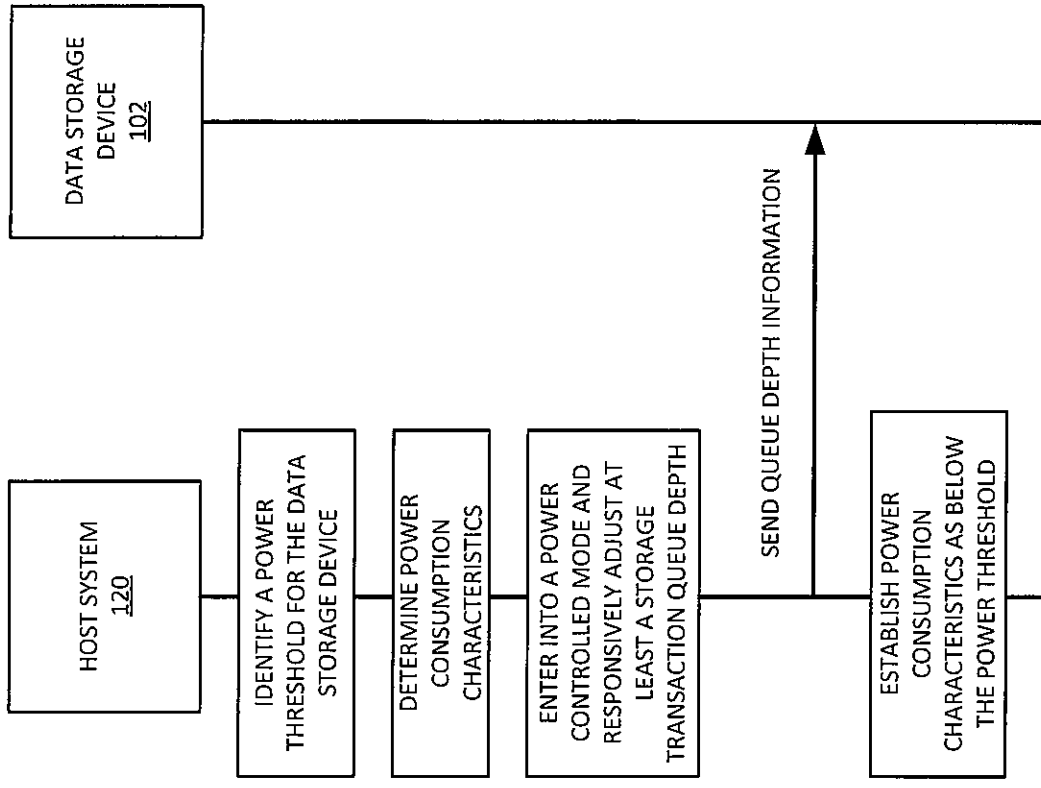


FIGURE 3B

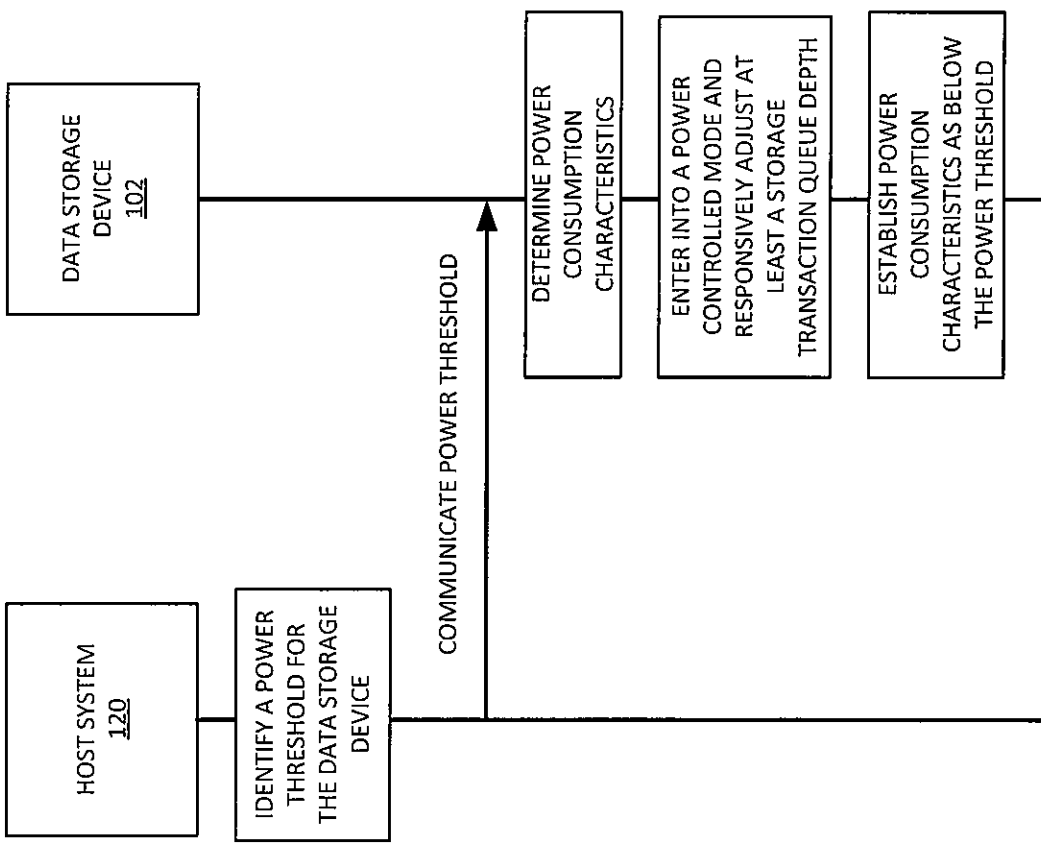


FIGURE 3A

400

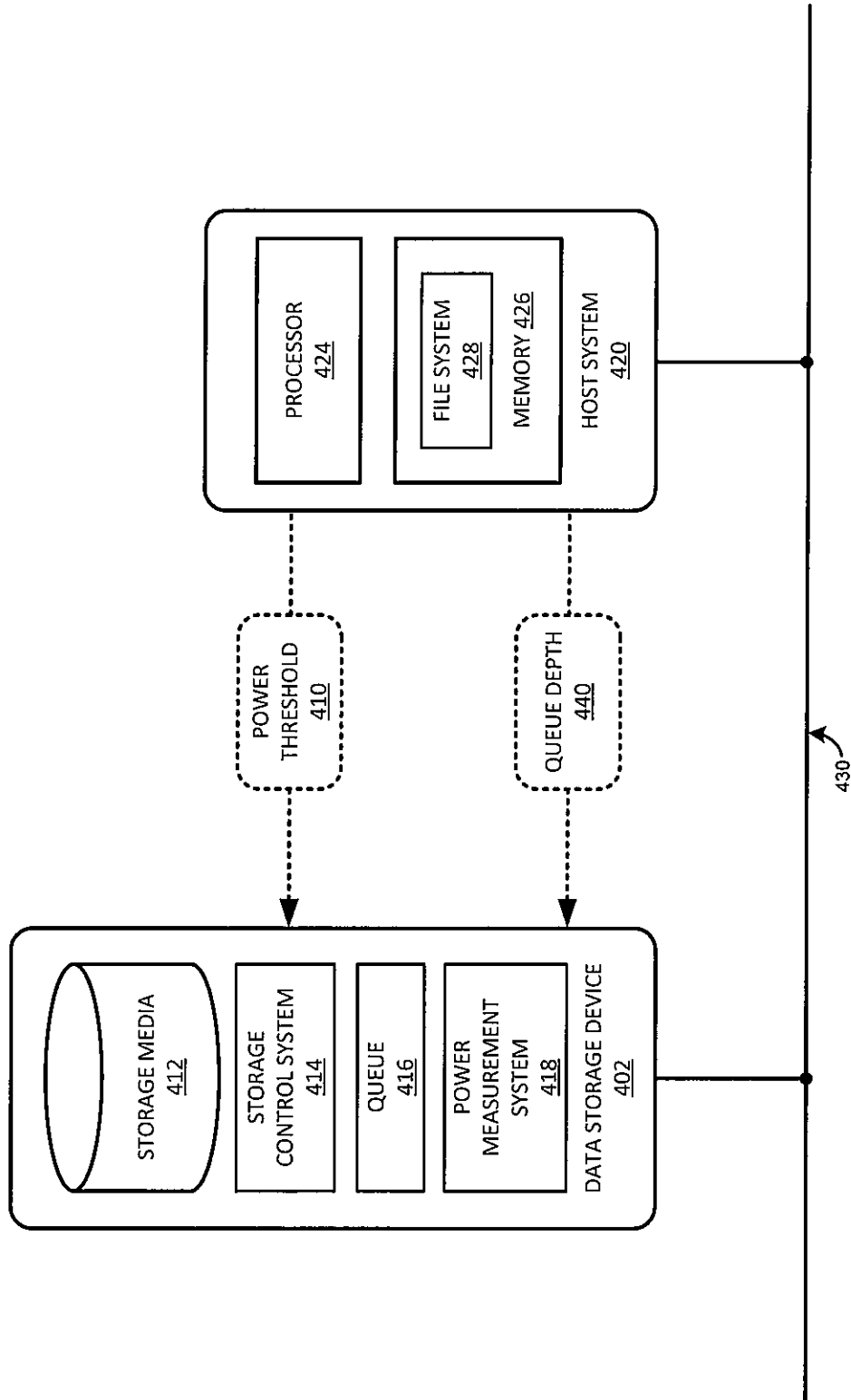
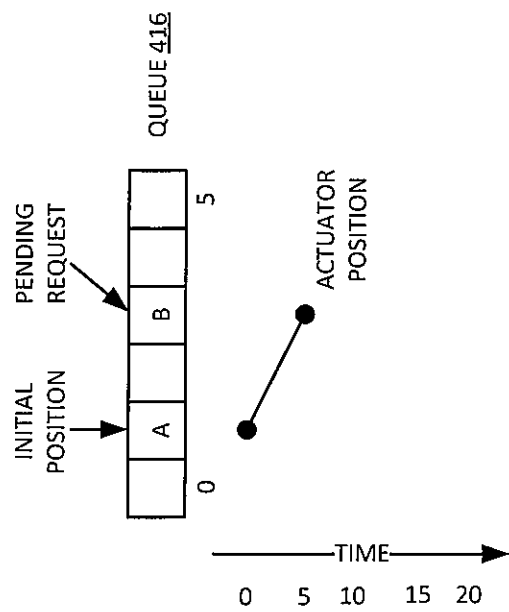
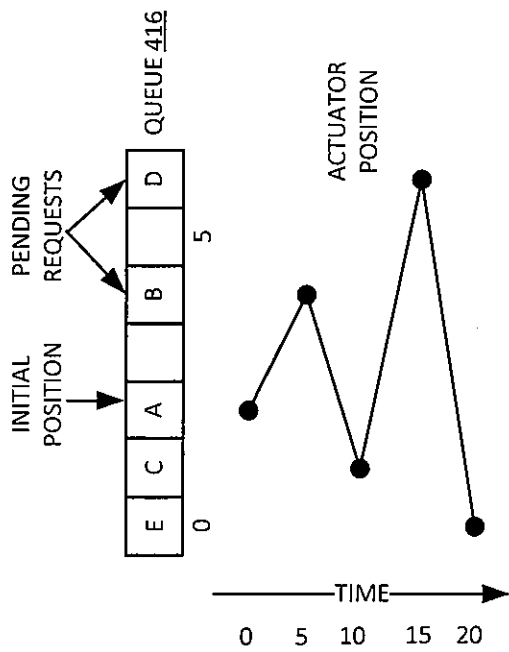


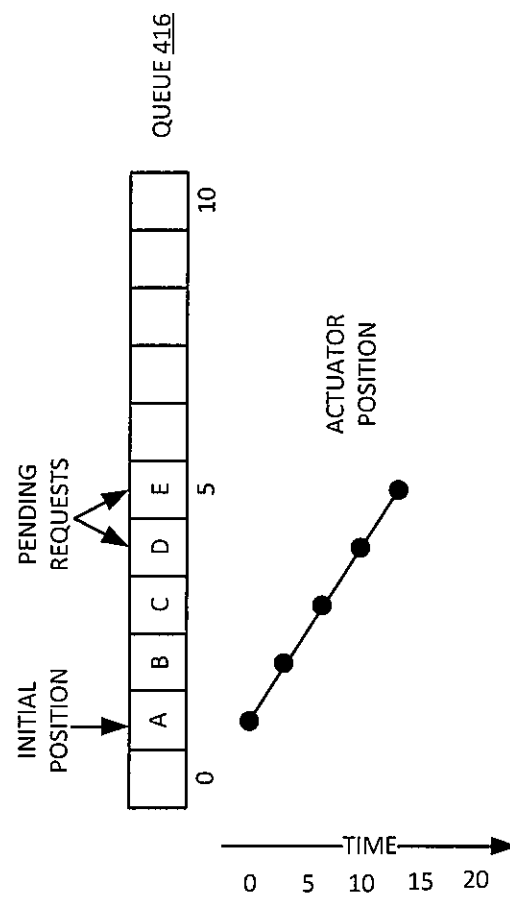
FIGURE 4



LATENCY MODE

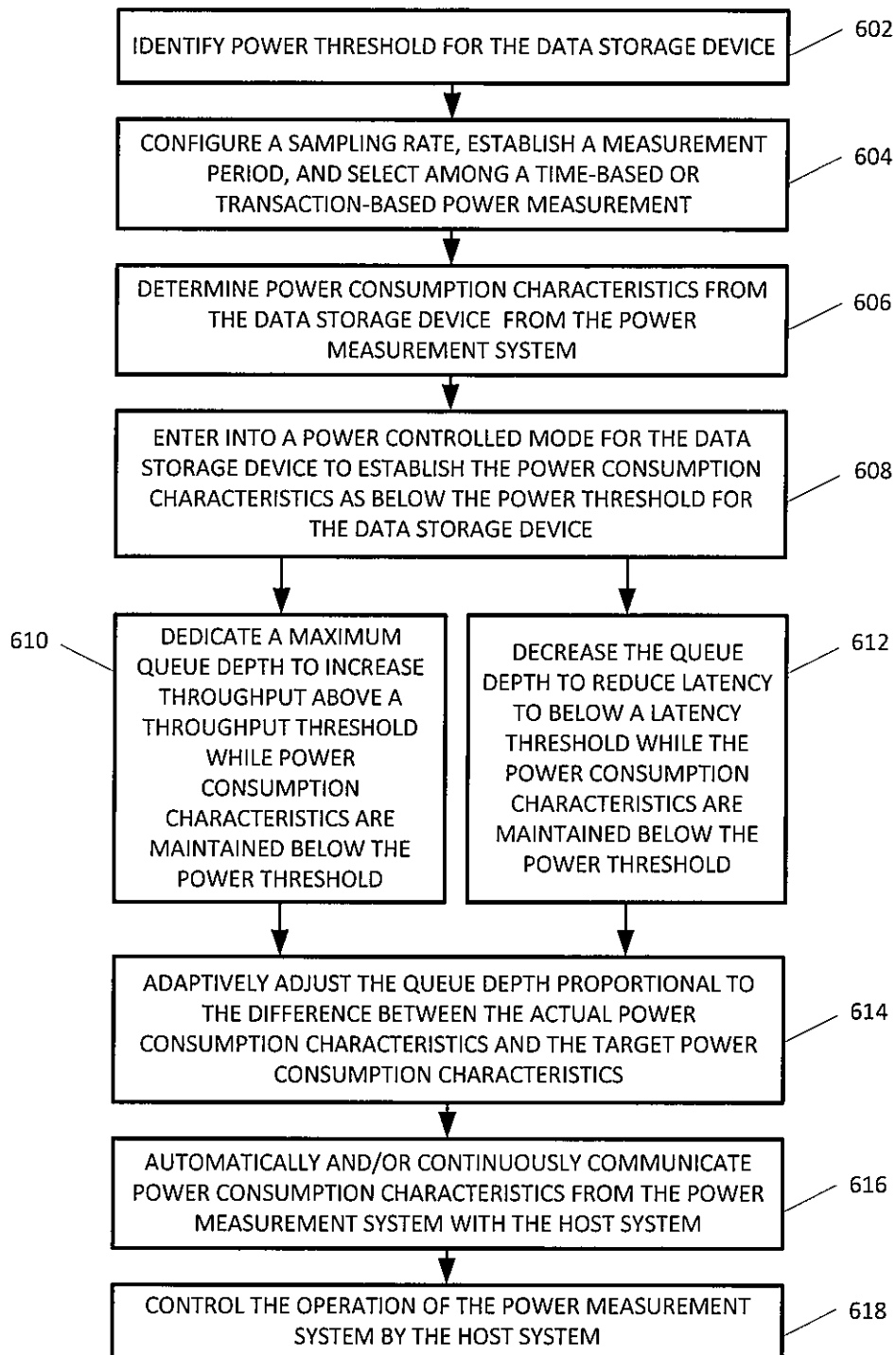


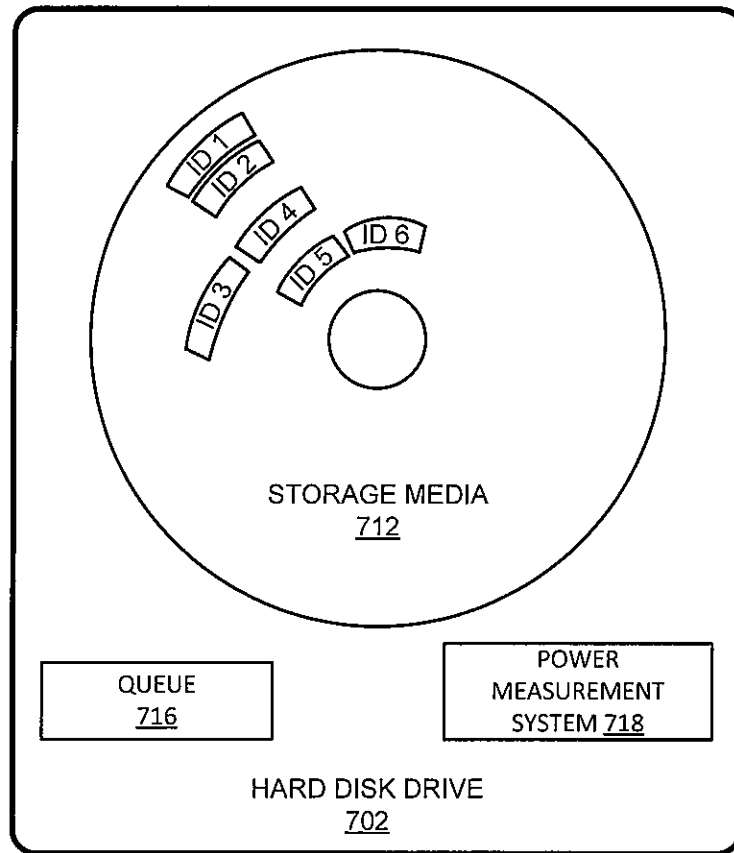
NORMAL OPERATING MODE

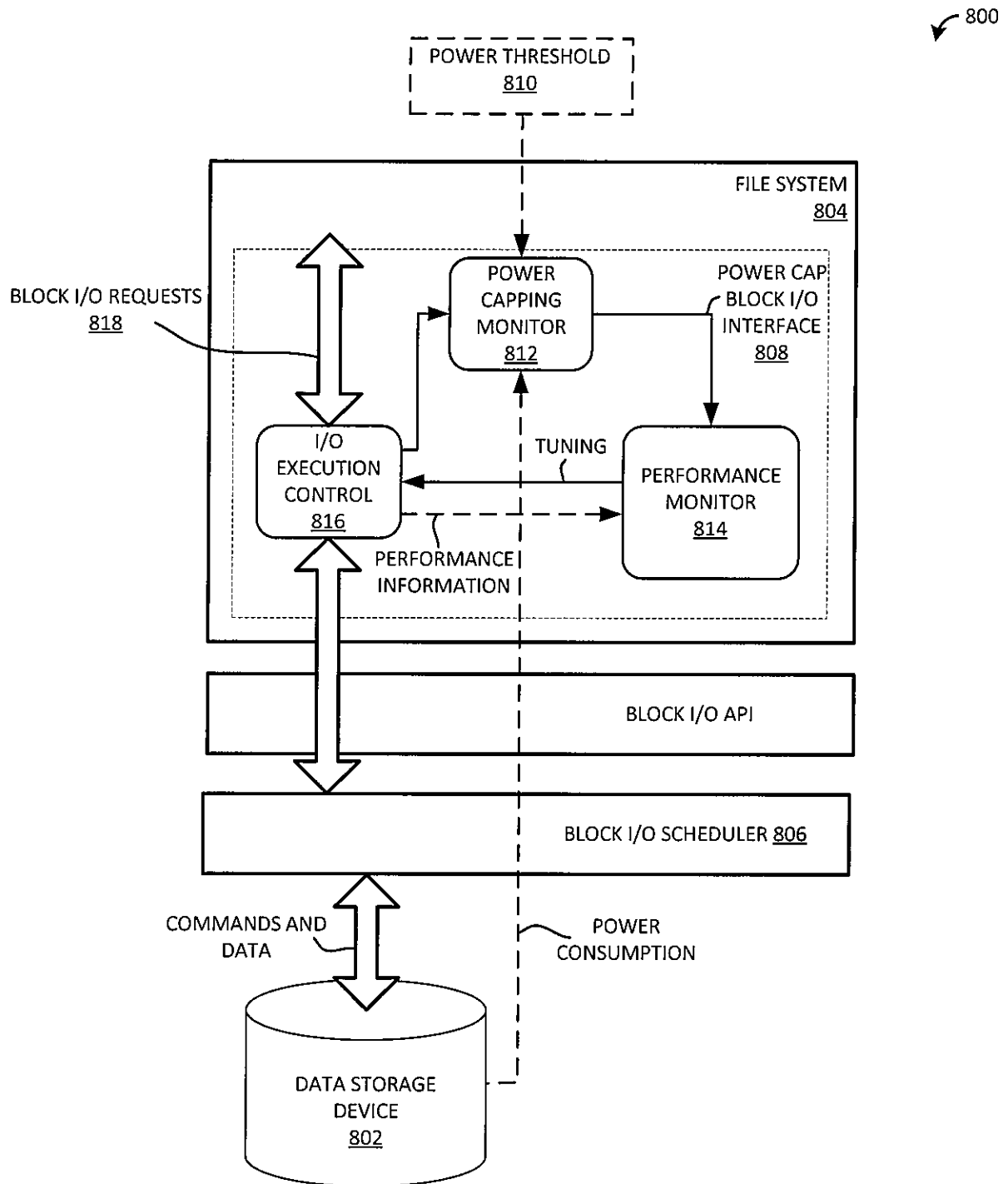


THROUGHPUT MODE

FIGURE 5

**FIGURE 6**

**FIGURE 7**

**FIGURE 8**

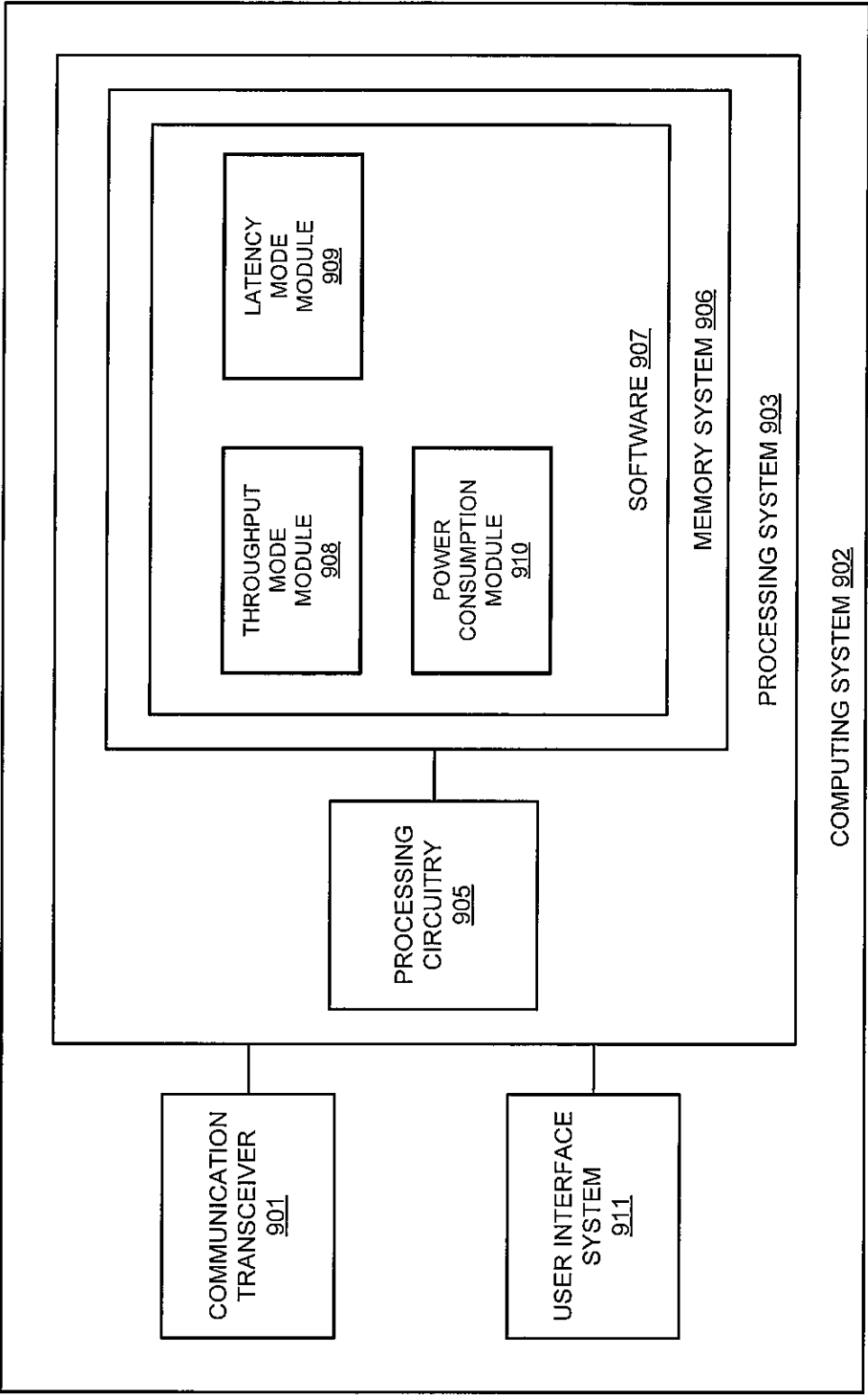


FIGURE 9