SELECT LINE DRIVER FOR A DISPLAY MATRIX WITH TOGGLING BACKPLANE

ABSTRACT

When a given row (118(n)) in an array of a liquid crystal display having a toggling backplane voltage is de-selected, the row select line voltage (OUT(n)a) also toggles to prevent capacitive current in the pixel capacitance. The row select line driver includes a pair of transistors (81,79) coupled in a push-pull configuration. The pair of transistors are responsive to a control signal (OUT(n-1)) that is produced in a corresponding stage (N-1,FIGURE 3) of cascaded stages of a shift register. The pair of transistors form a buffer stage that prevents the toggling voltage (OUT(n)a) developed in the row select line (118(n)) when the row is de-selected from affecting the operation of the shift register.

Figure 4