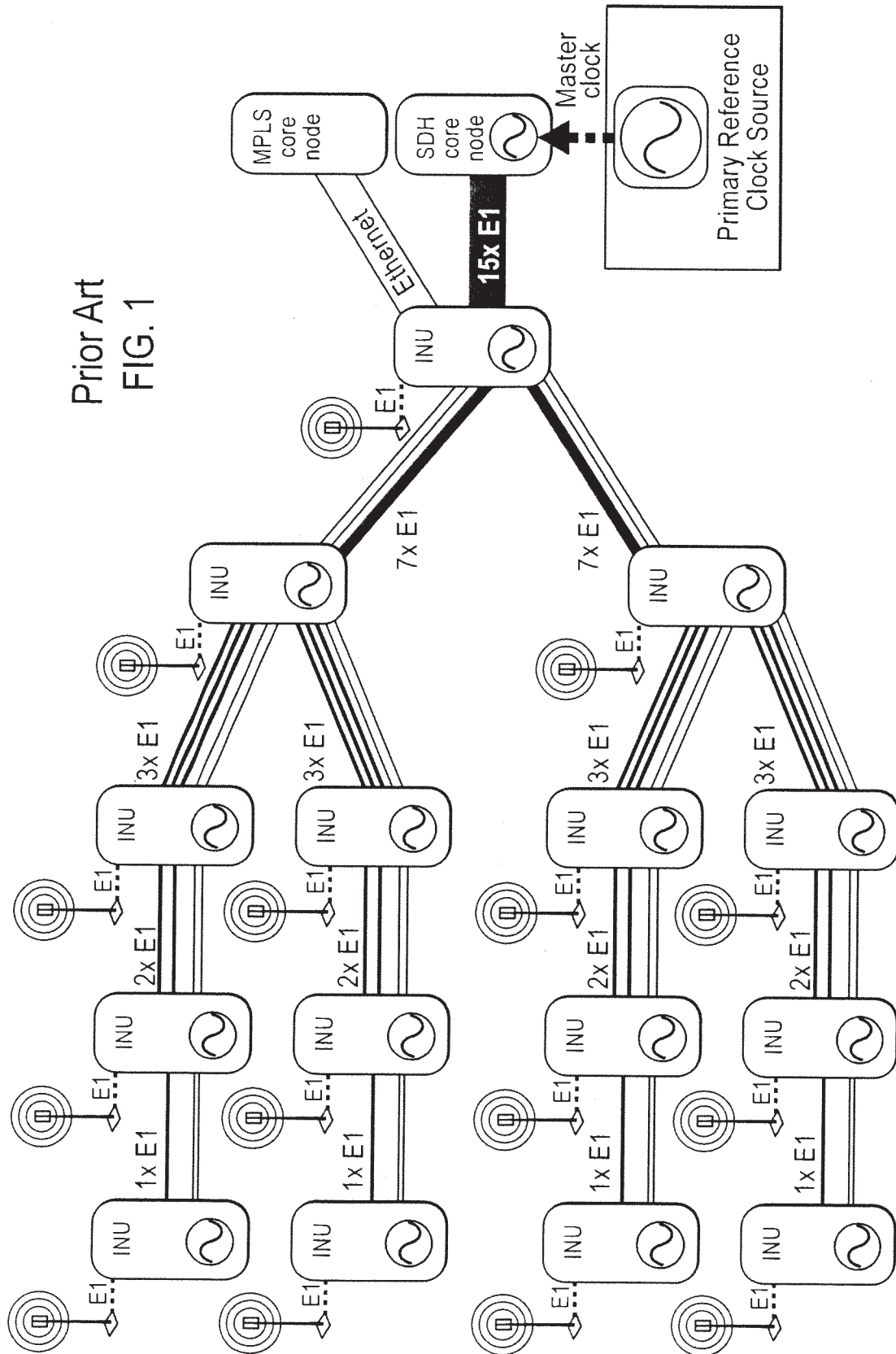
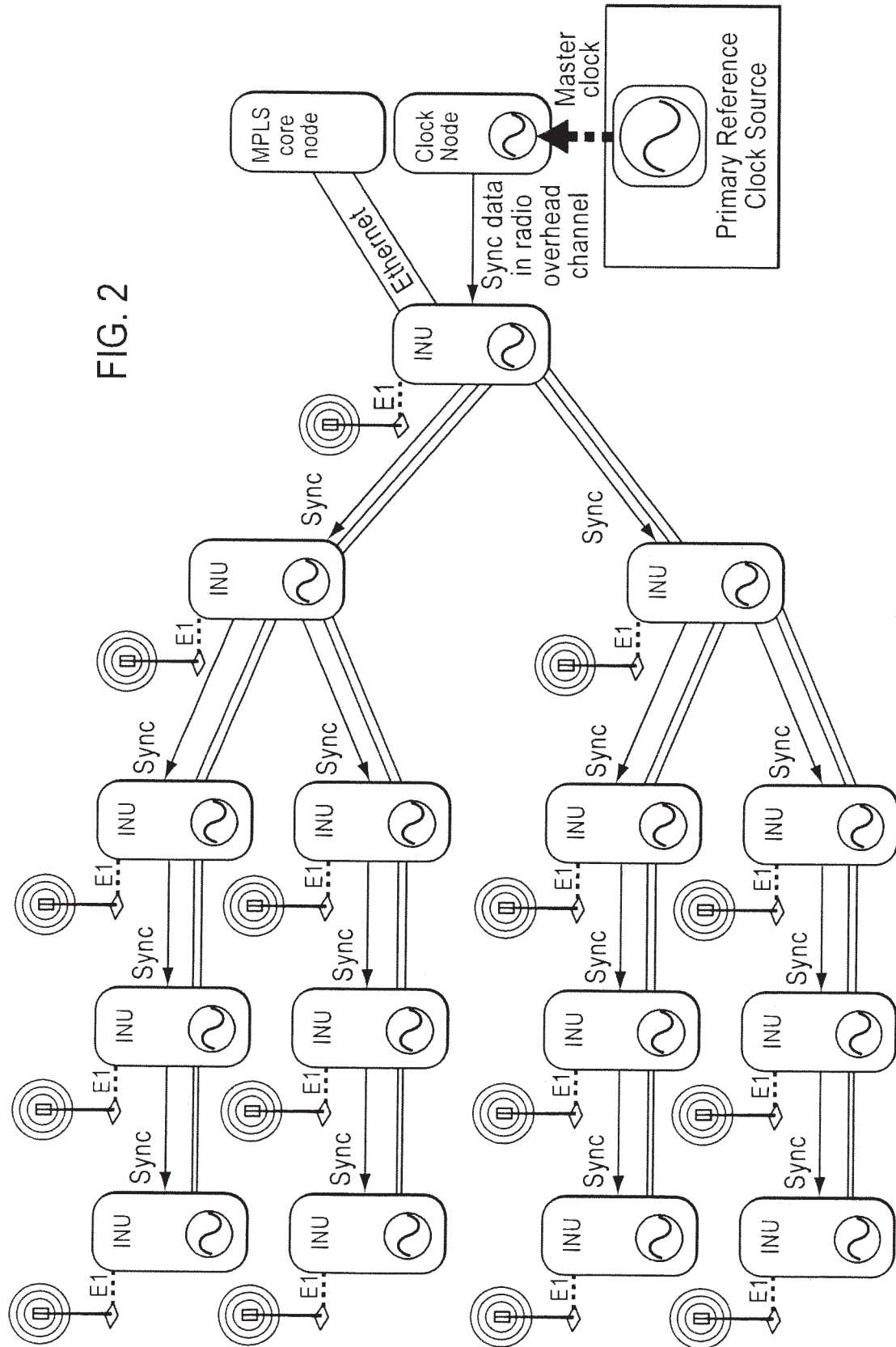






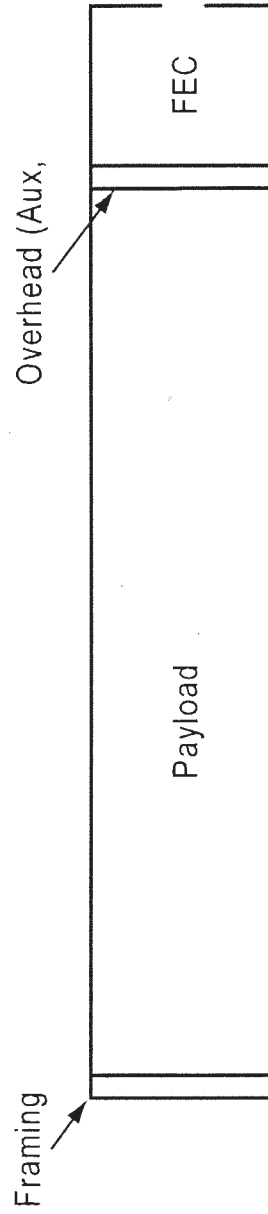


FIG. 3

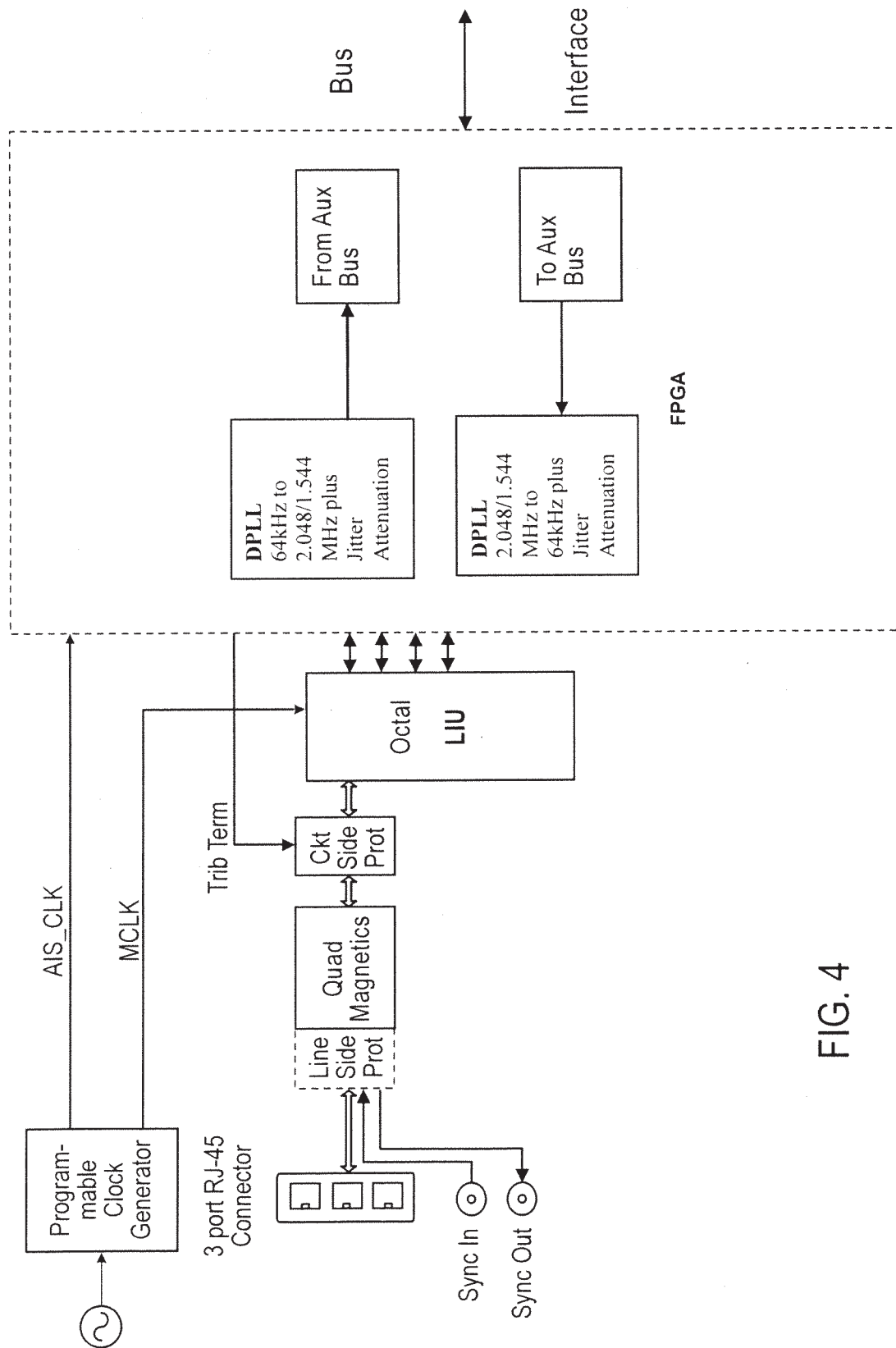
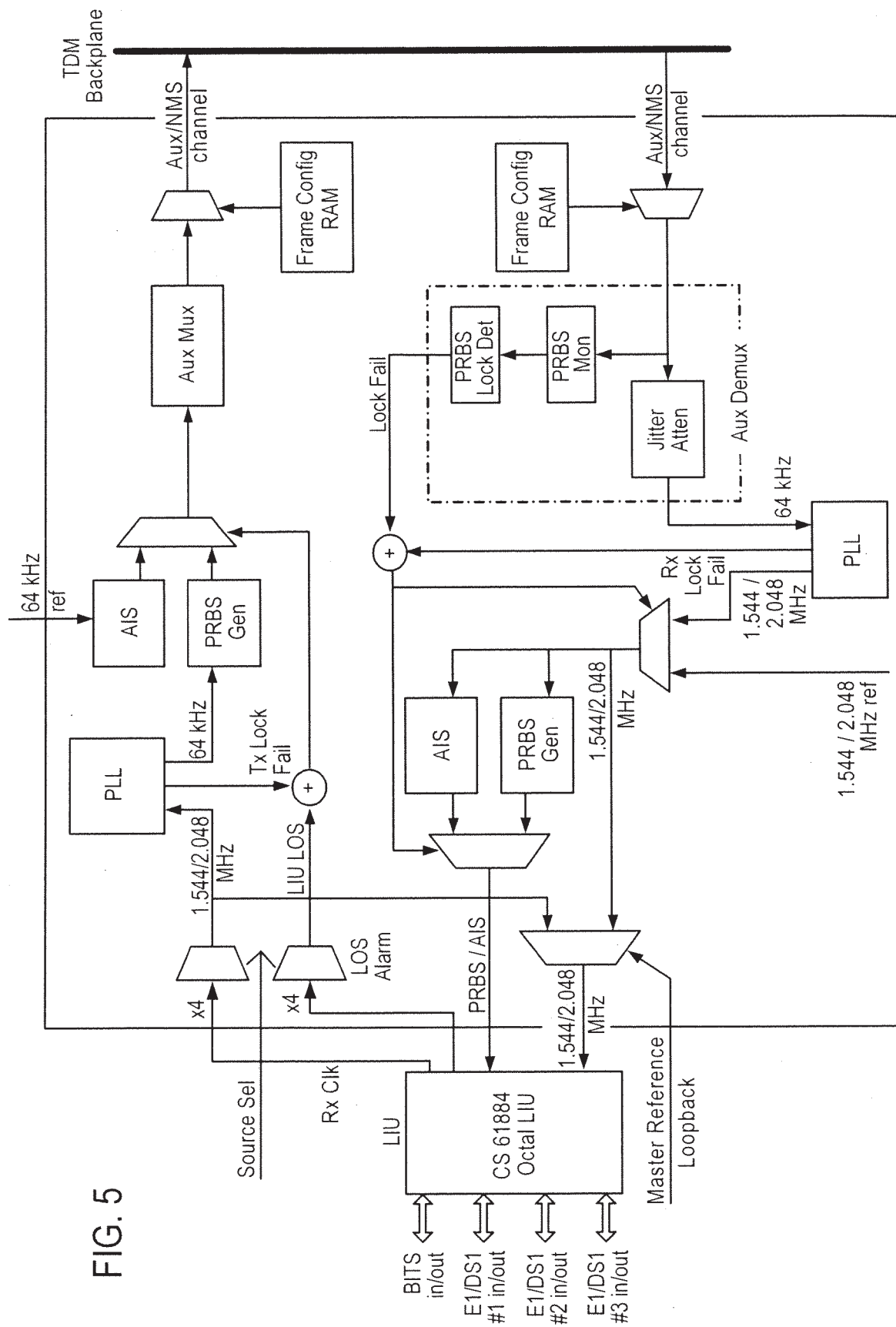


FIG. 4



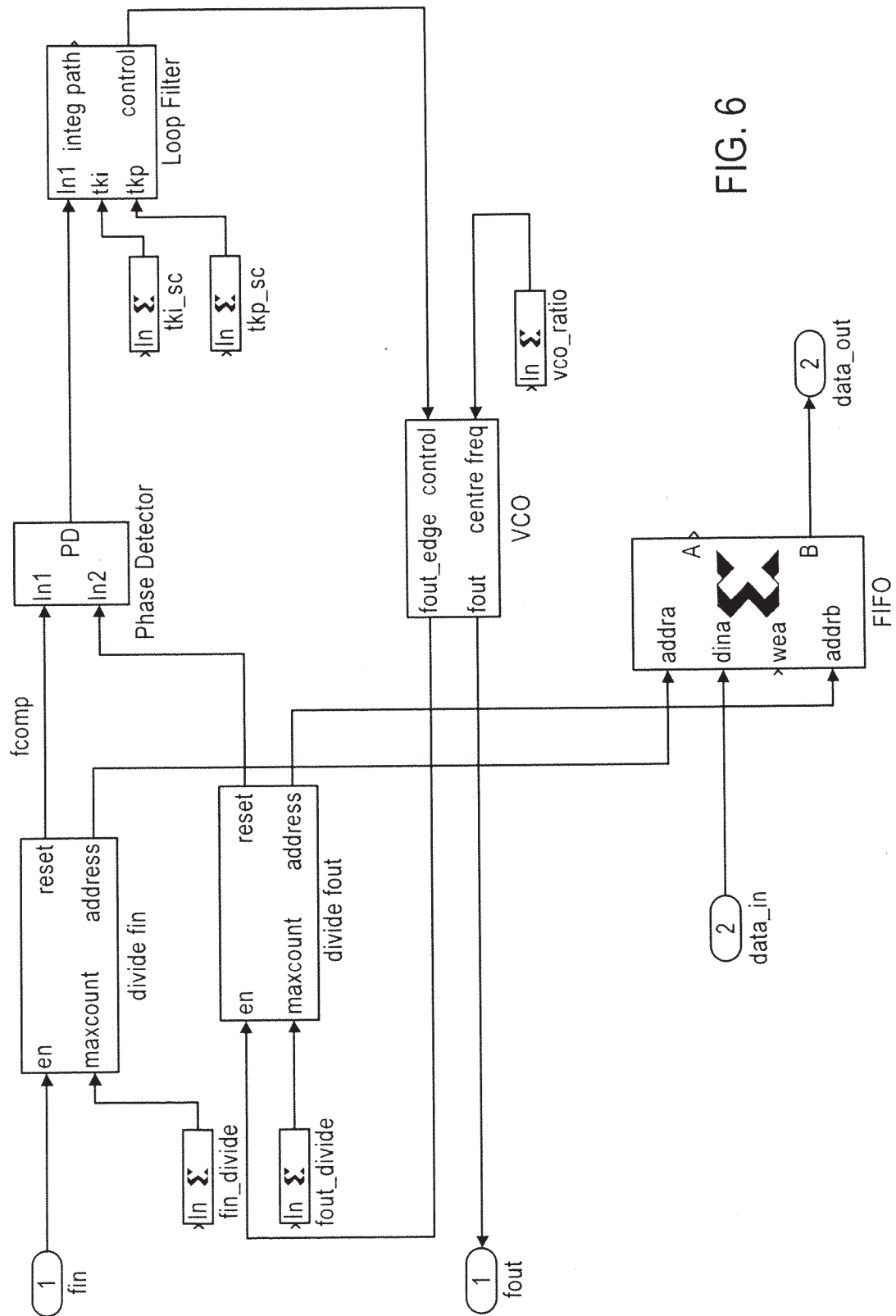


FIG. 6

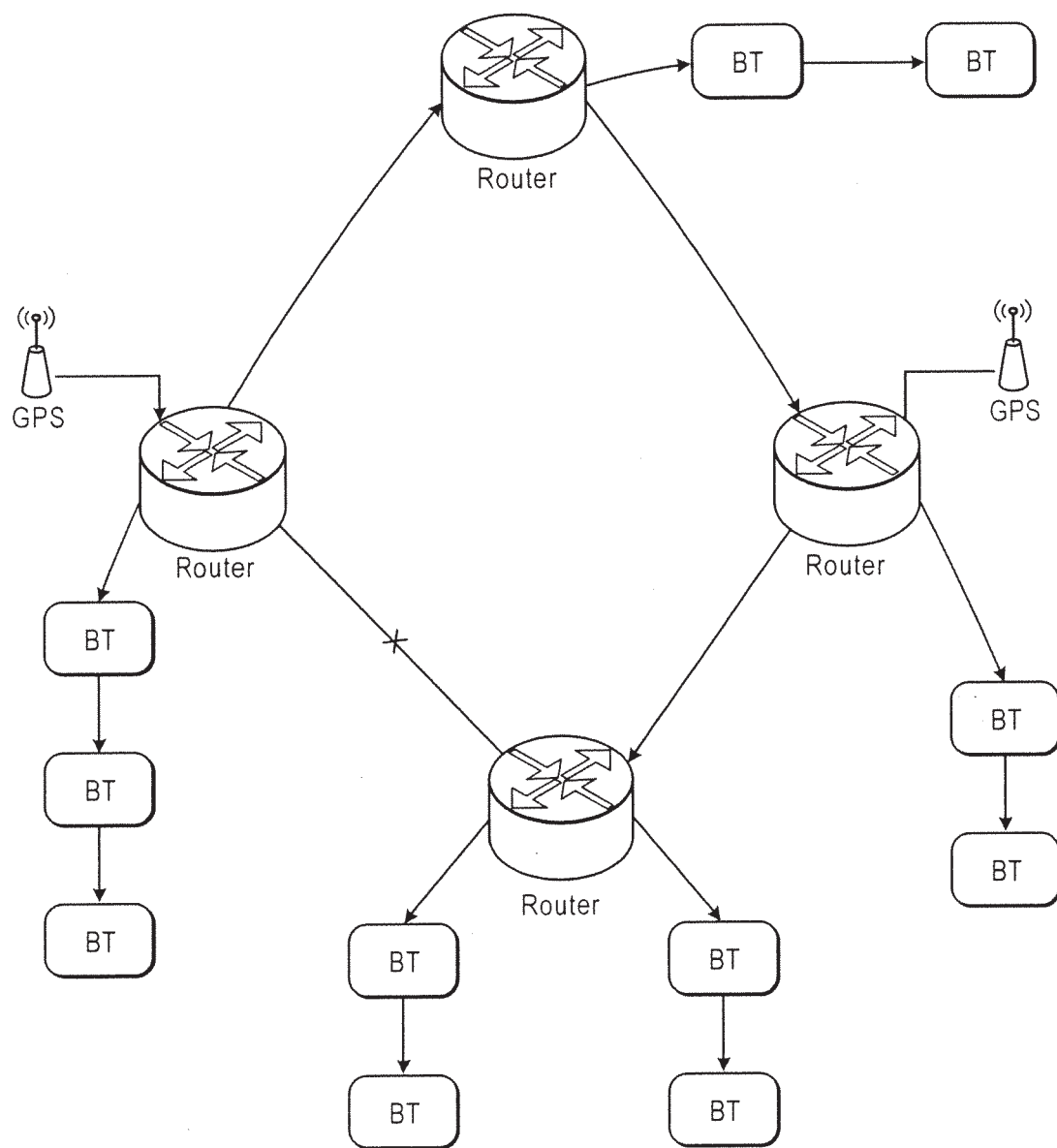
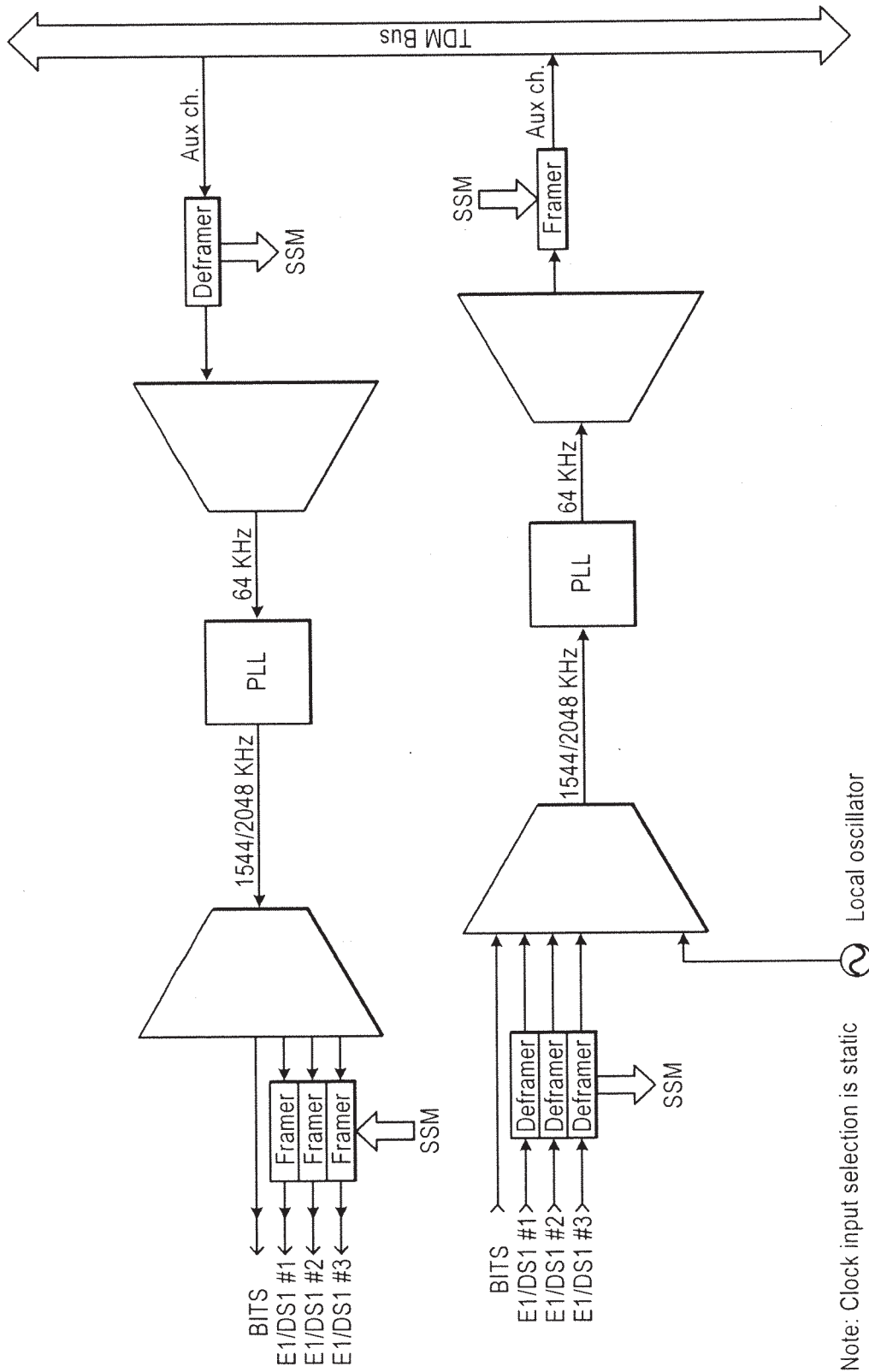


FIG. 7



Note: Clock input selection is static

FIG. 8

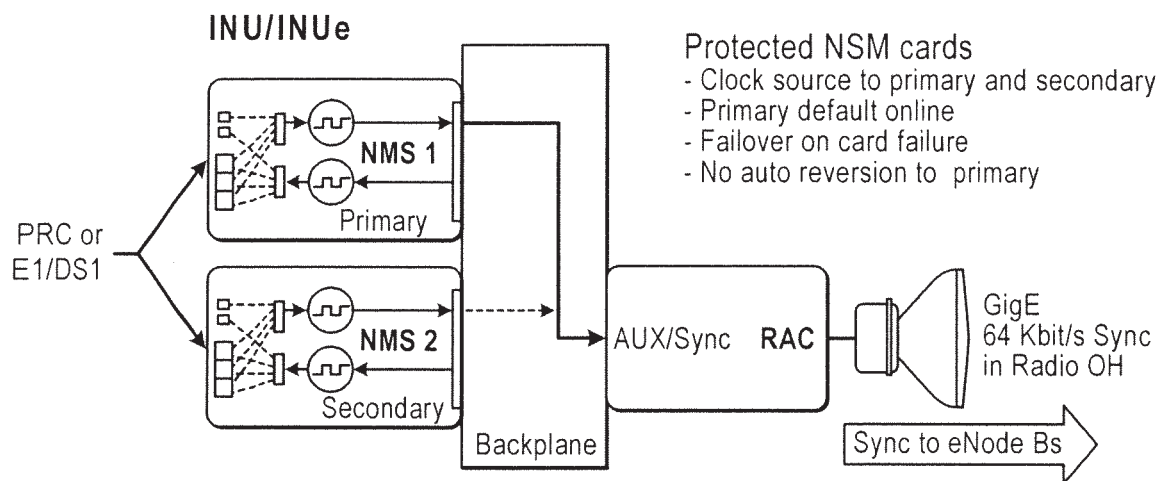
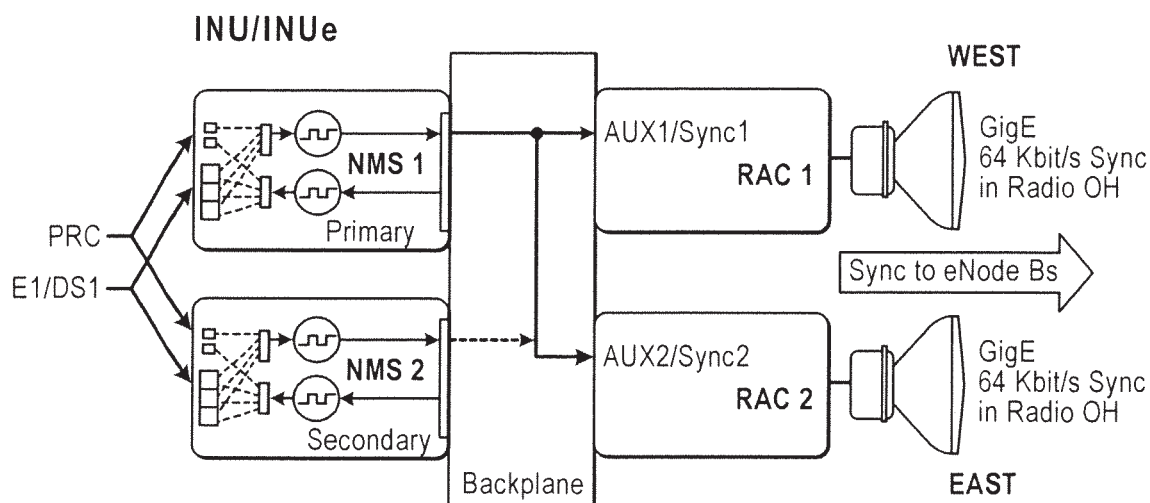


FIG. 9



Protected Cards and Clocks at Ring Source

- Primary online and writing to AUX1/RAC1 & AUX2/RAC2
- PRC set as priority clock
- Clock fail-over on clock LOS, AIS
- Auto-reversion to priority clock
- Card fail-over on card failure
- No card auto-reversion

FIG. 10

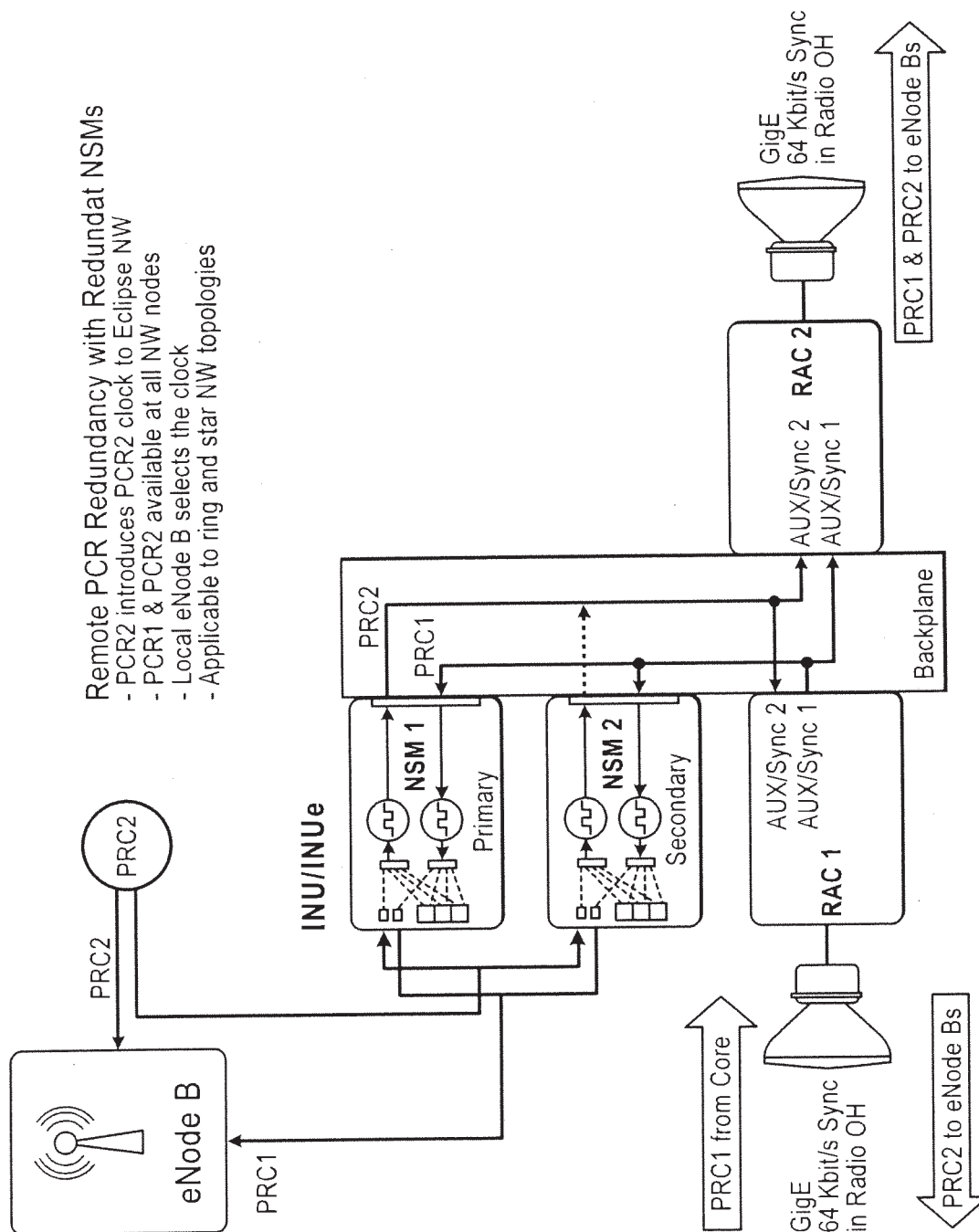


FIG. 11