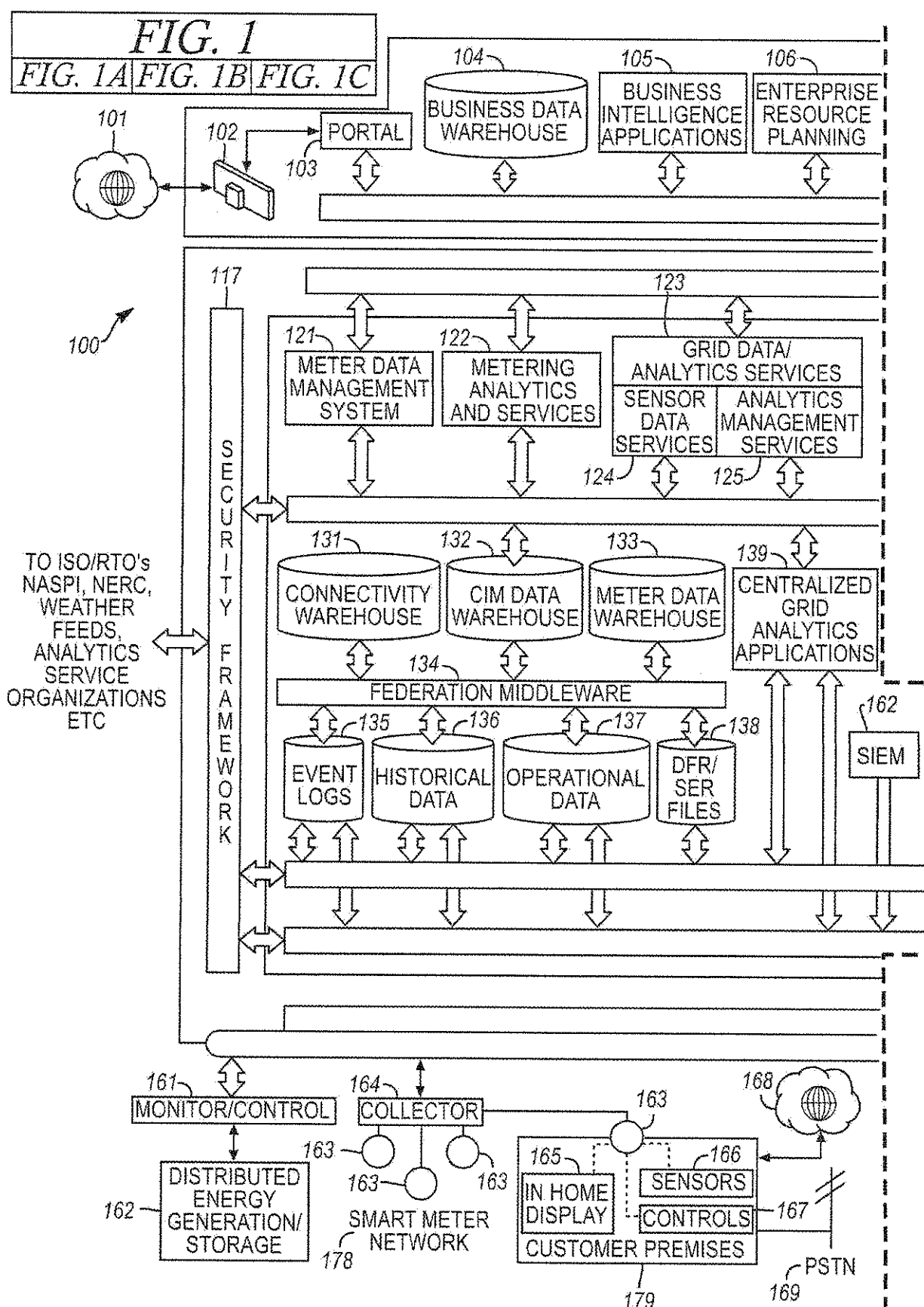
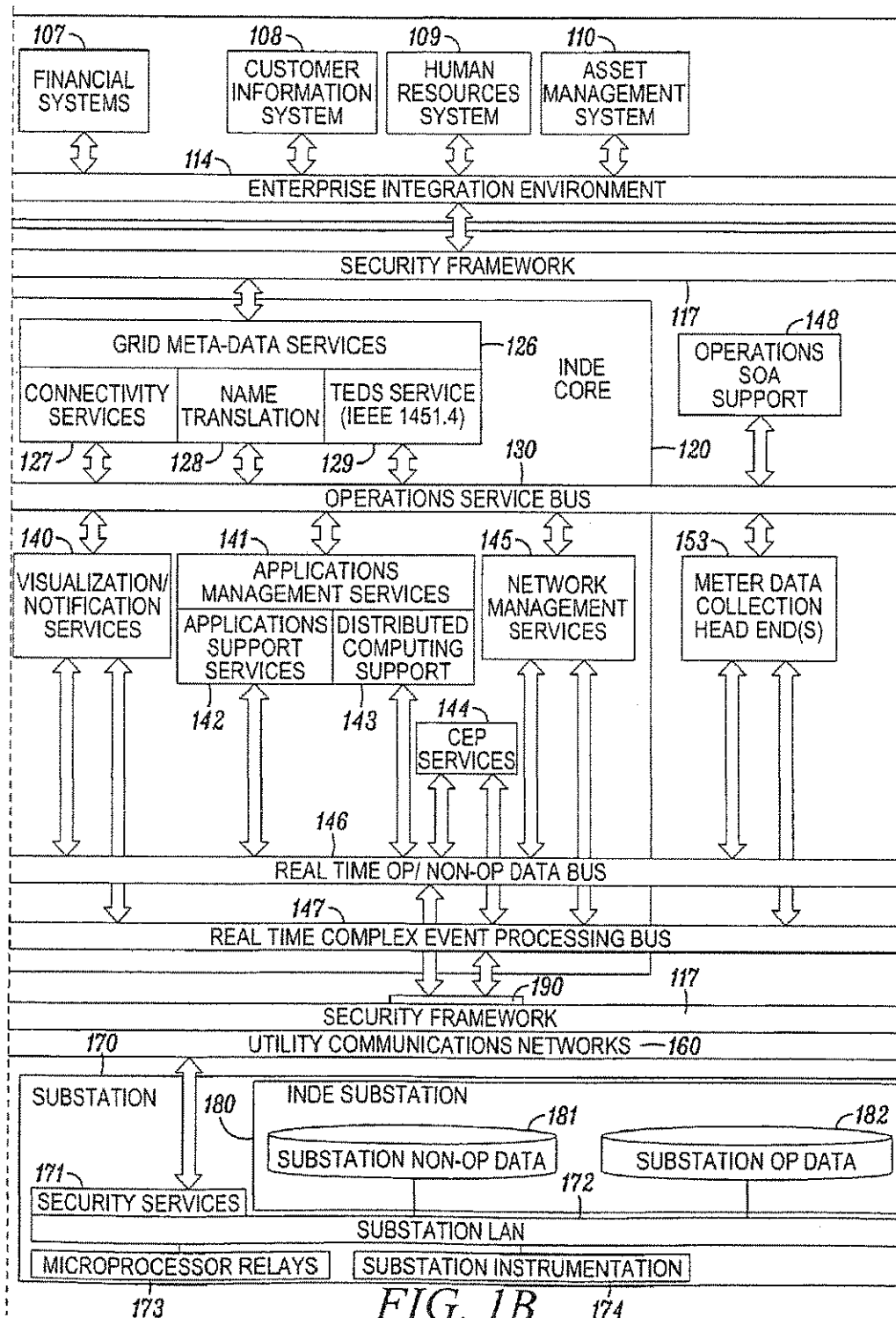


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**FIG. 1A**

SUBSTITUTE SHEET (RULE 26)

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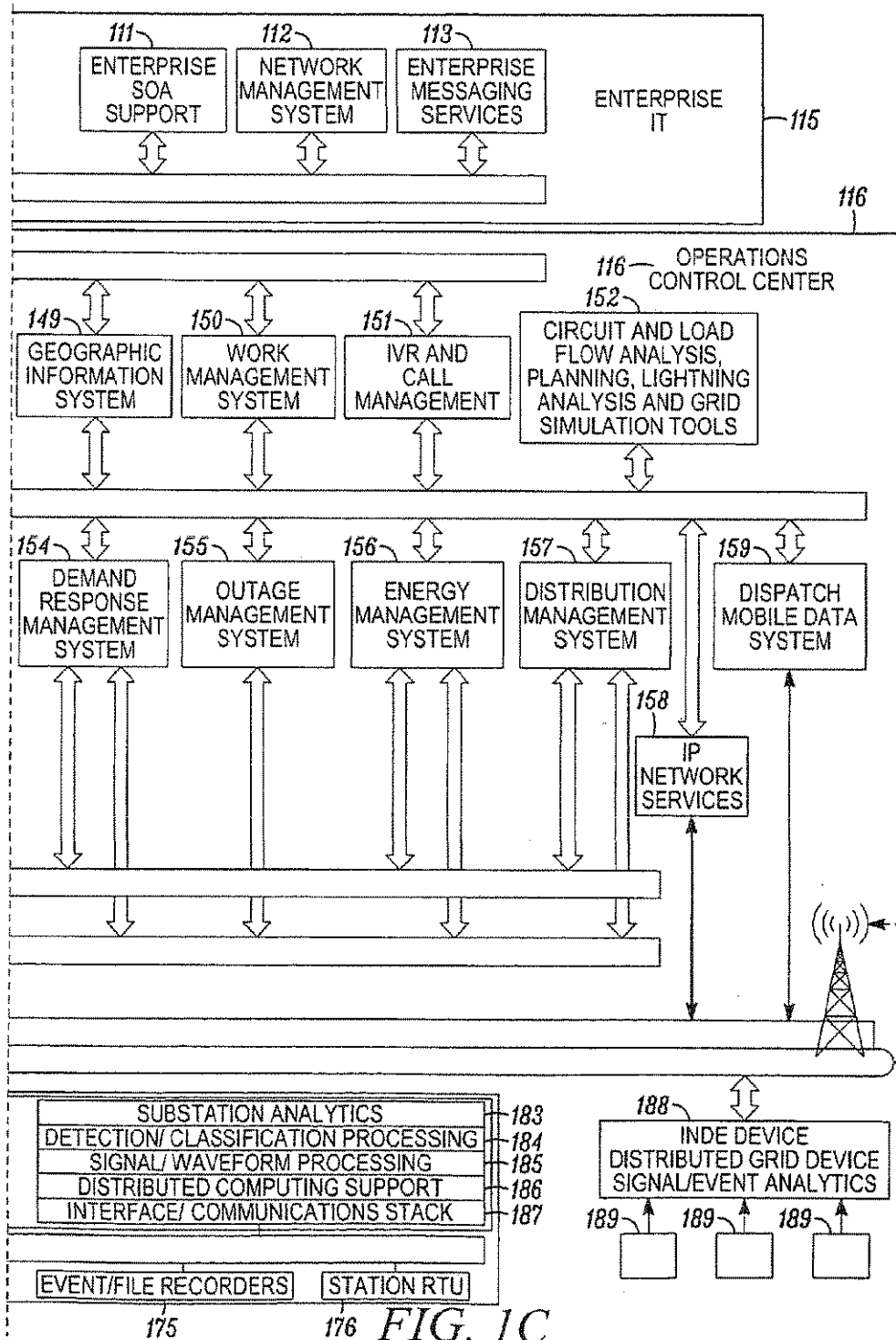


FIG. 1C

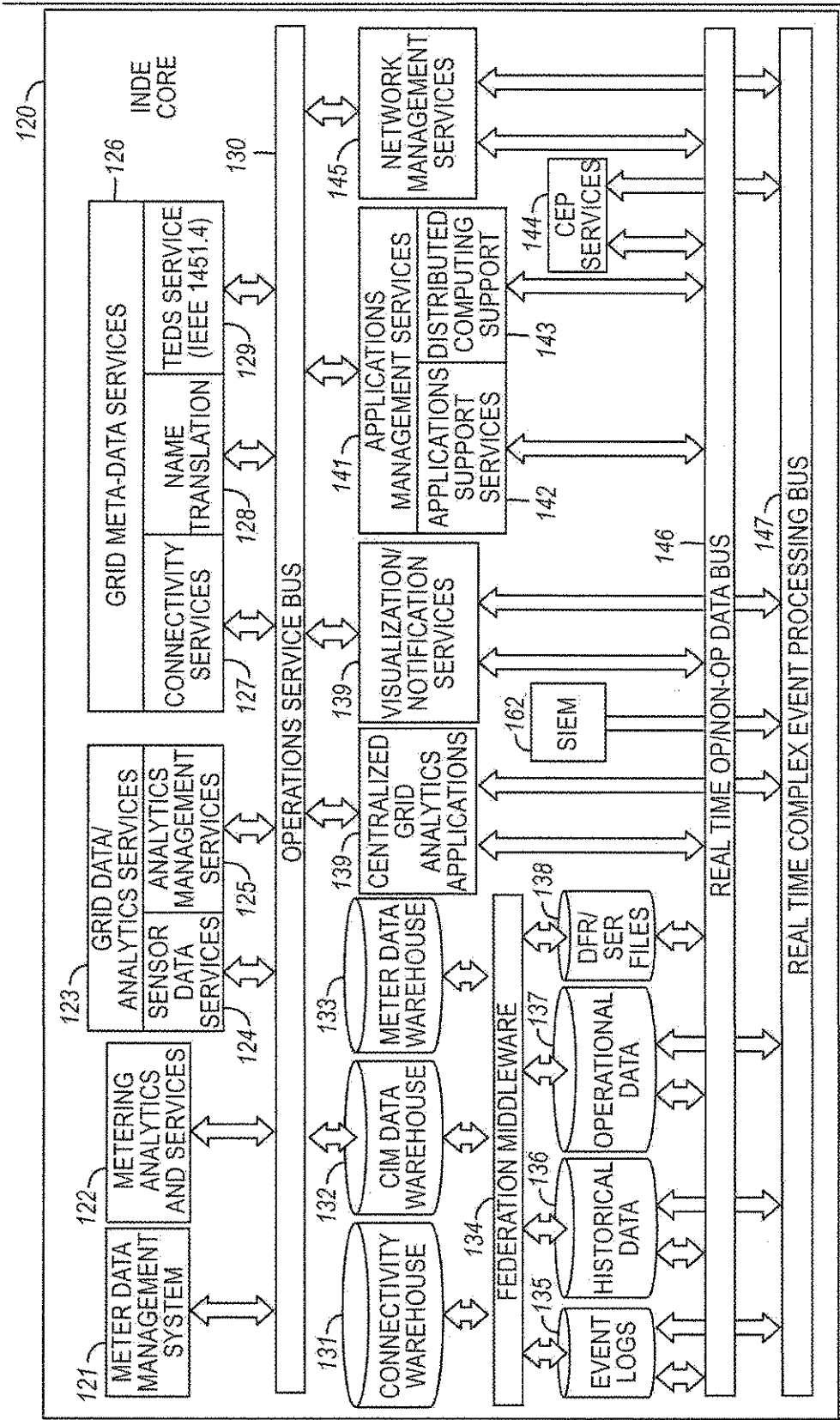
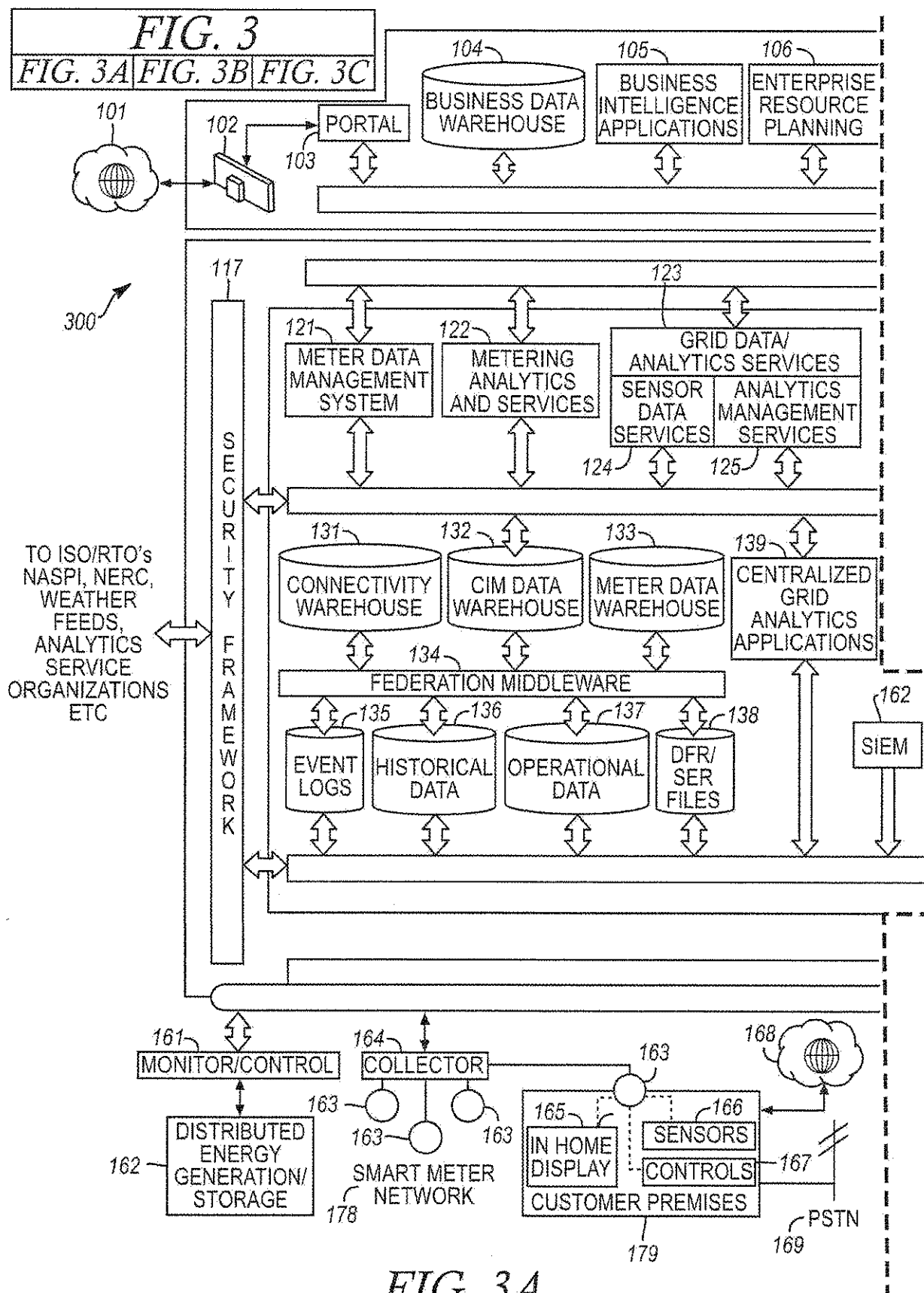
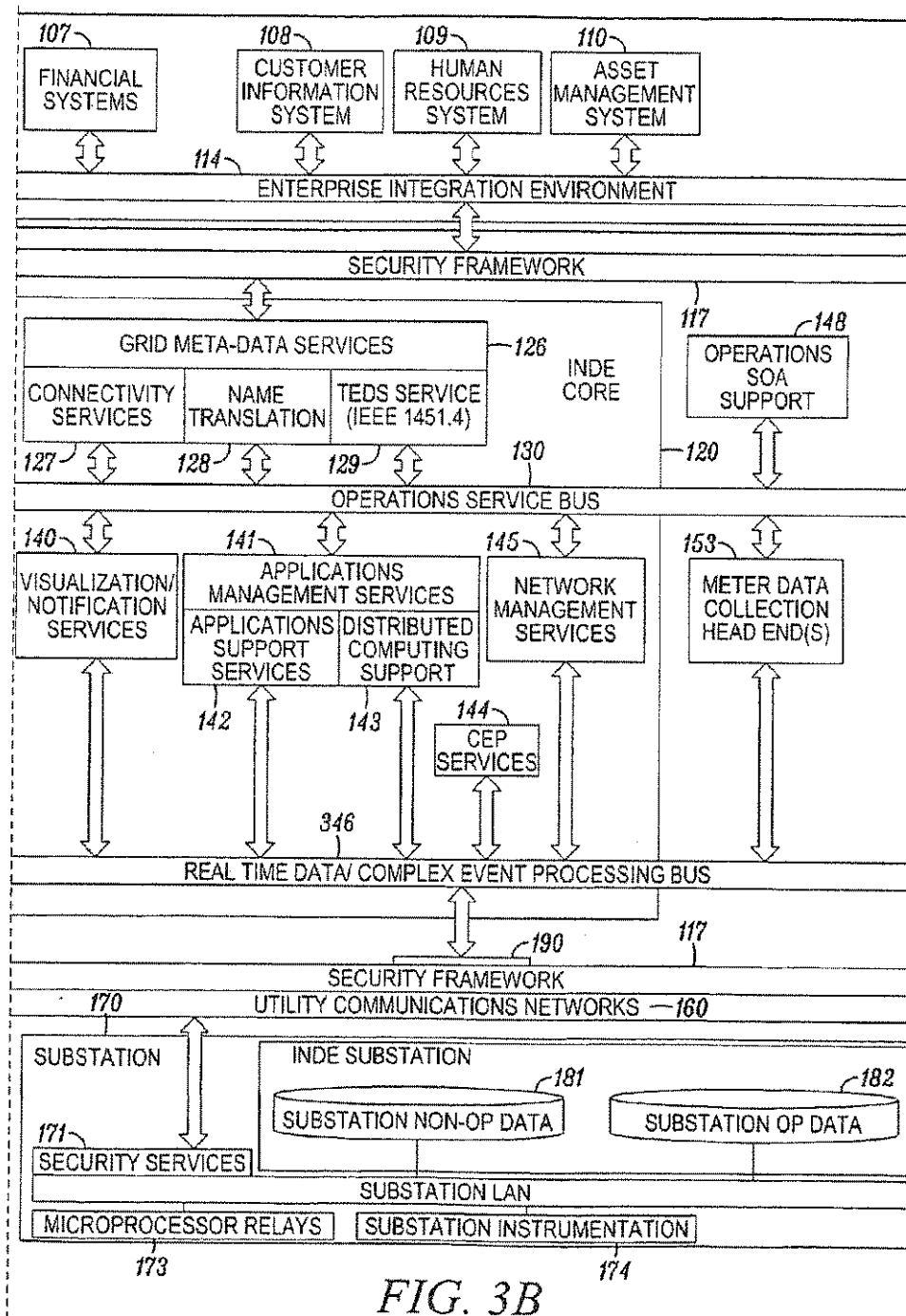


FIG. 2

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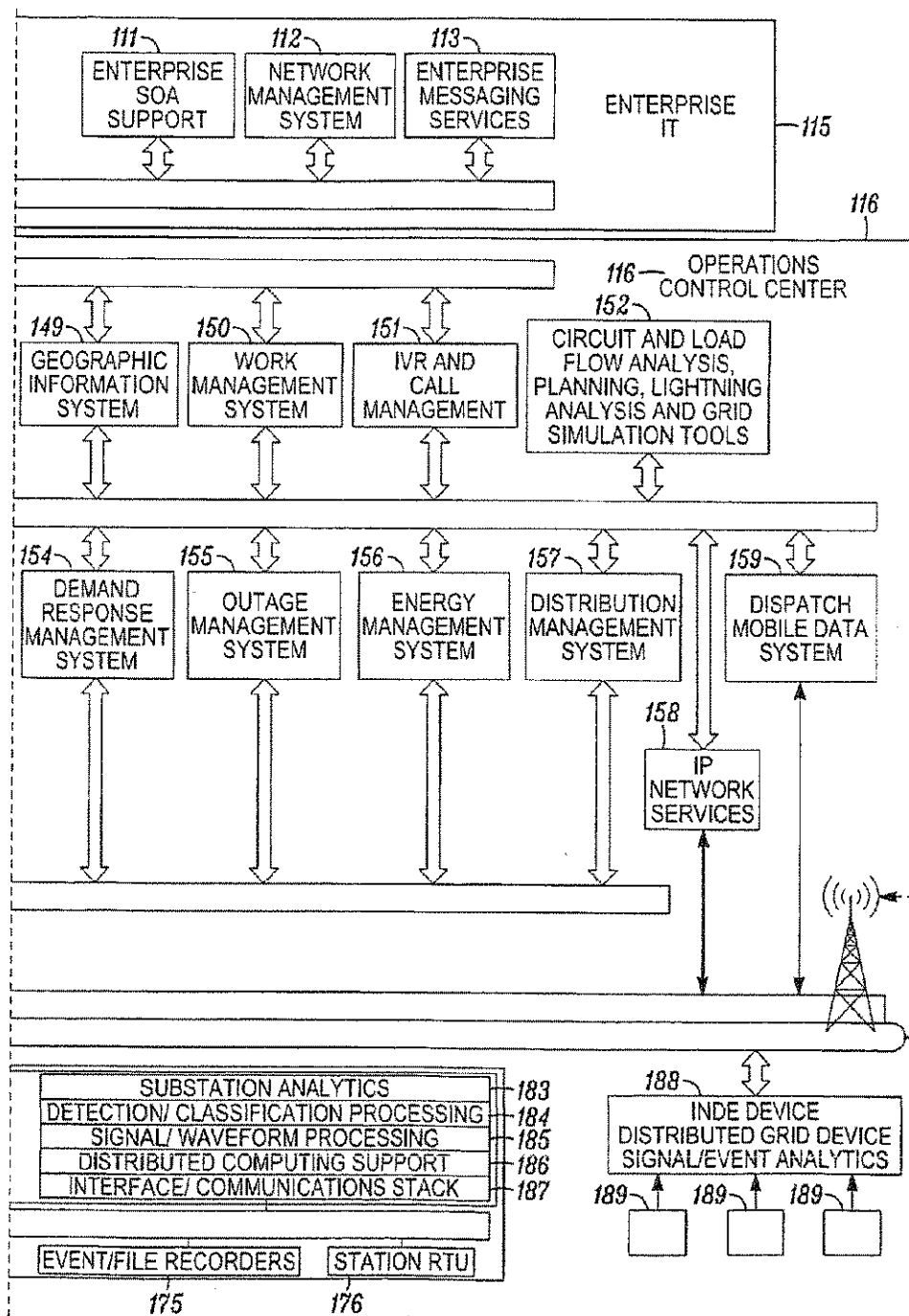


FIG. 3C

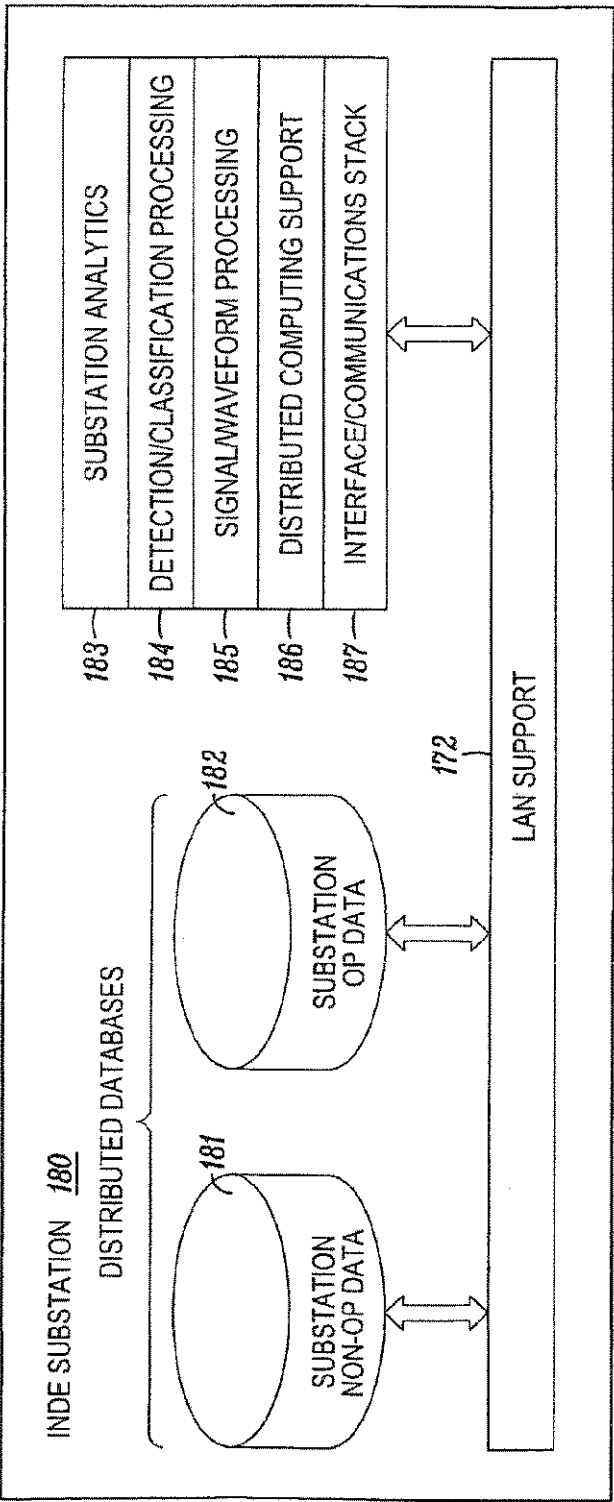


FIG. 4

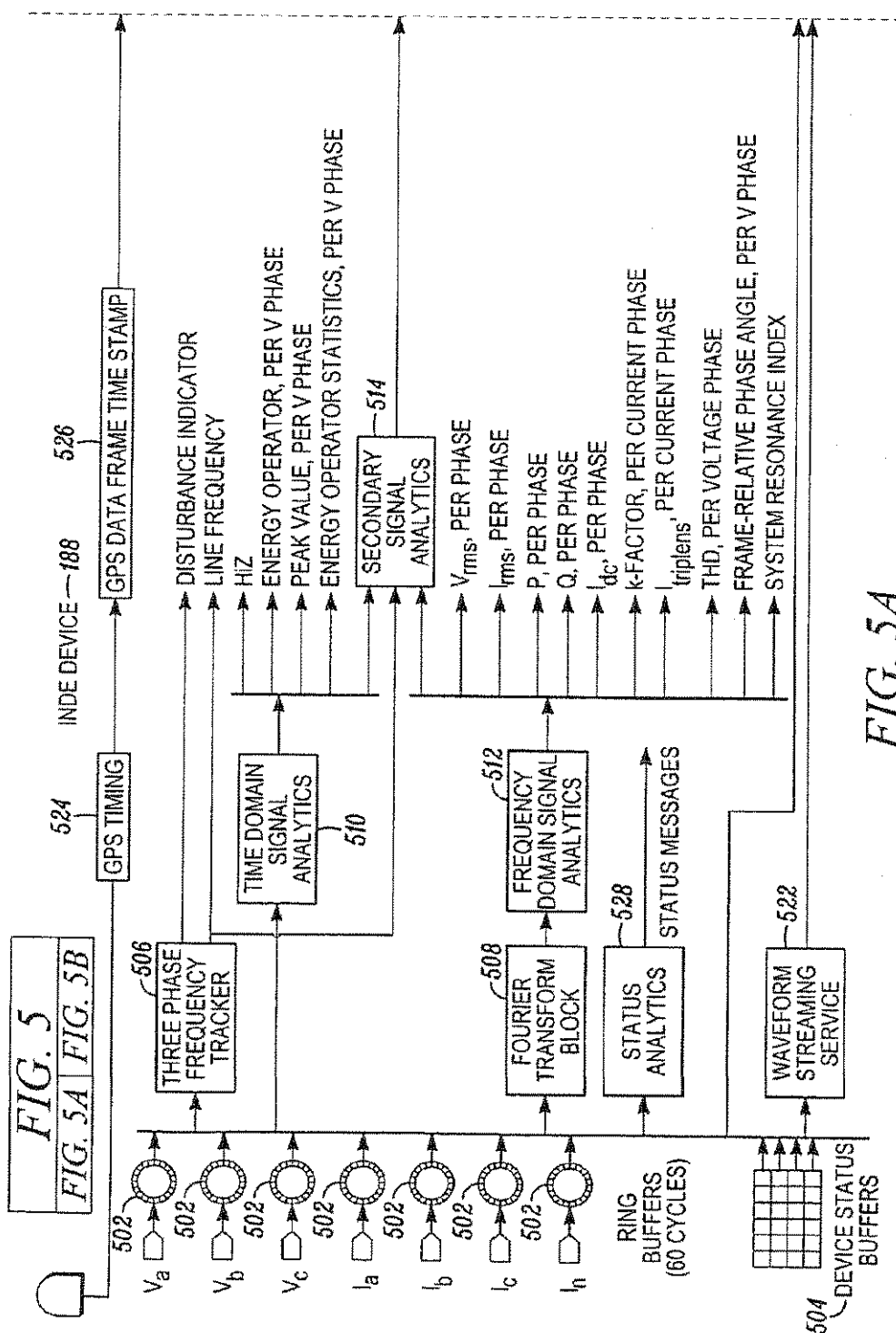


FIG. 5A

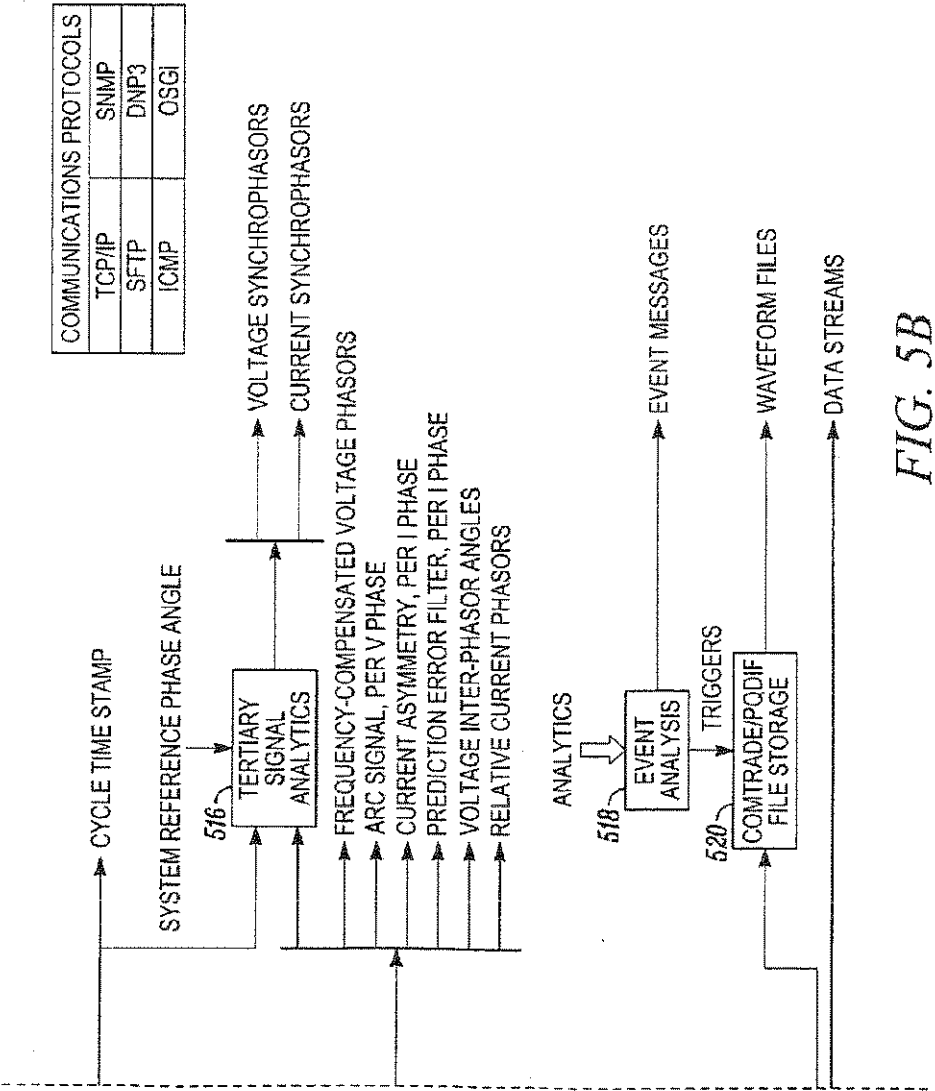
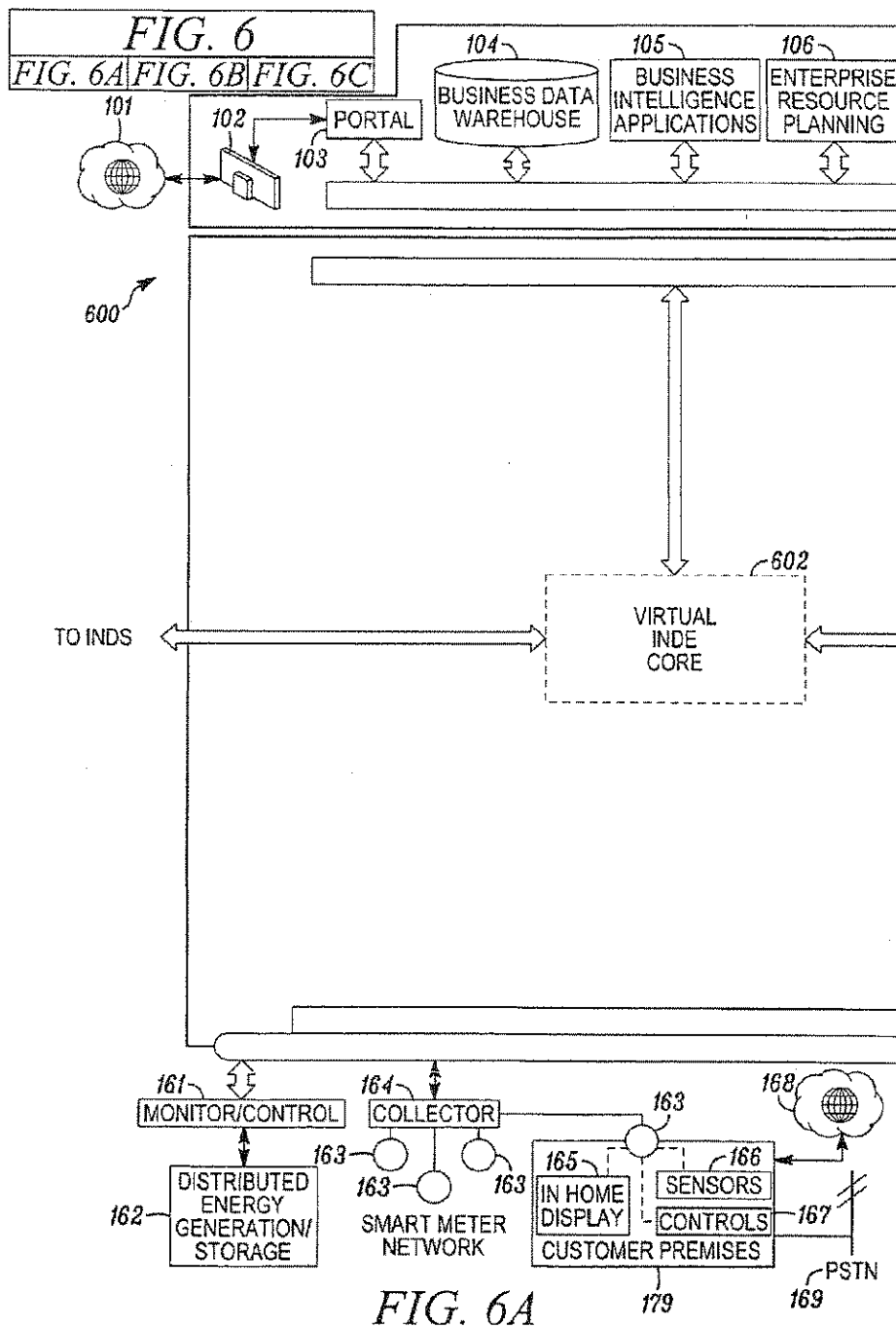
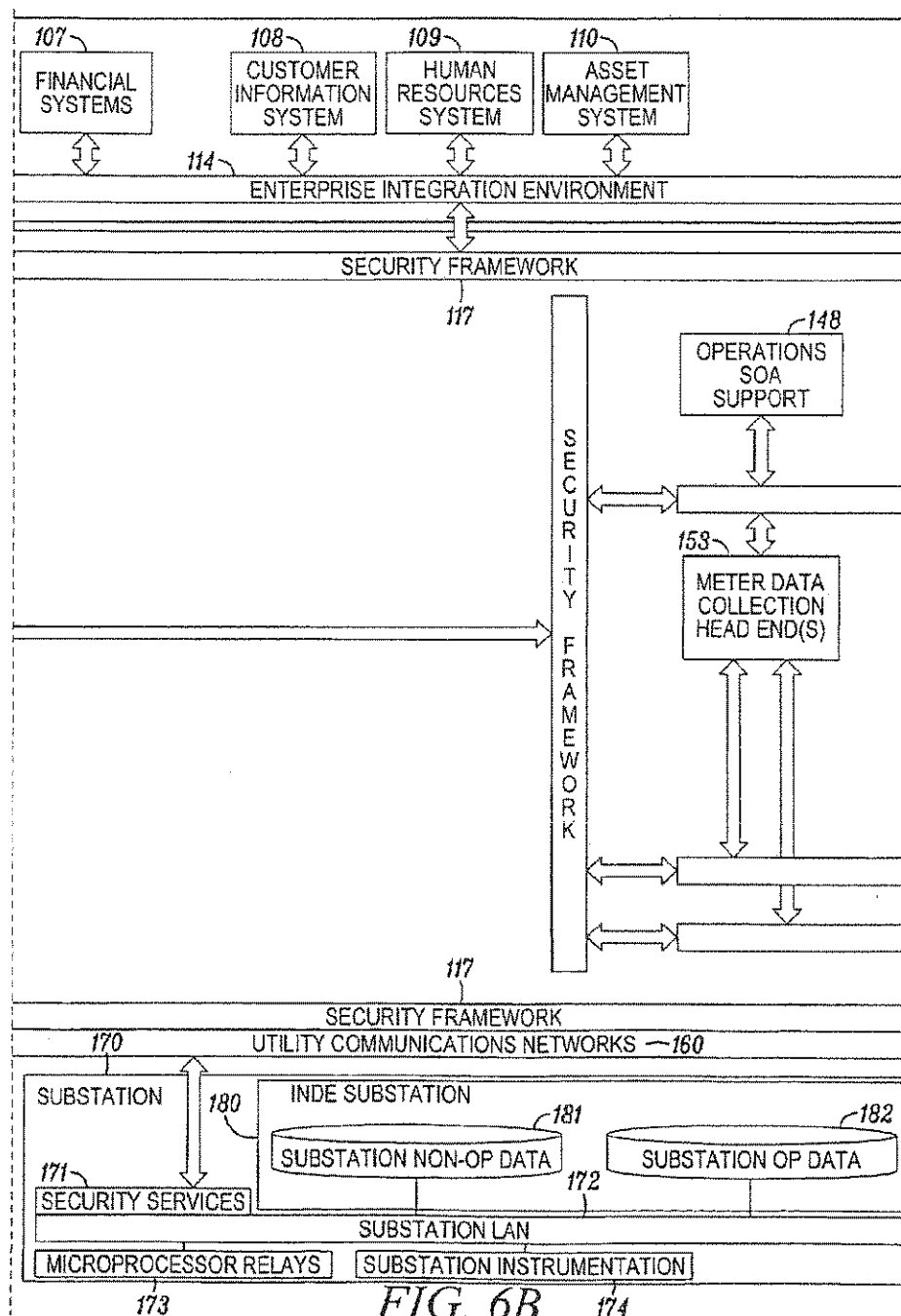


FIG. 5B

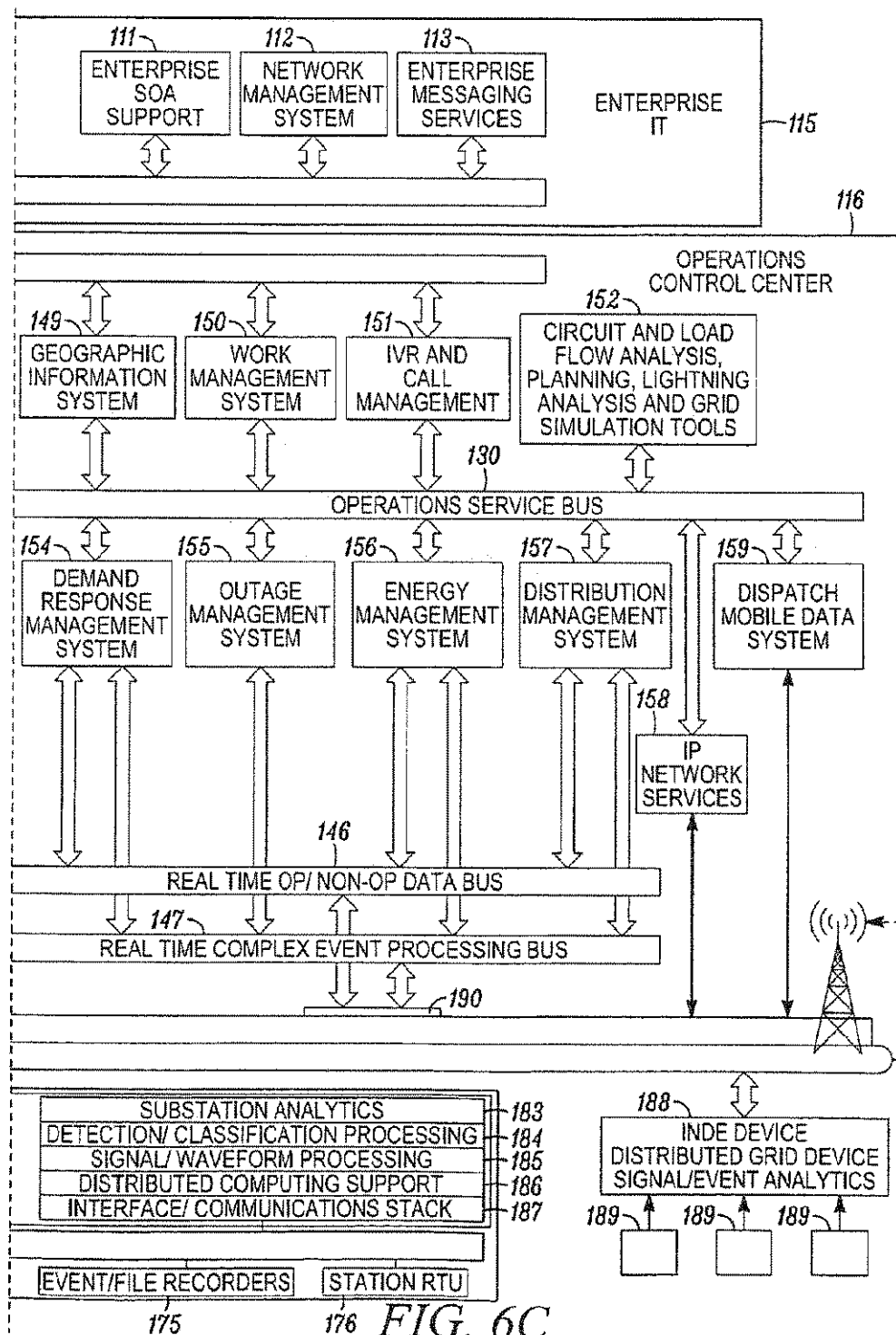
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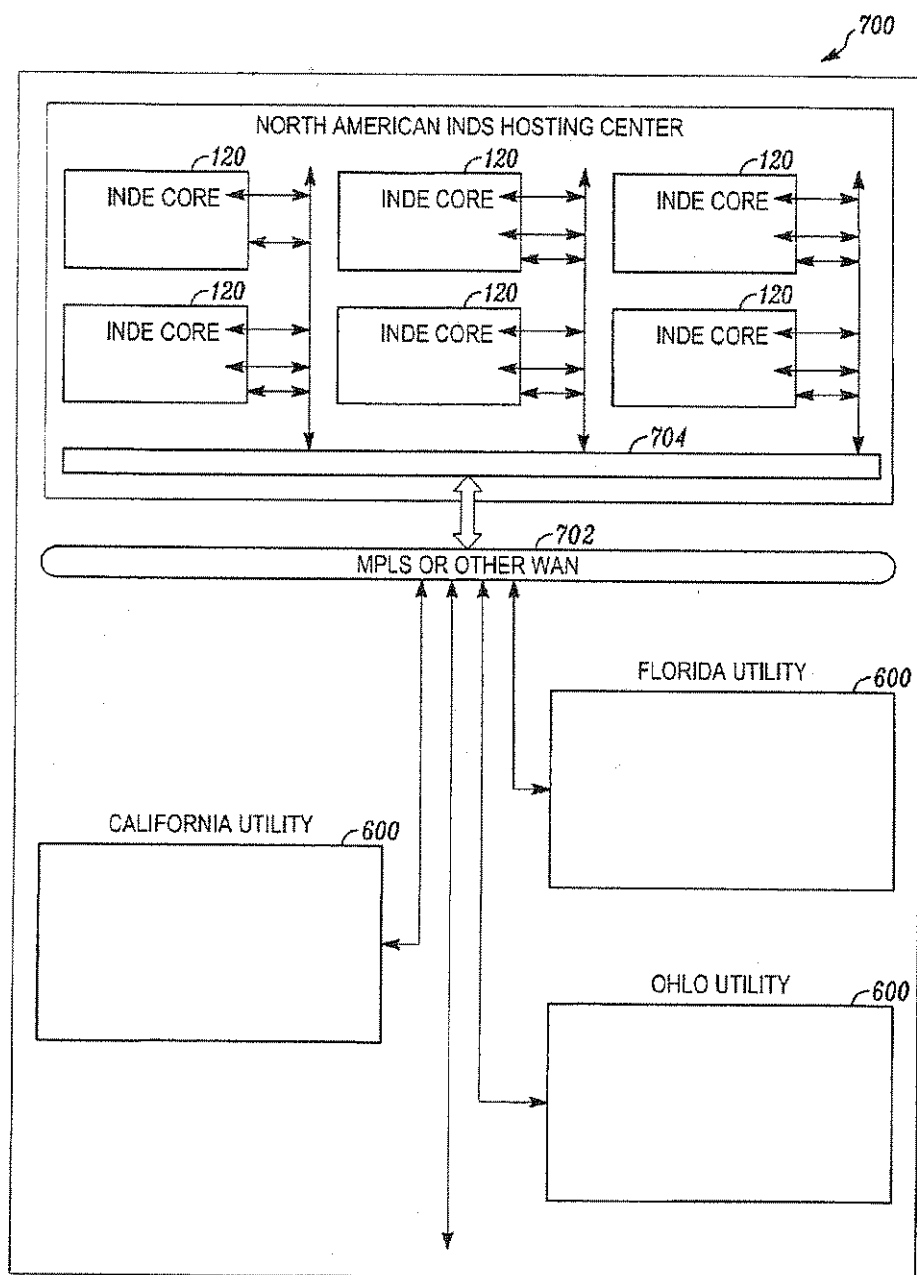


FIG. 7

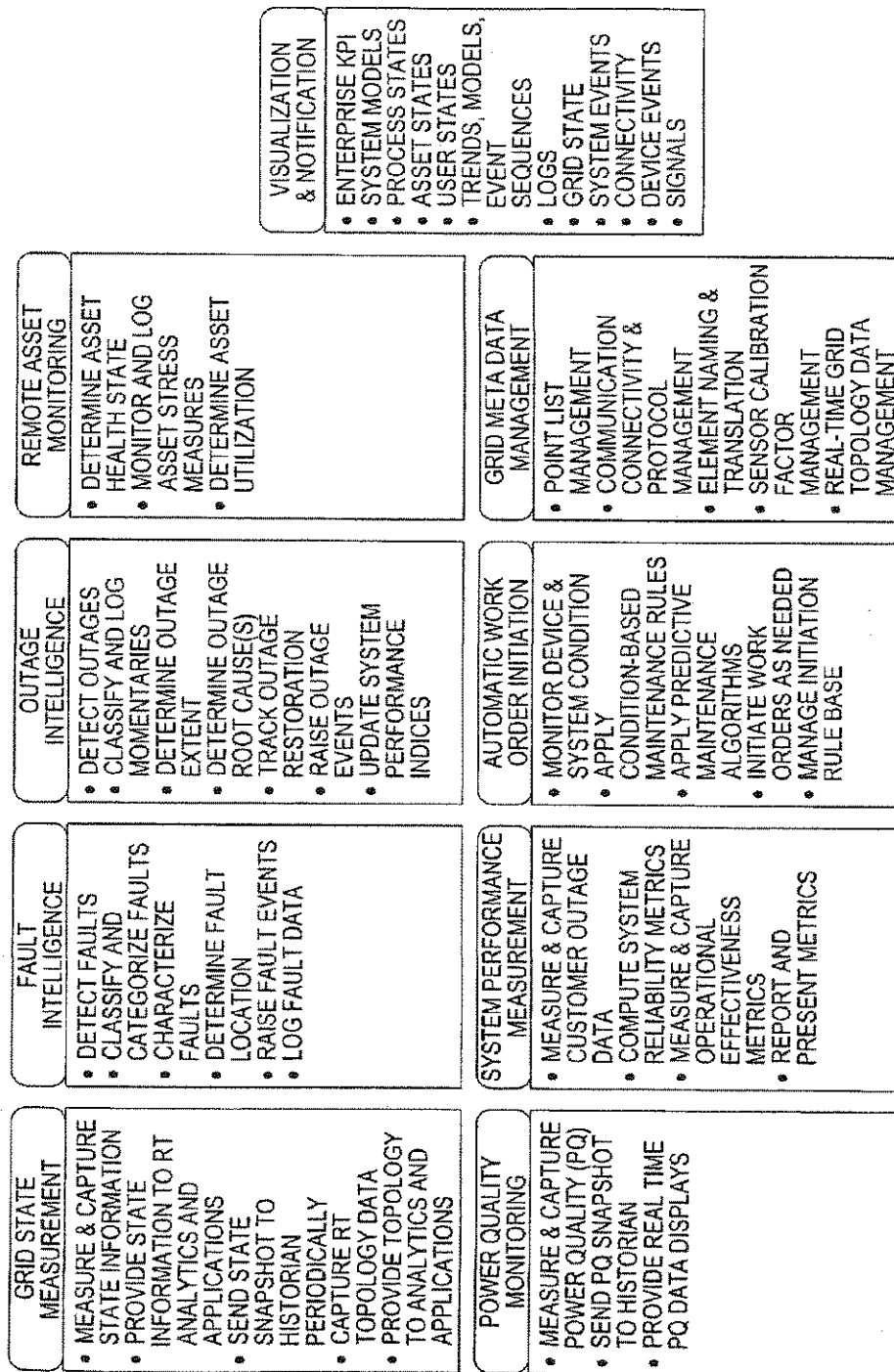
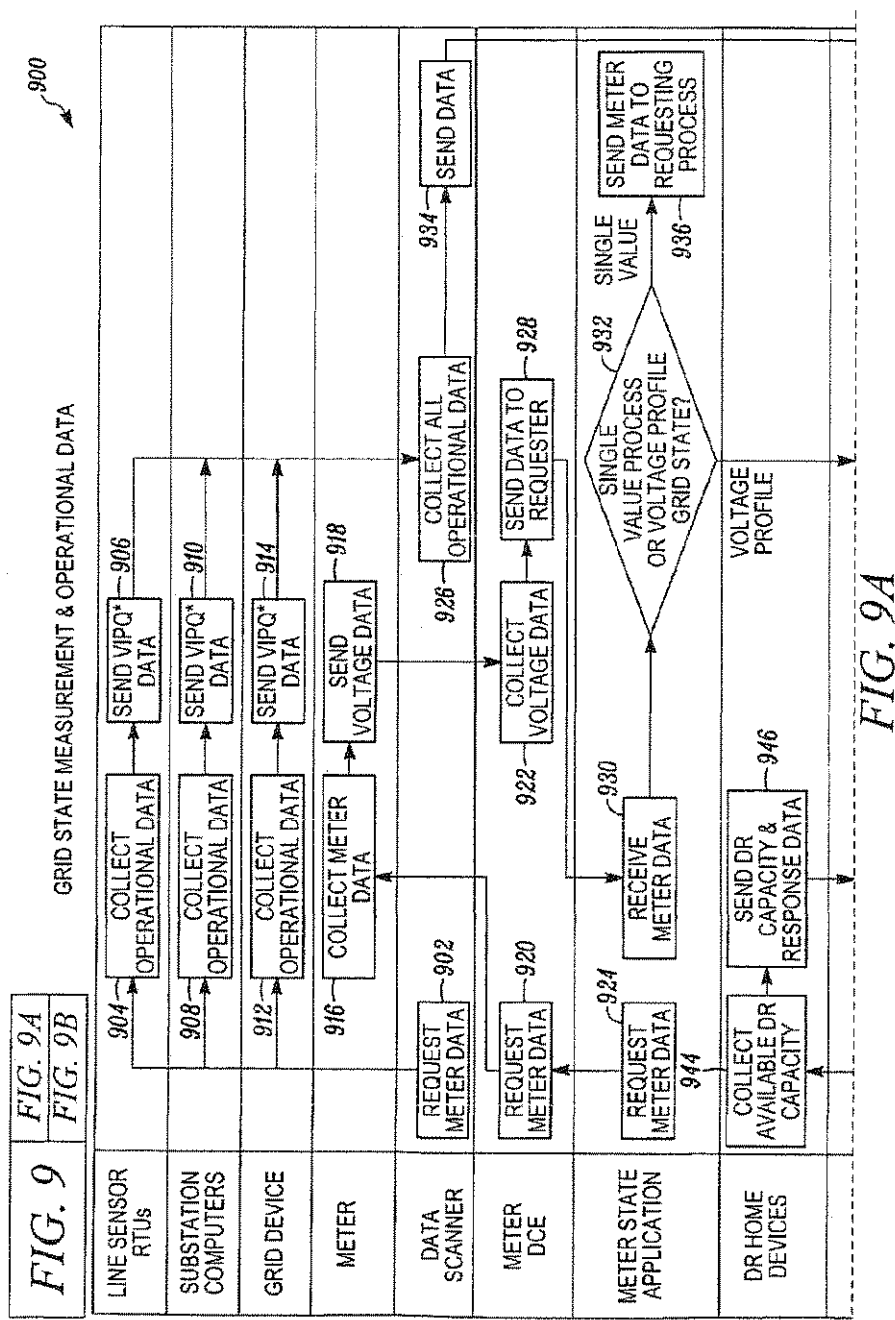
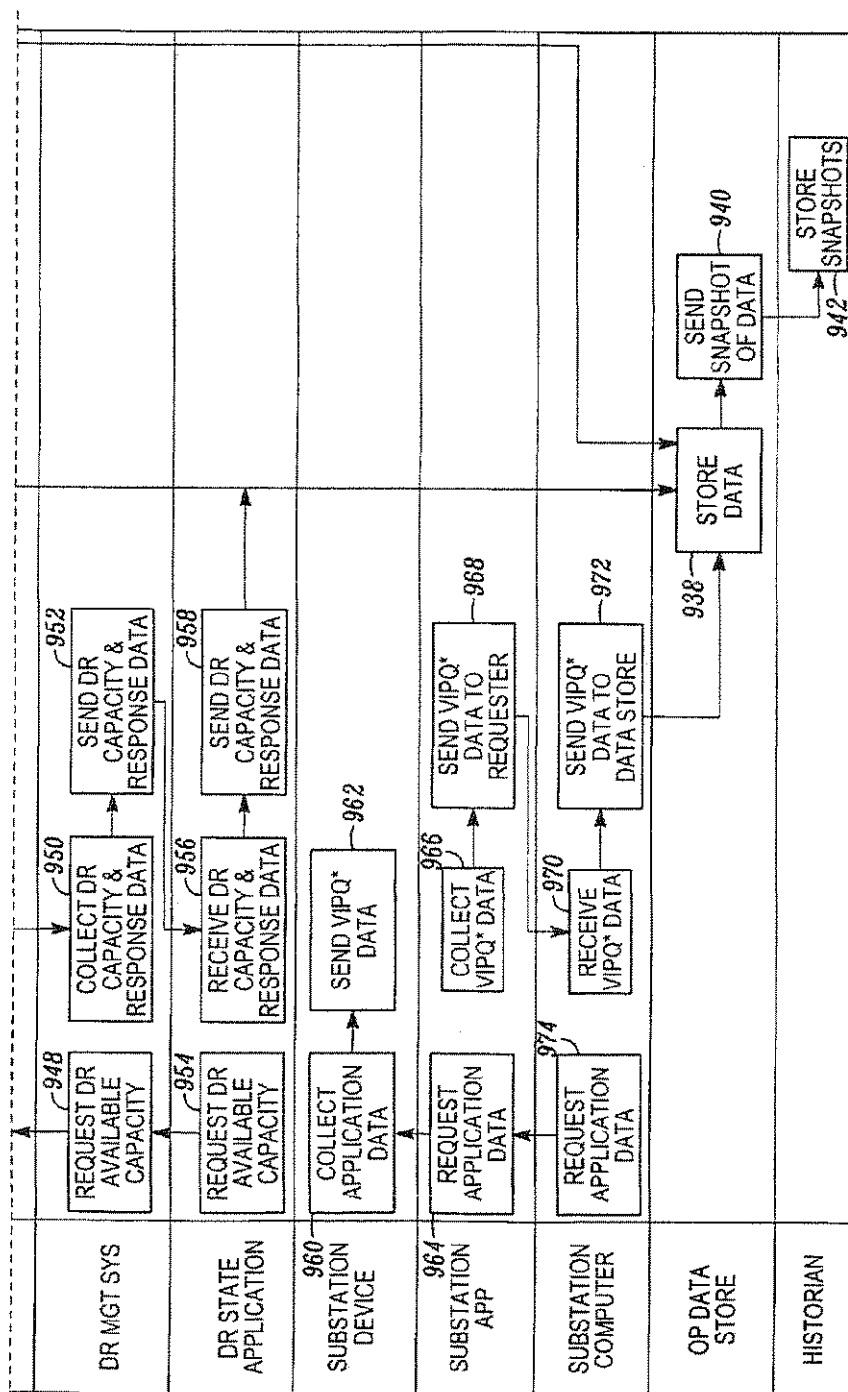


FIG. 8





* V = VOLTAGE, I = CURRENT, P = REAL POWER, Q = REACTIVE POWER

FIG. 9B

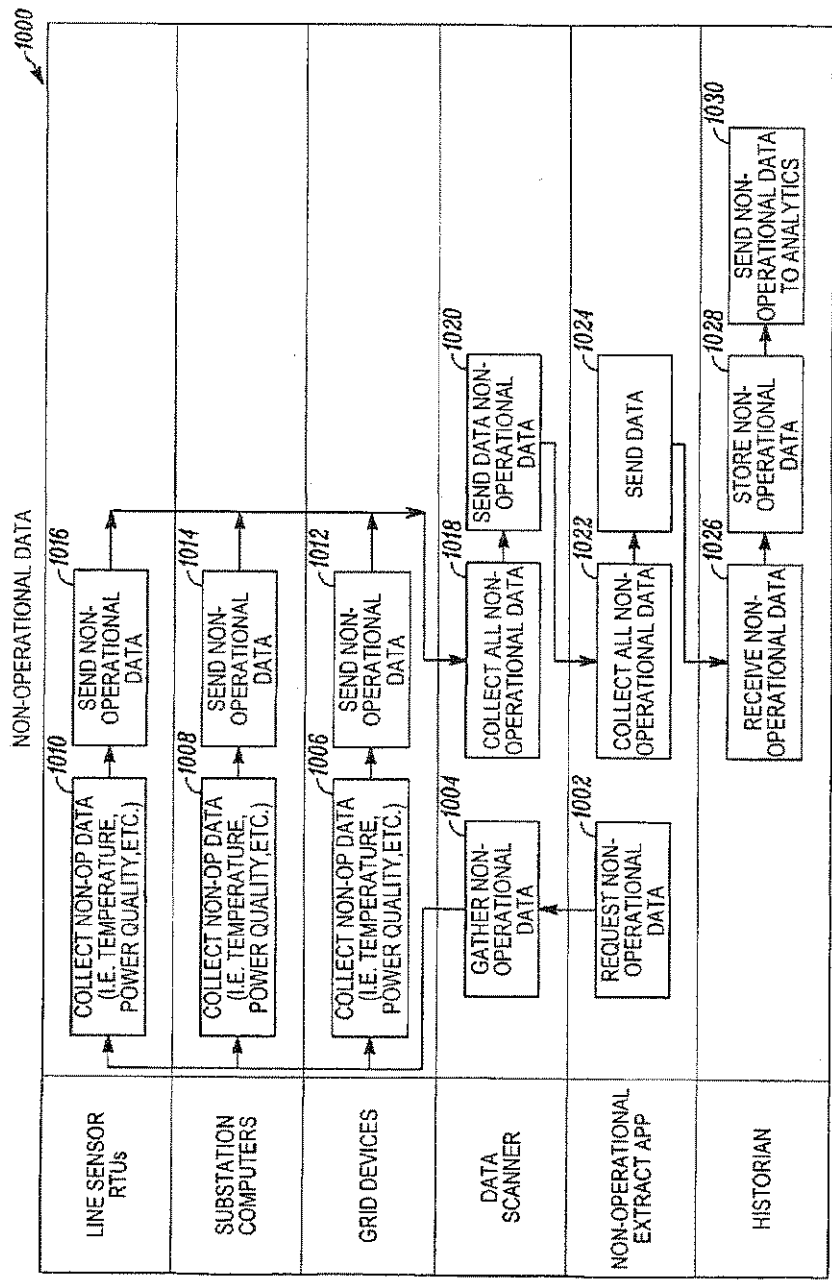


FIG. 10

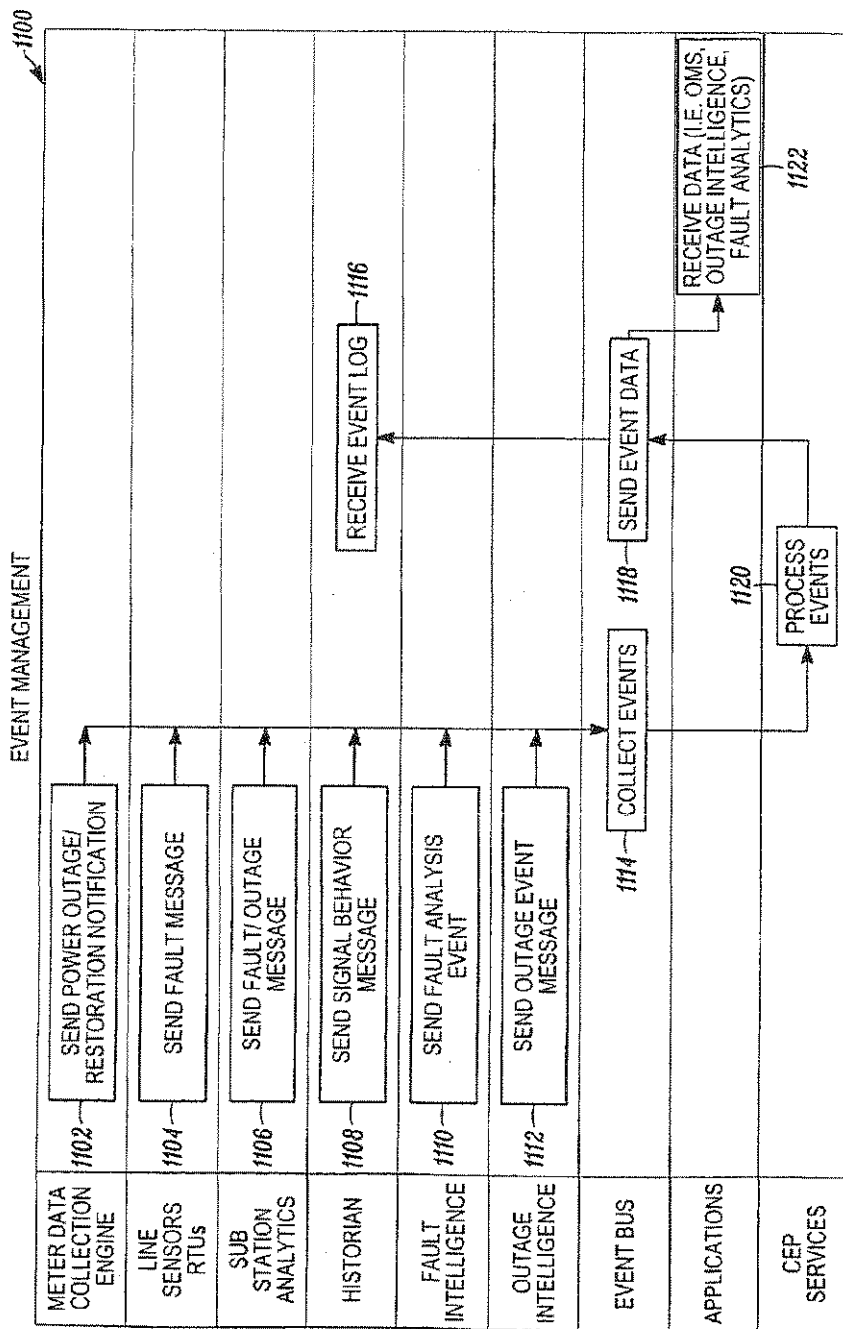
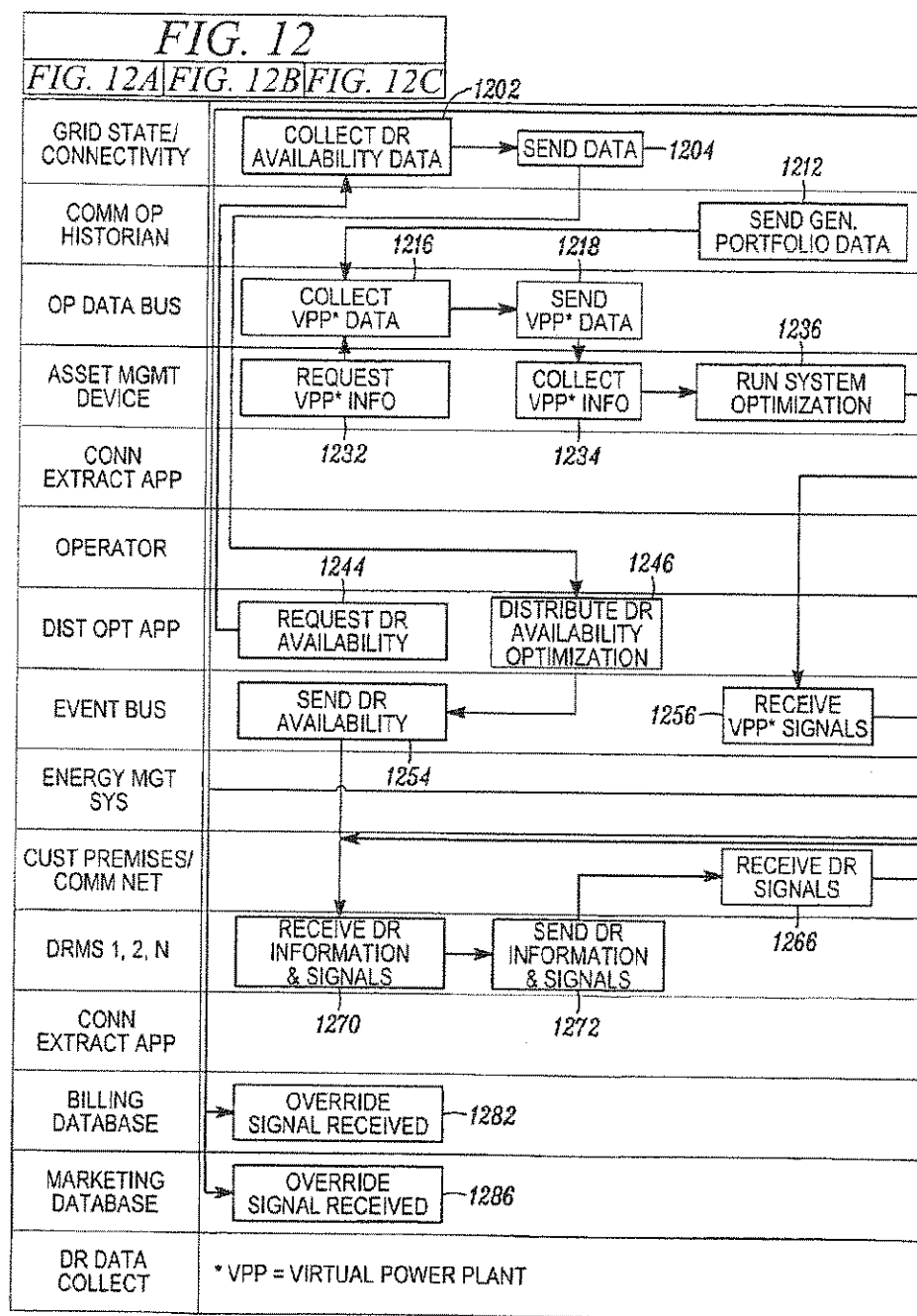


FIG. 11

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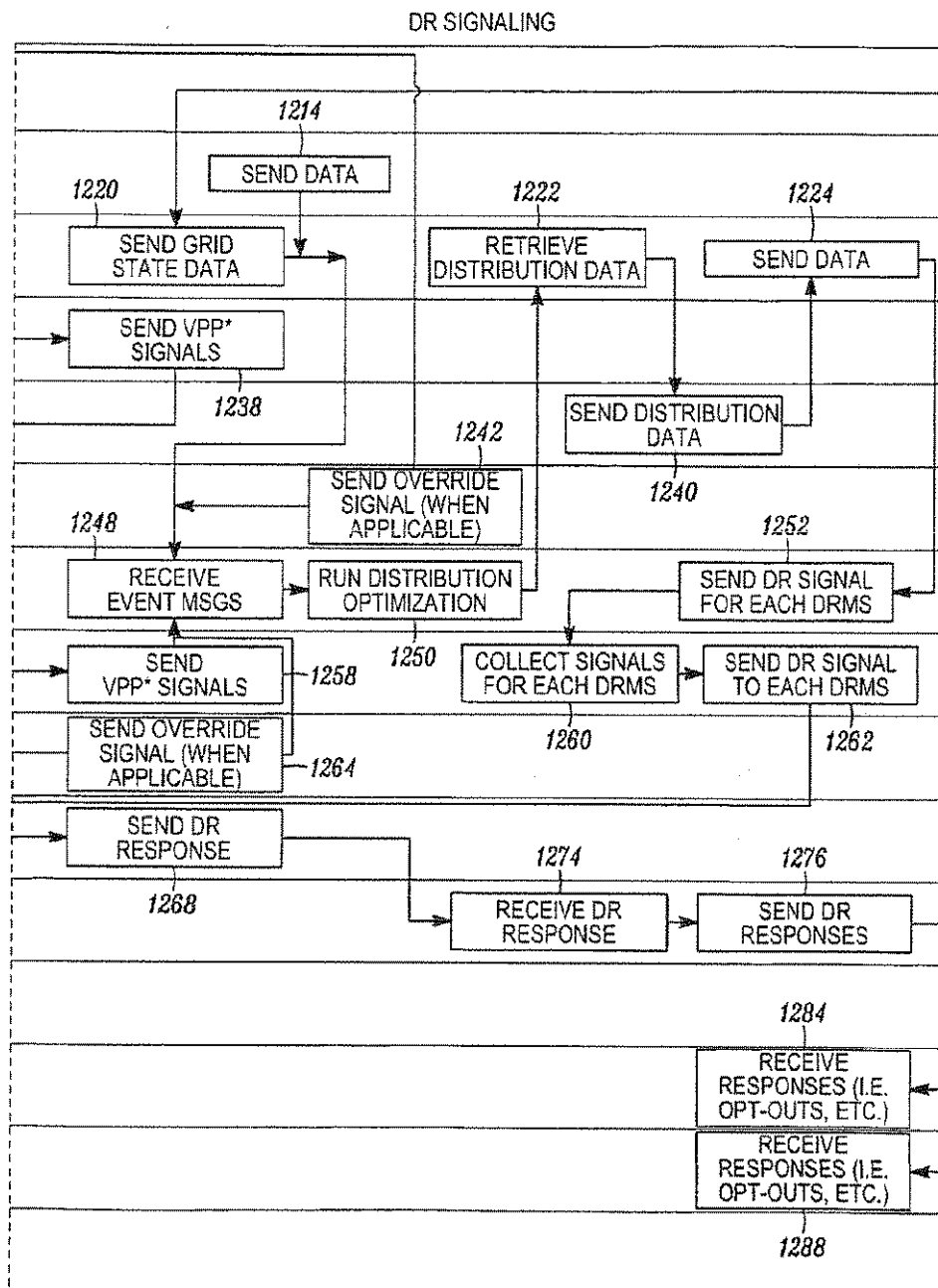


FIG. 12B

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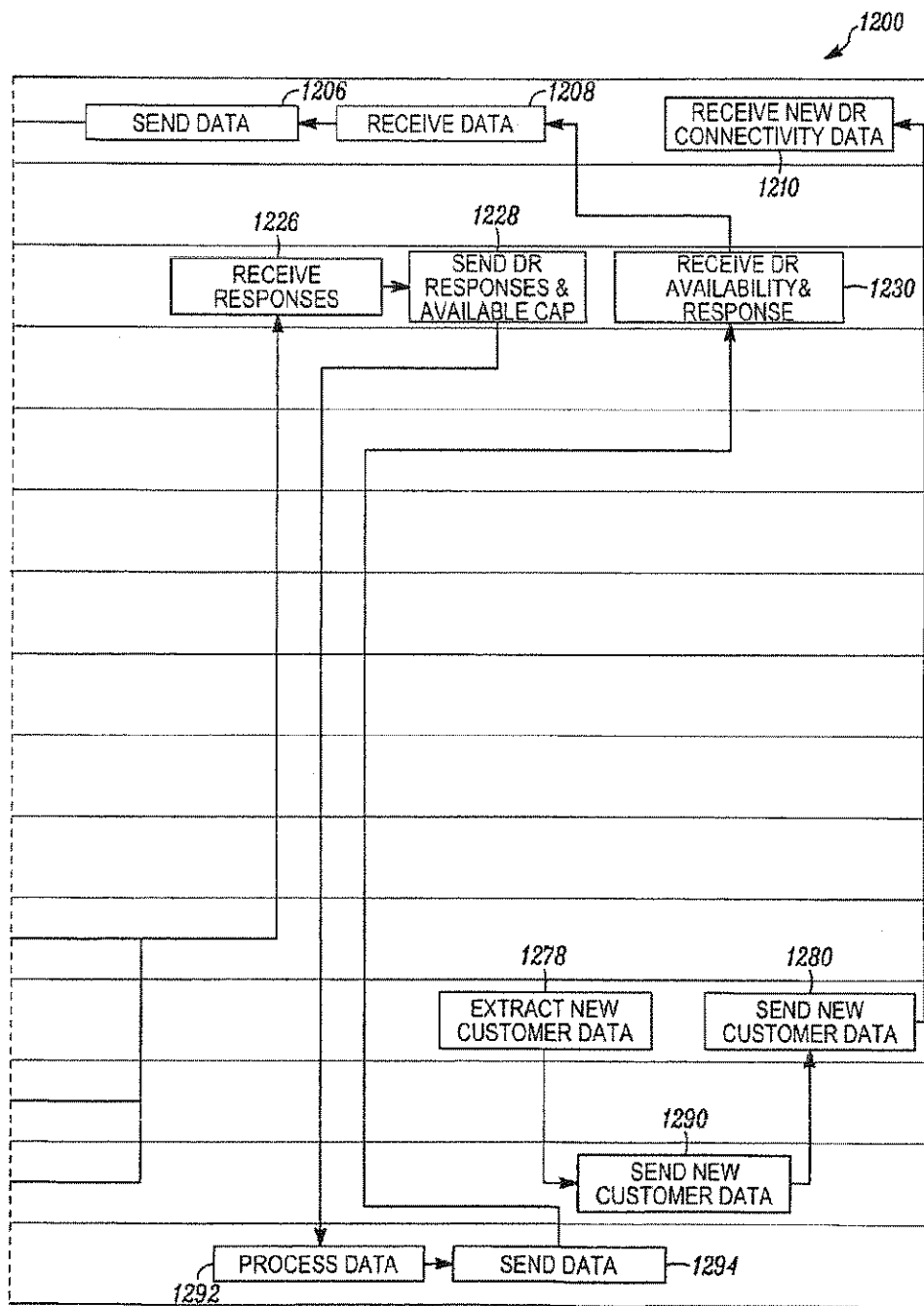


FIG. 12C

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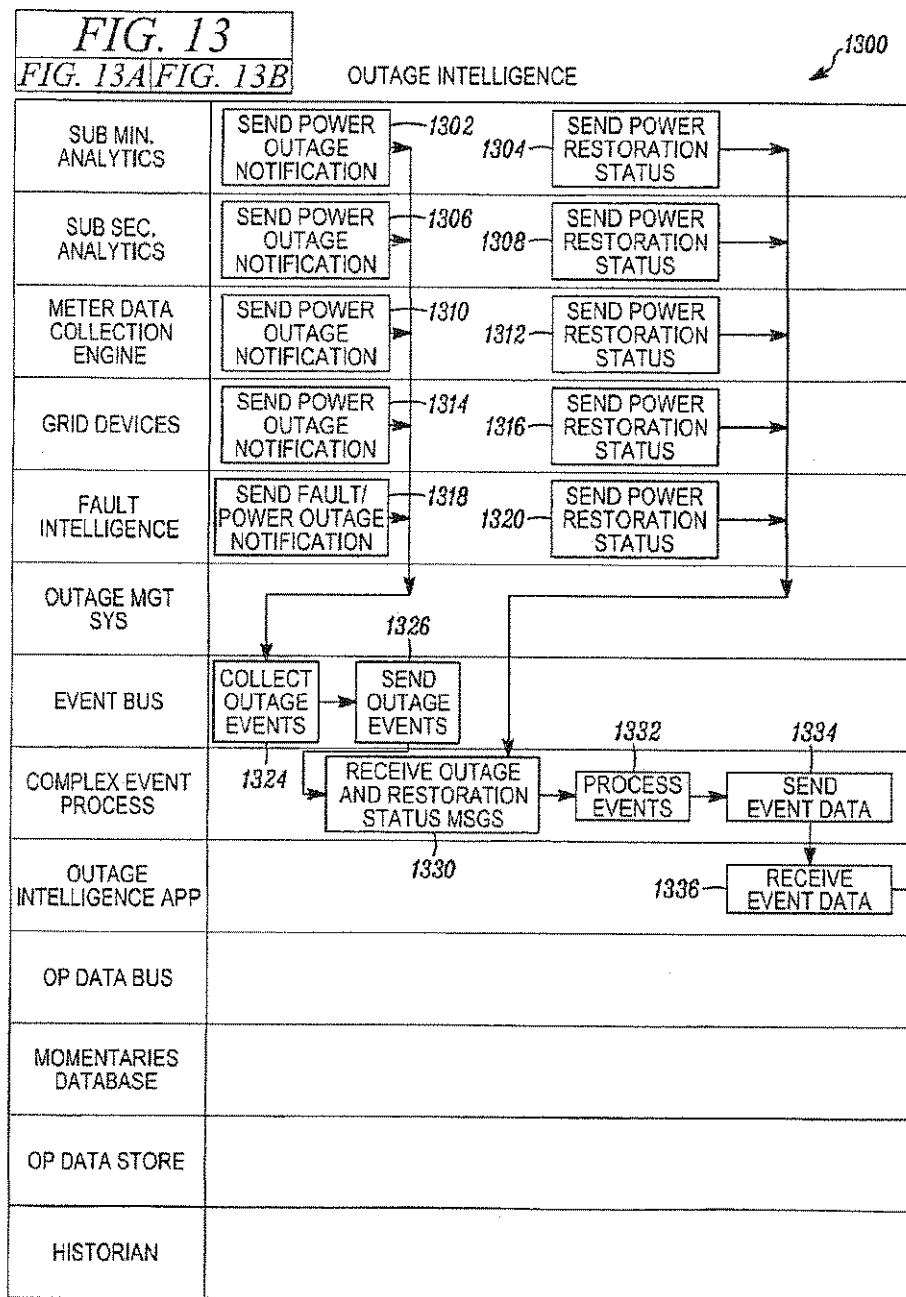


FIG. 13A

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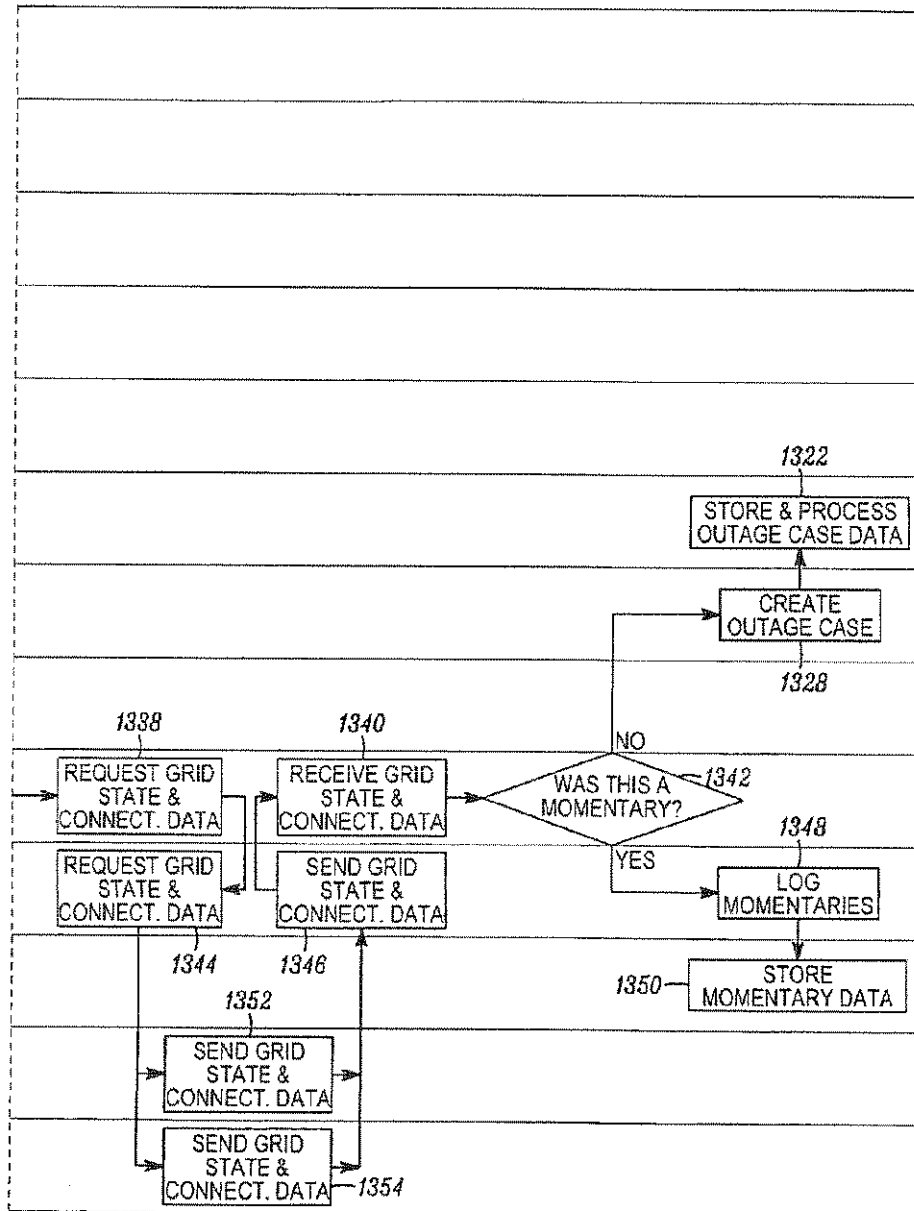


FIG. 13B

FAULT INTELLIGENCE

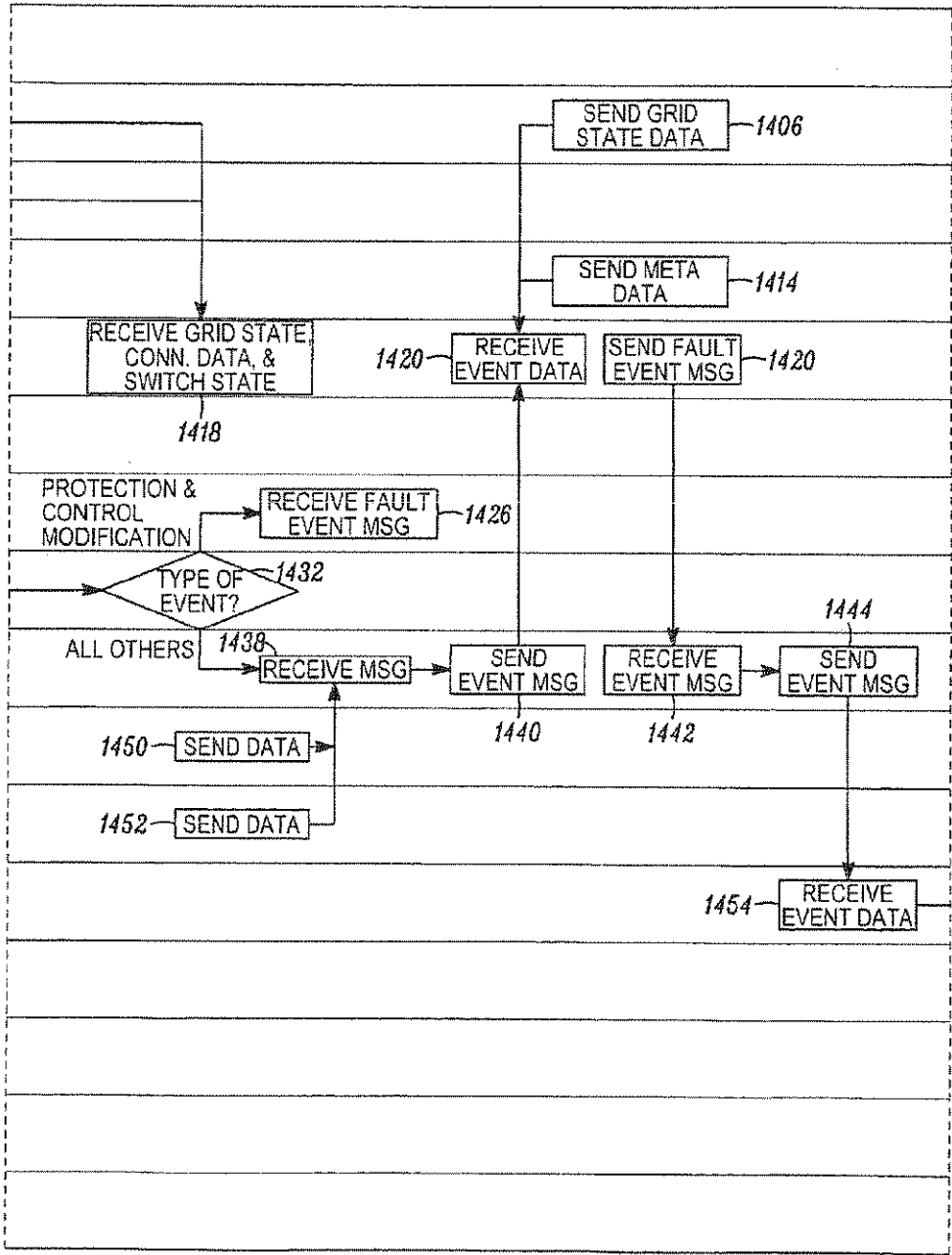


FIG. 14B

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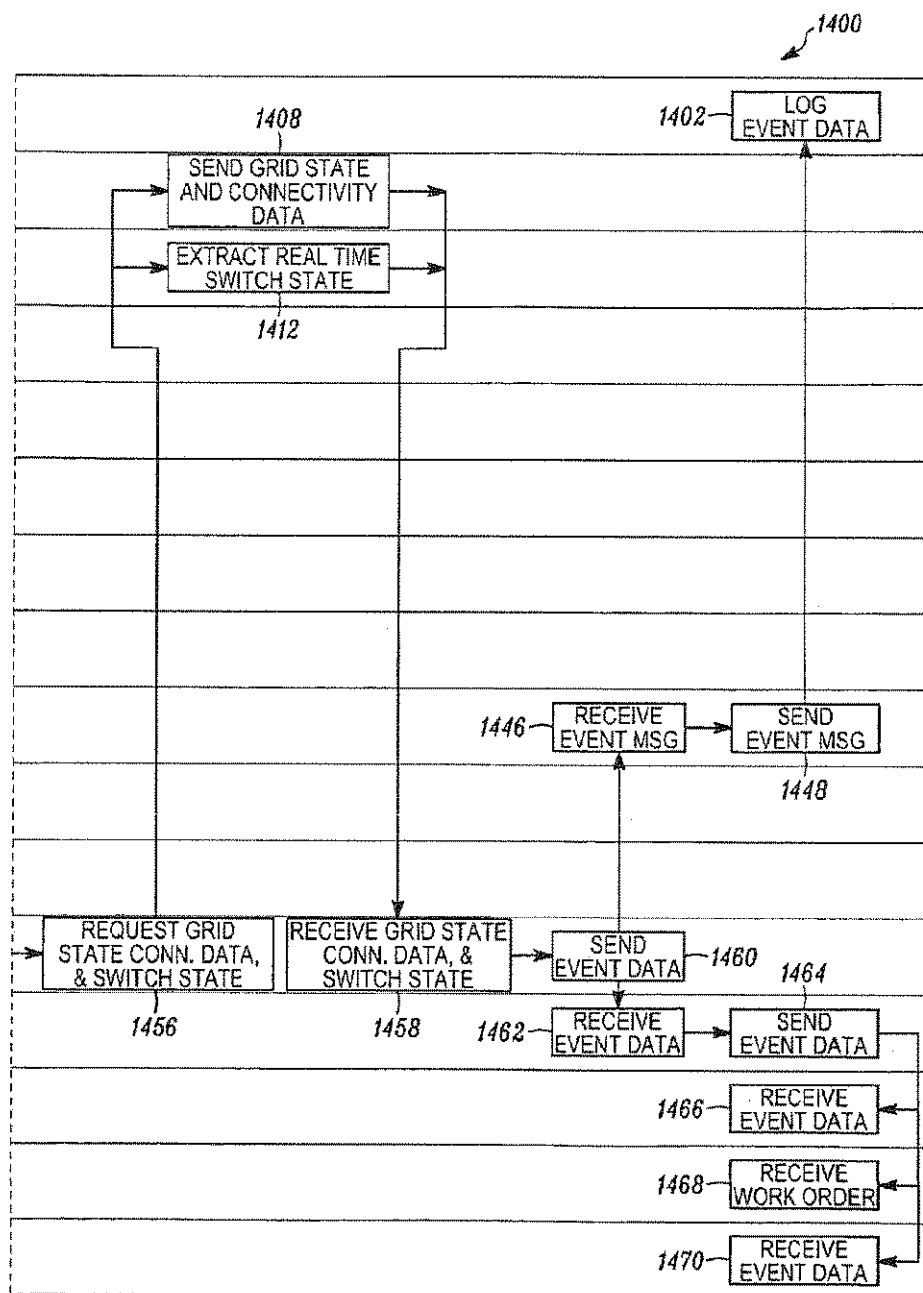
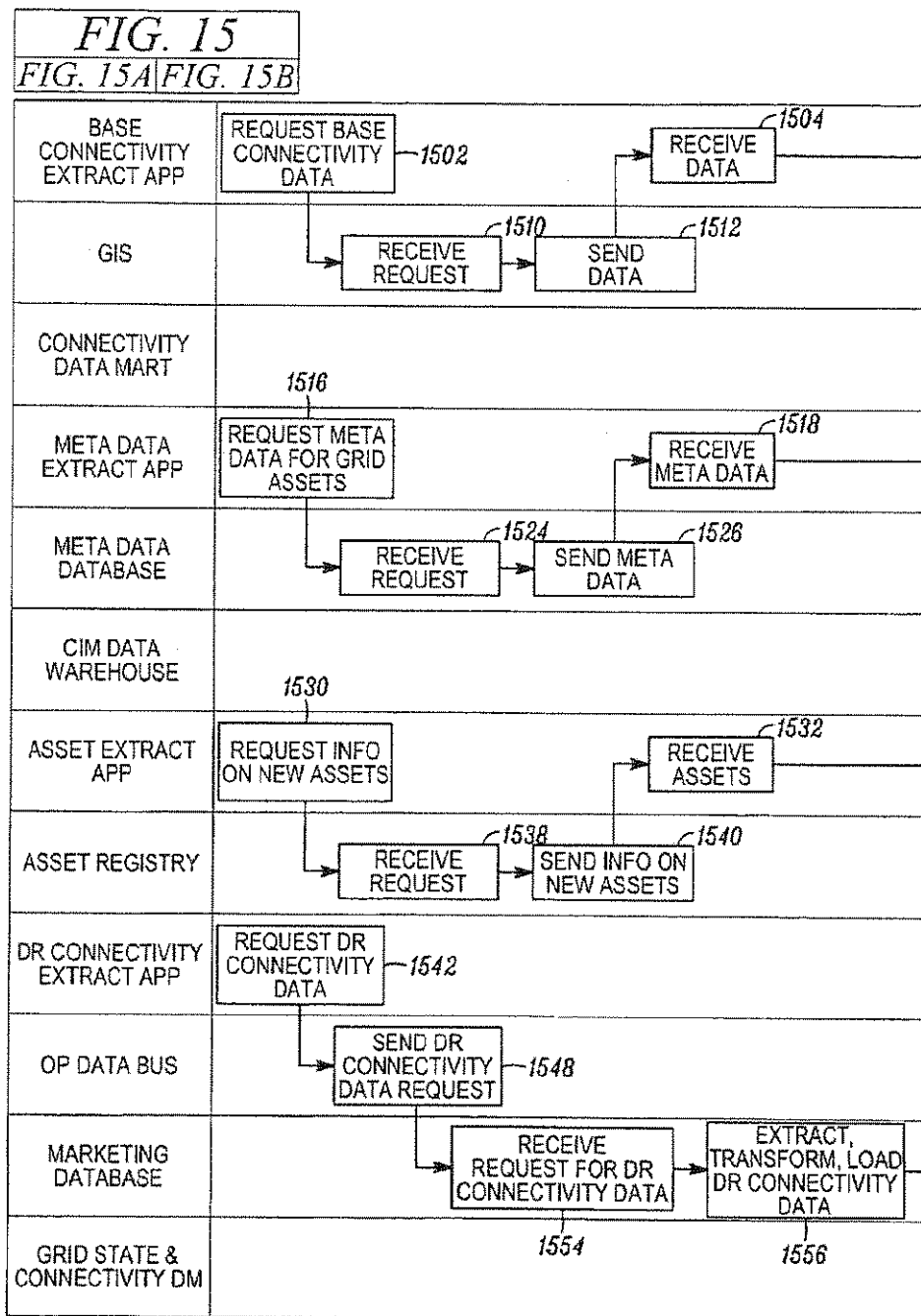


FIG. 14C

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**FIG. 15A**

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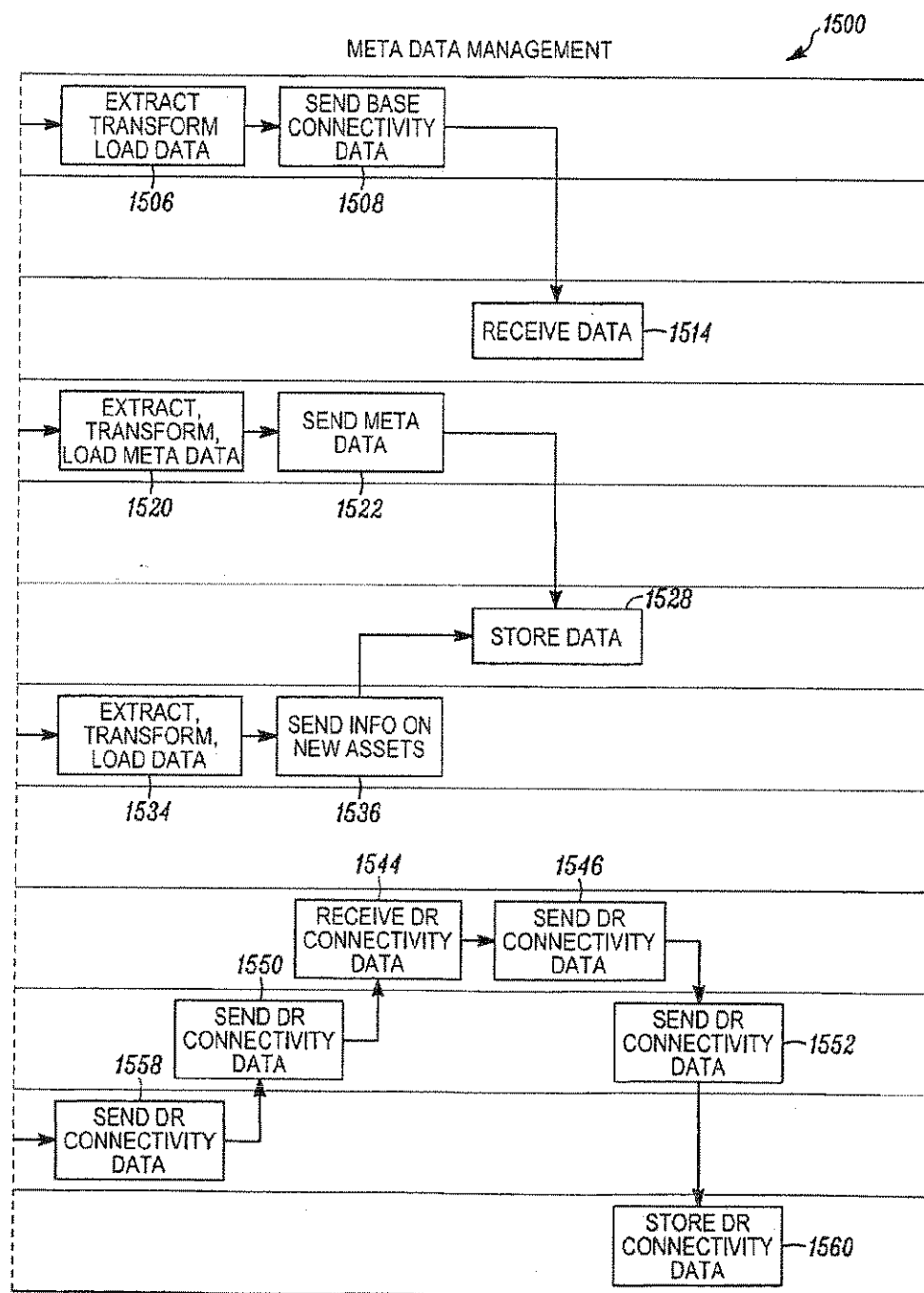


FIG. 15B

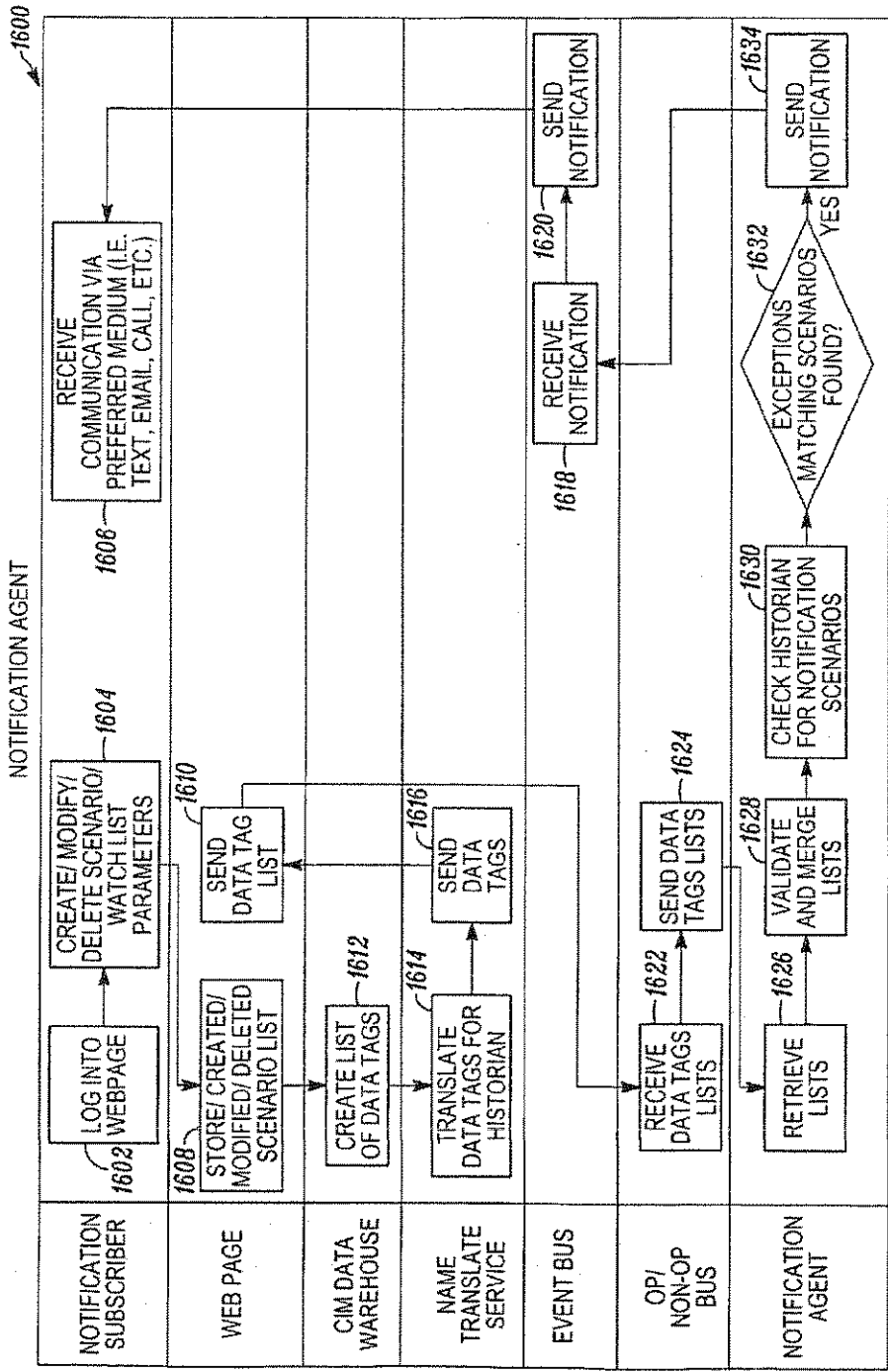


FIG. 16

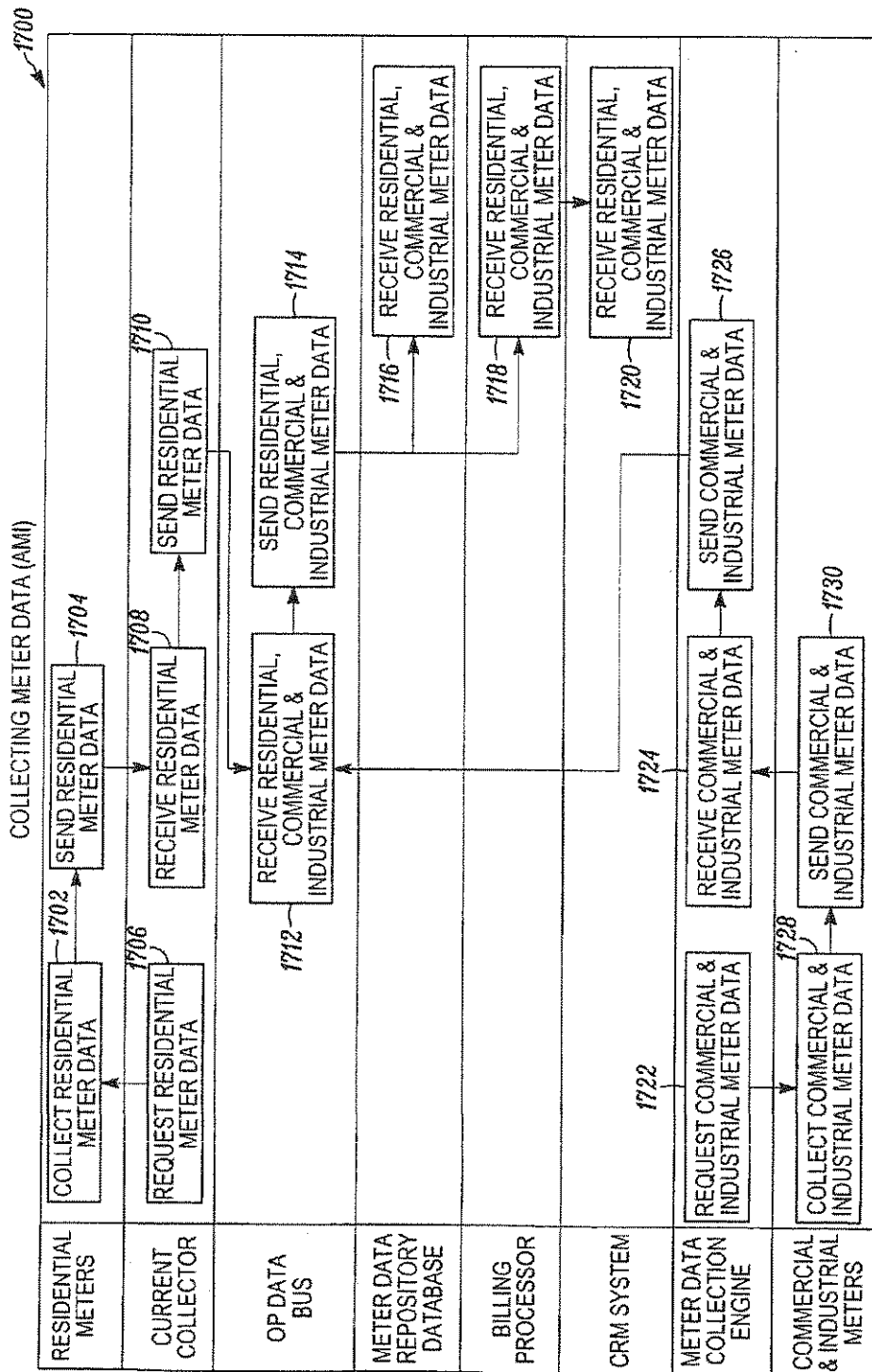
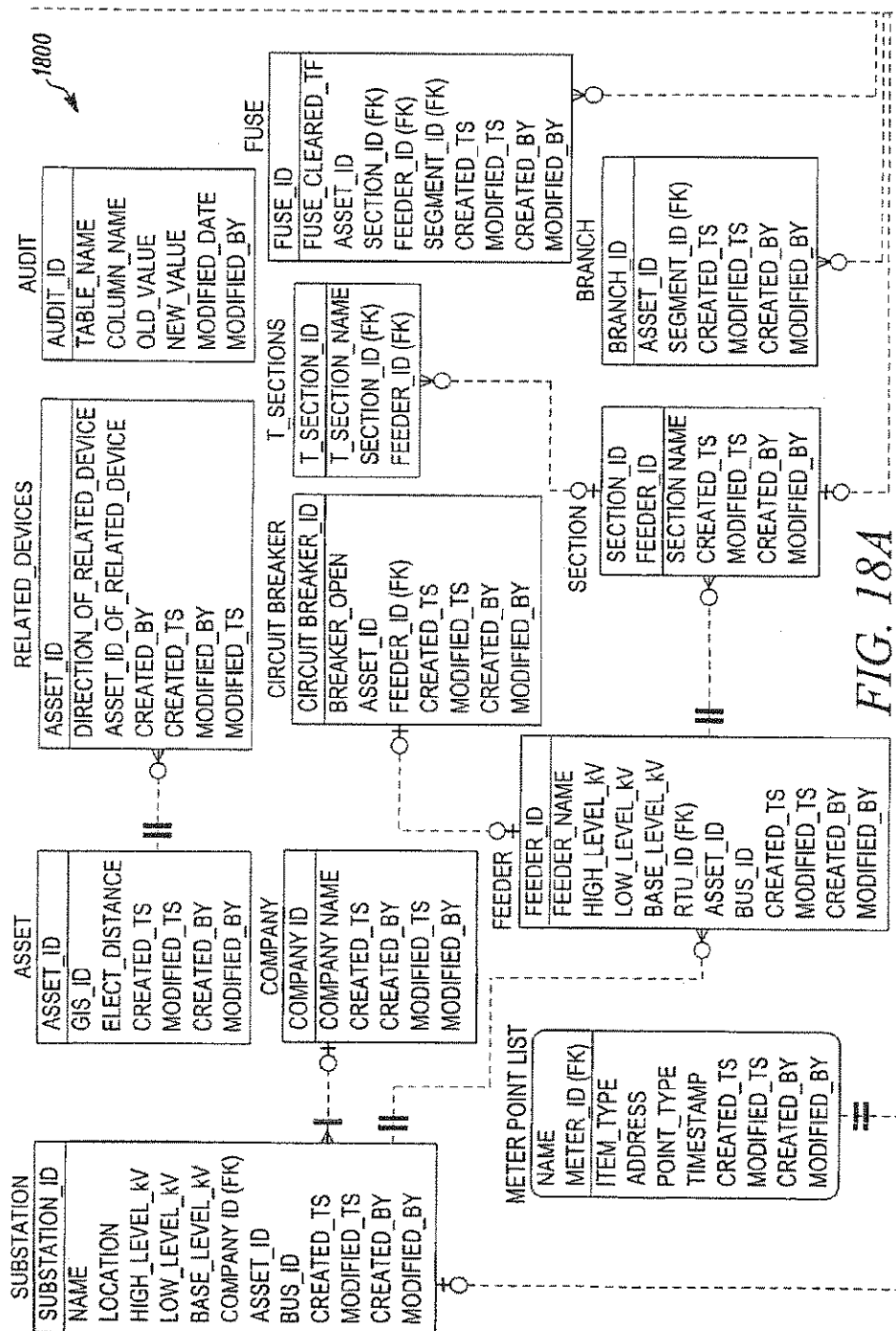
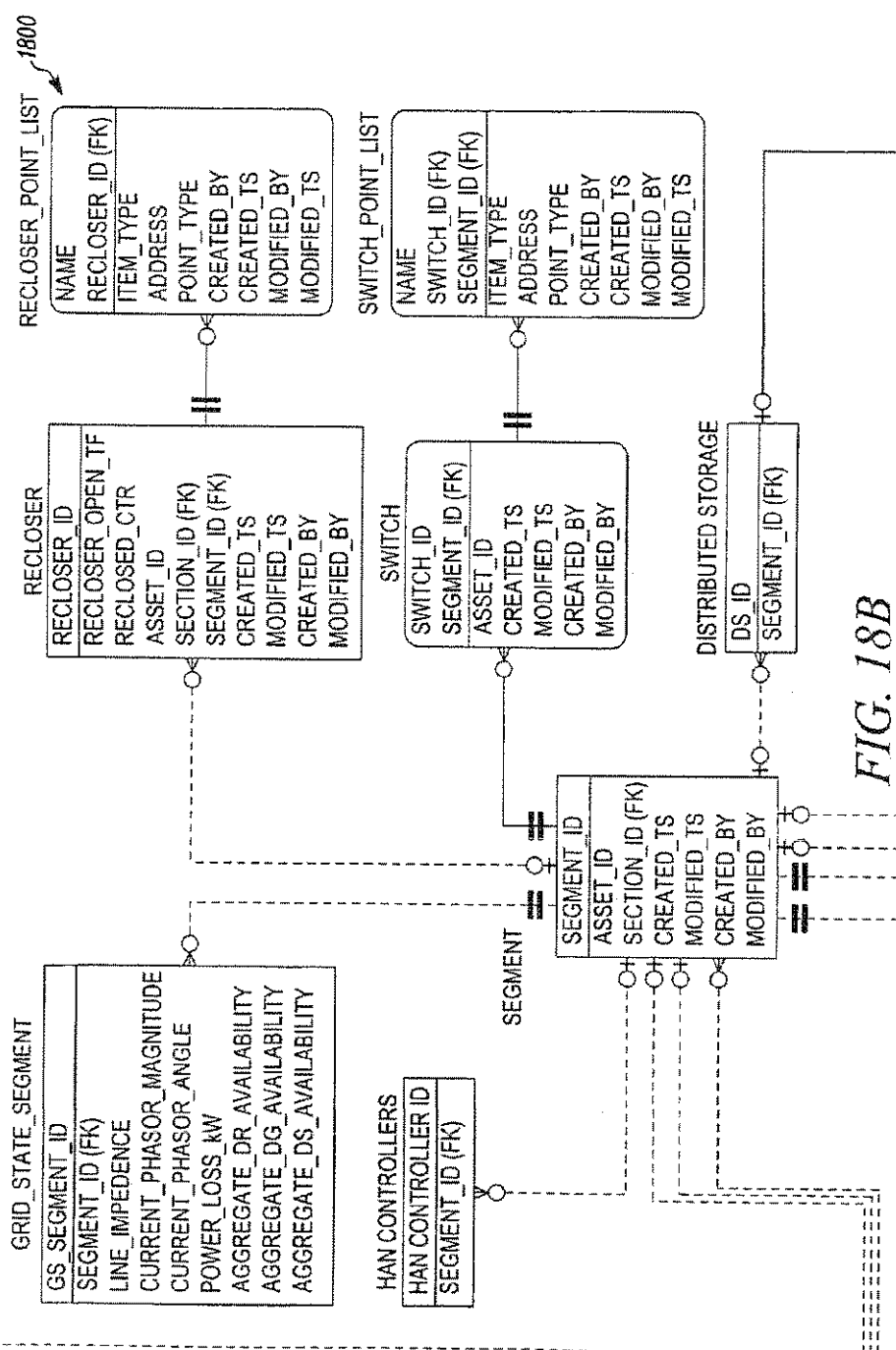


FIG. 17



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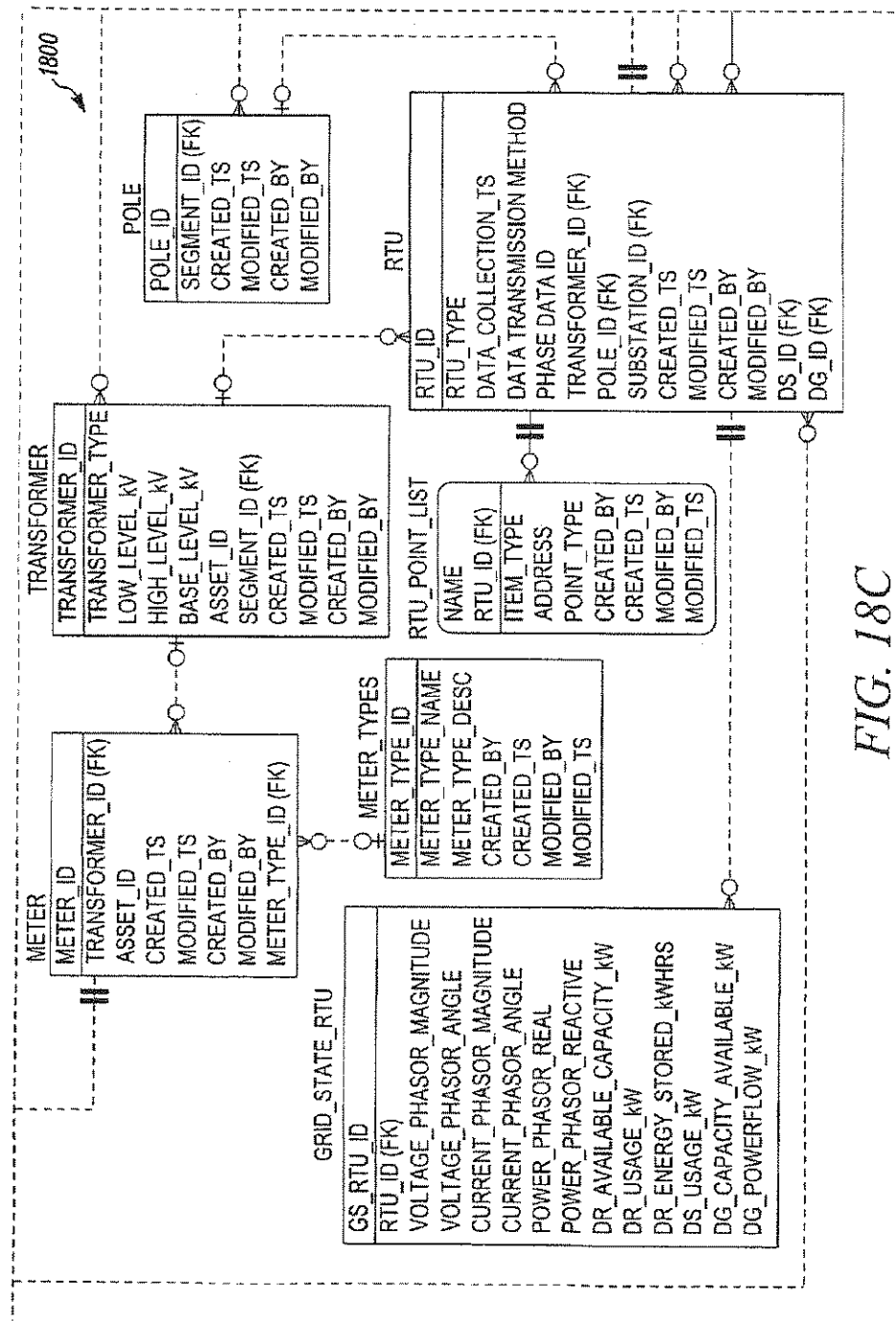


FIG. 18C

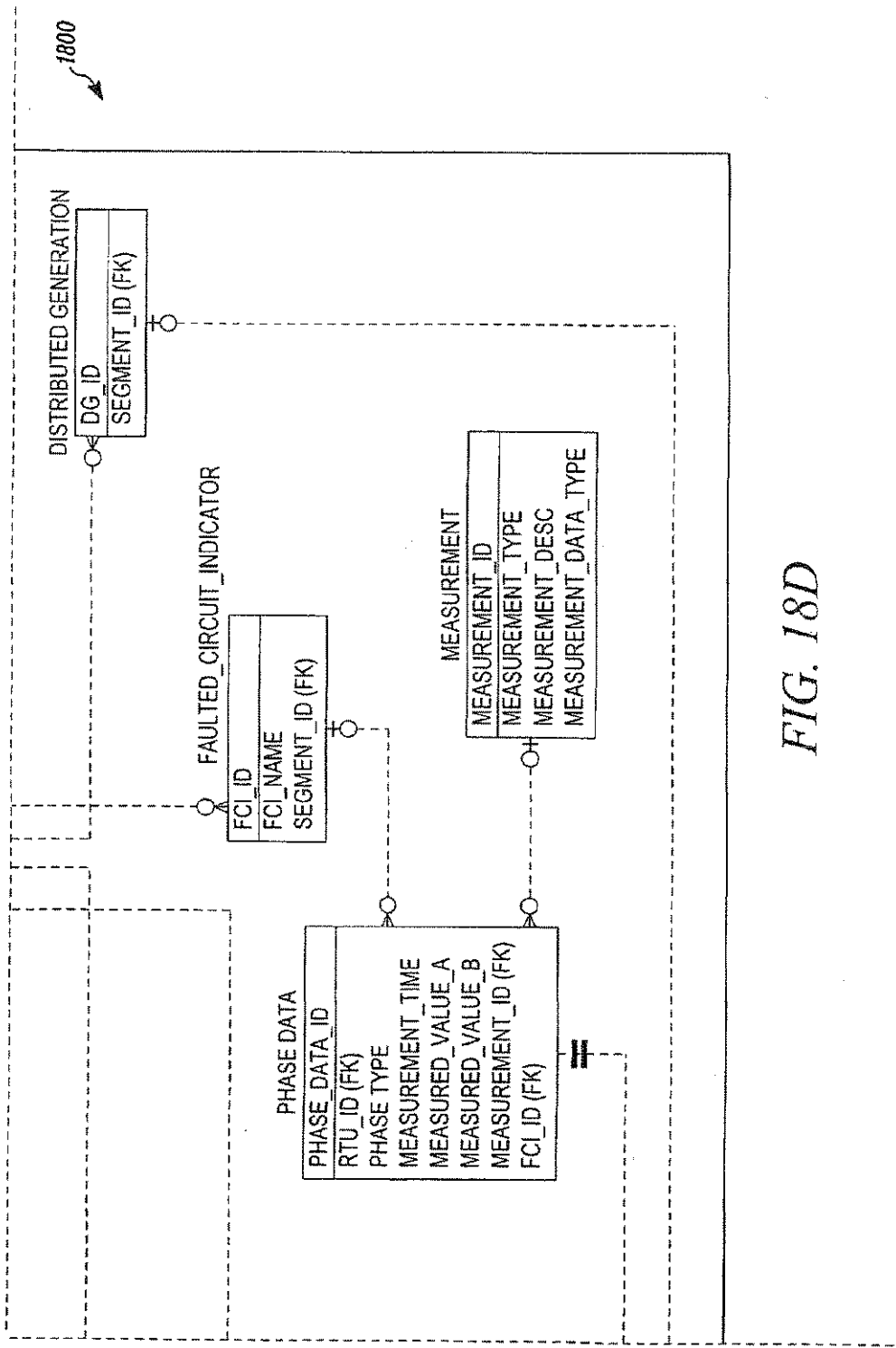
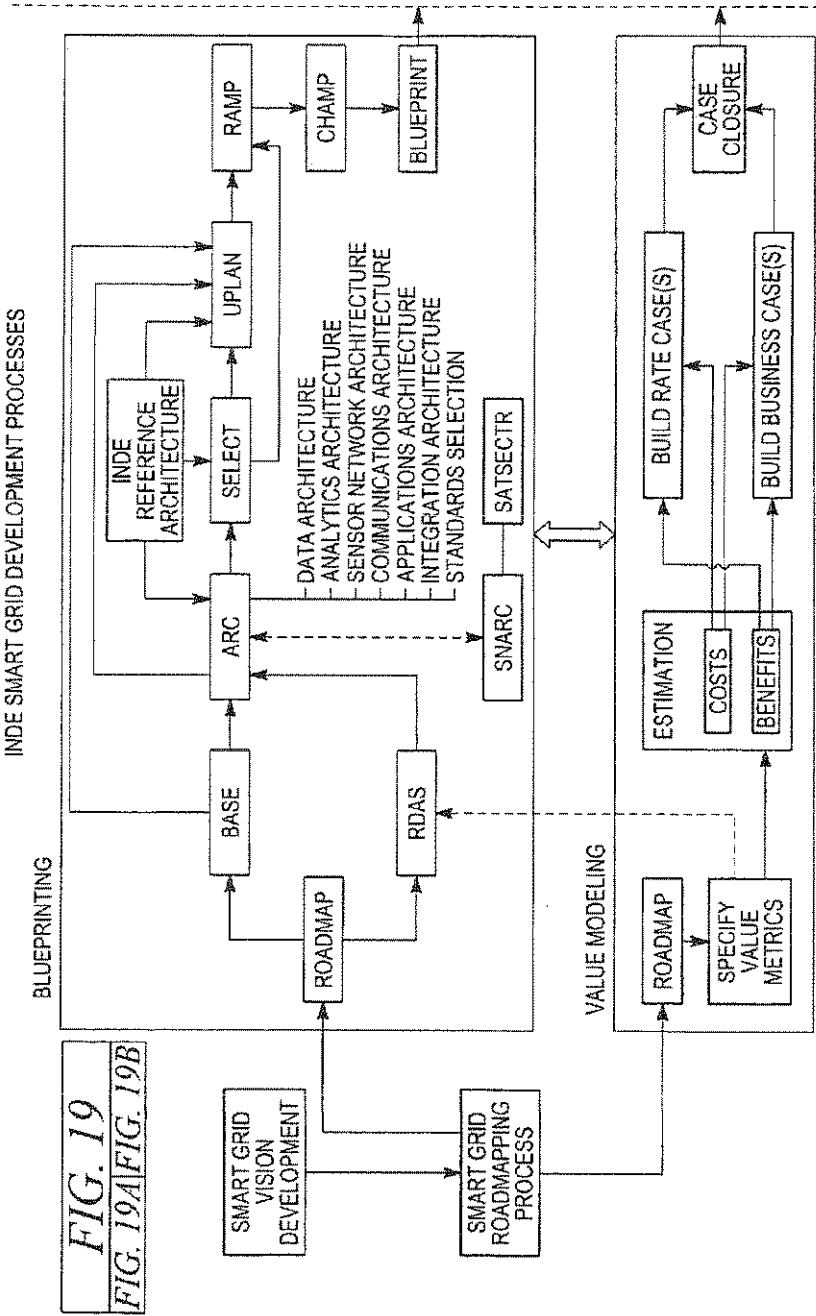


FIG. 18D



BLUEPRINT PROCESS LIST	
BASE	BASELINE AND SYSTEMS EVALUATION
RDAS	REQUIREMENTS DEFINITION AND ANALYTICS SELECTION
ARC	ARCHITECTURE CONFIGURATION
SELECT	SOLUTION ELEMENT EVALUATION AND COMPONENTS TEMPLATE
UPLAN	UPGRADE PLANNING FOR APPLICATIONS AND NETWORKS
RAMP	RISK ASSESSMENT AND MANAGEMENT PLANNING
CHAMP	CHANGE ANALYSIS AND MANAGEMENT PLANNING
SNARC	SENSOR NETWORK ARCHITECTURE CONFIGURATION
SATSECTR	SENSOR ALLOCATION VIA T-SECTION RECURSION

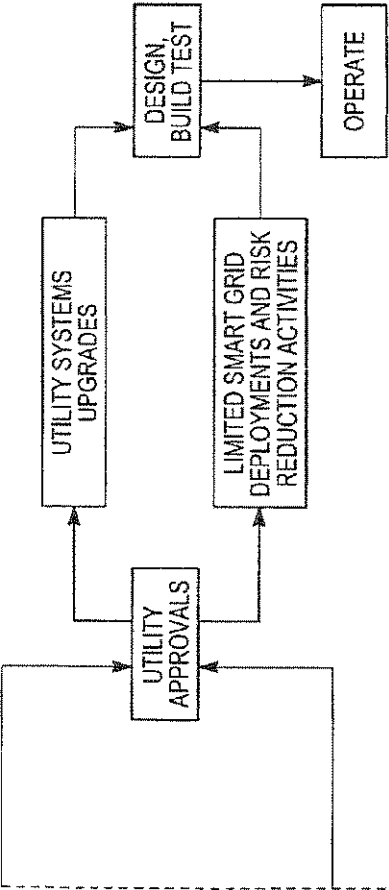


FIG. 19B

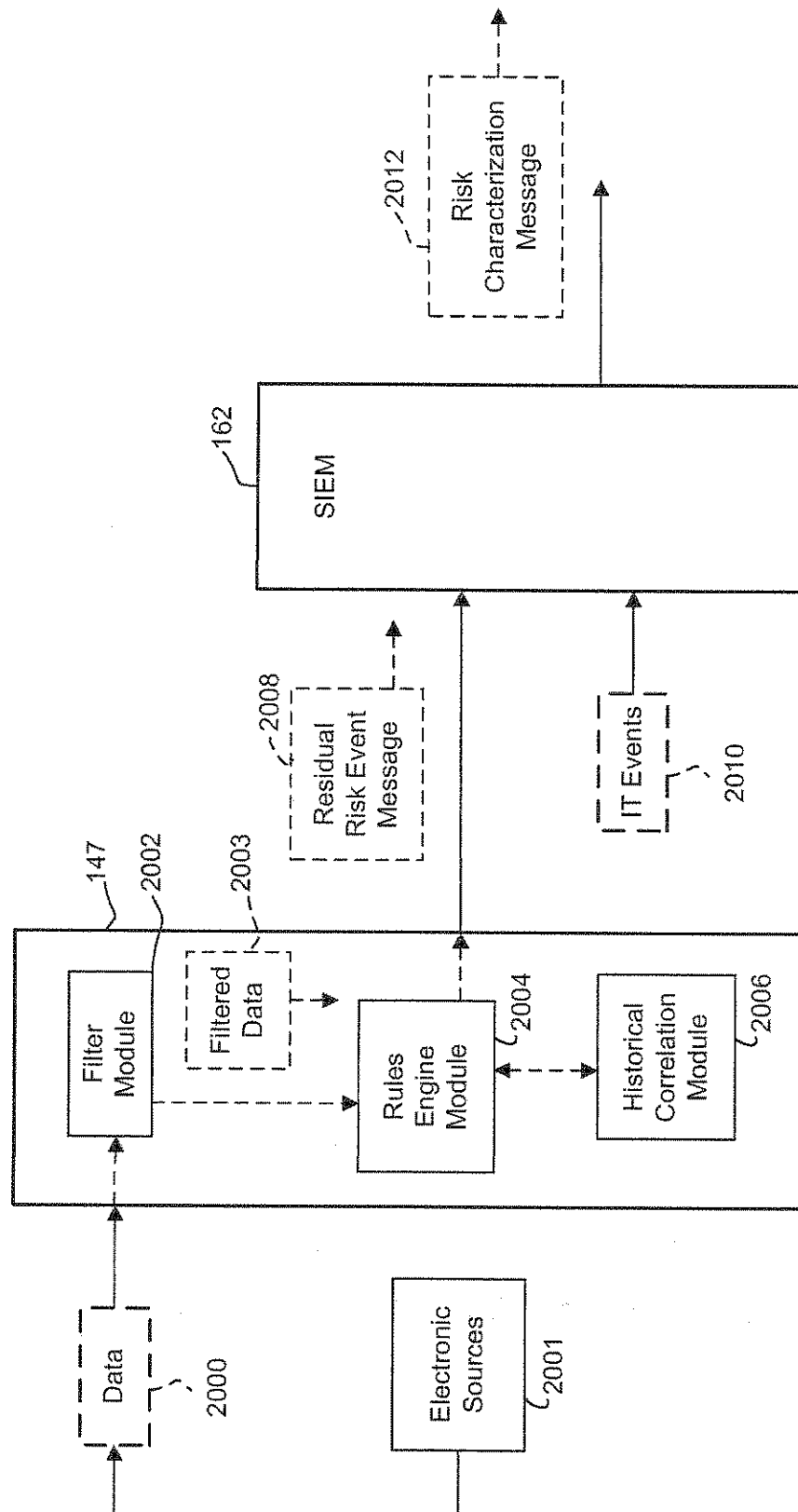


FIG. 20

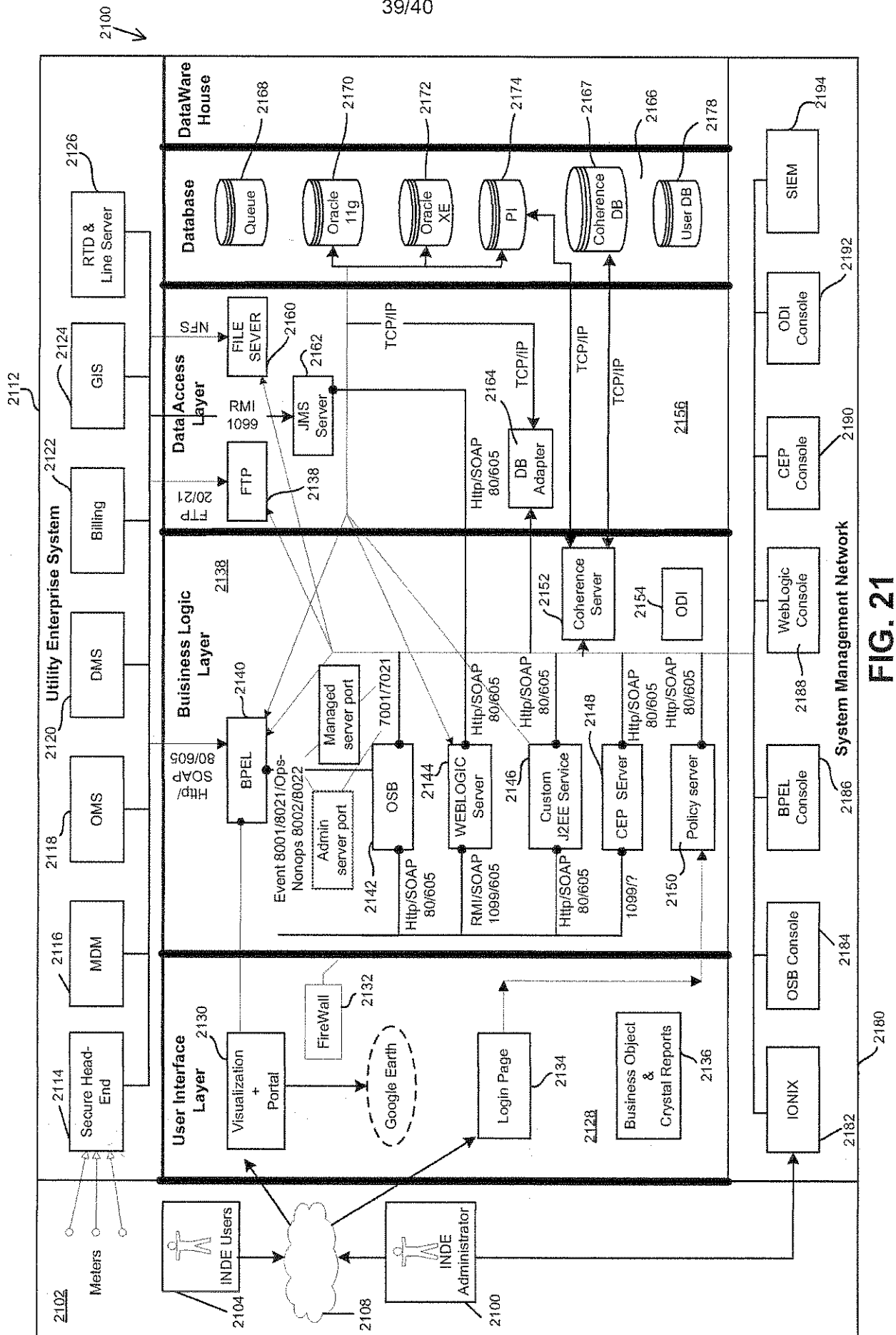
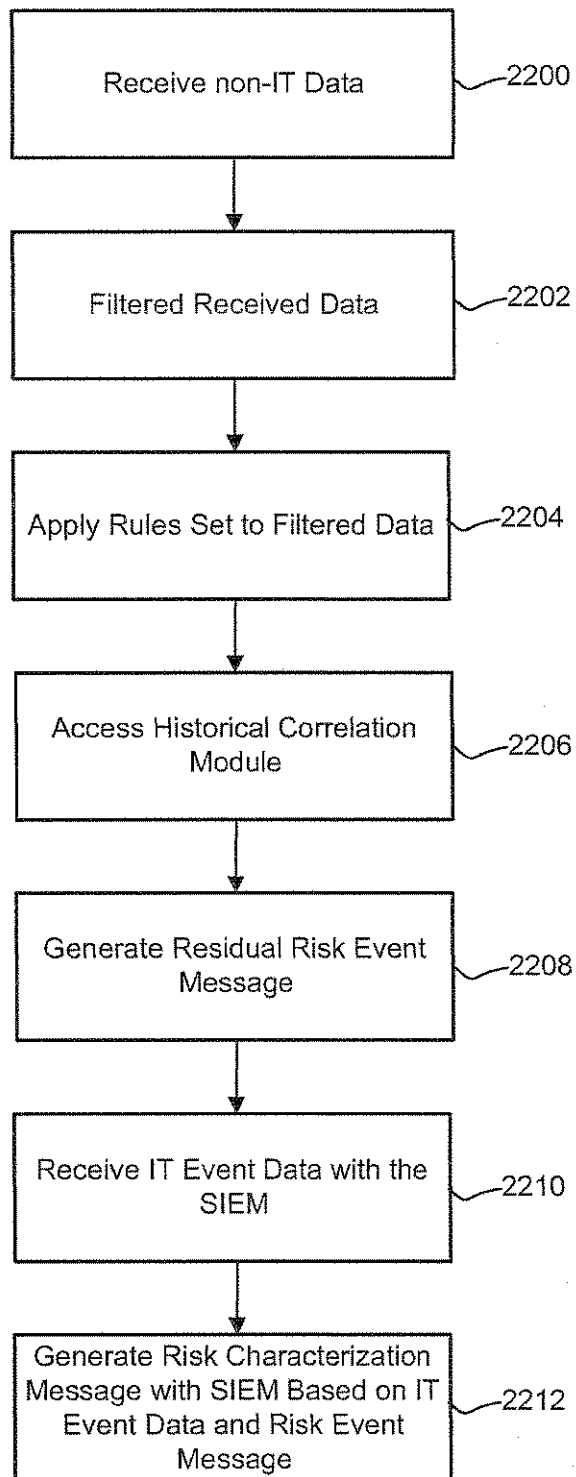


FIG. 21

**FIG. 22**