

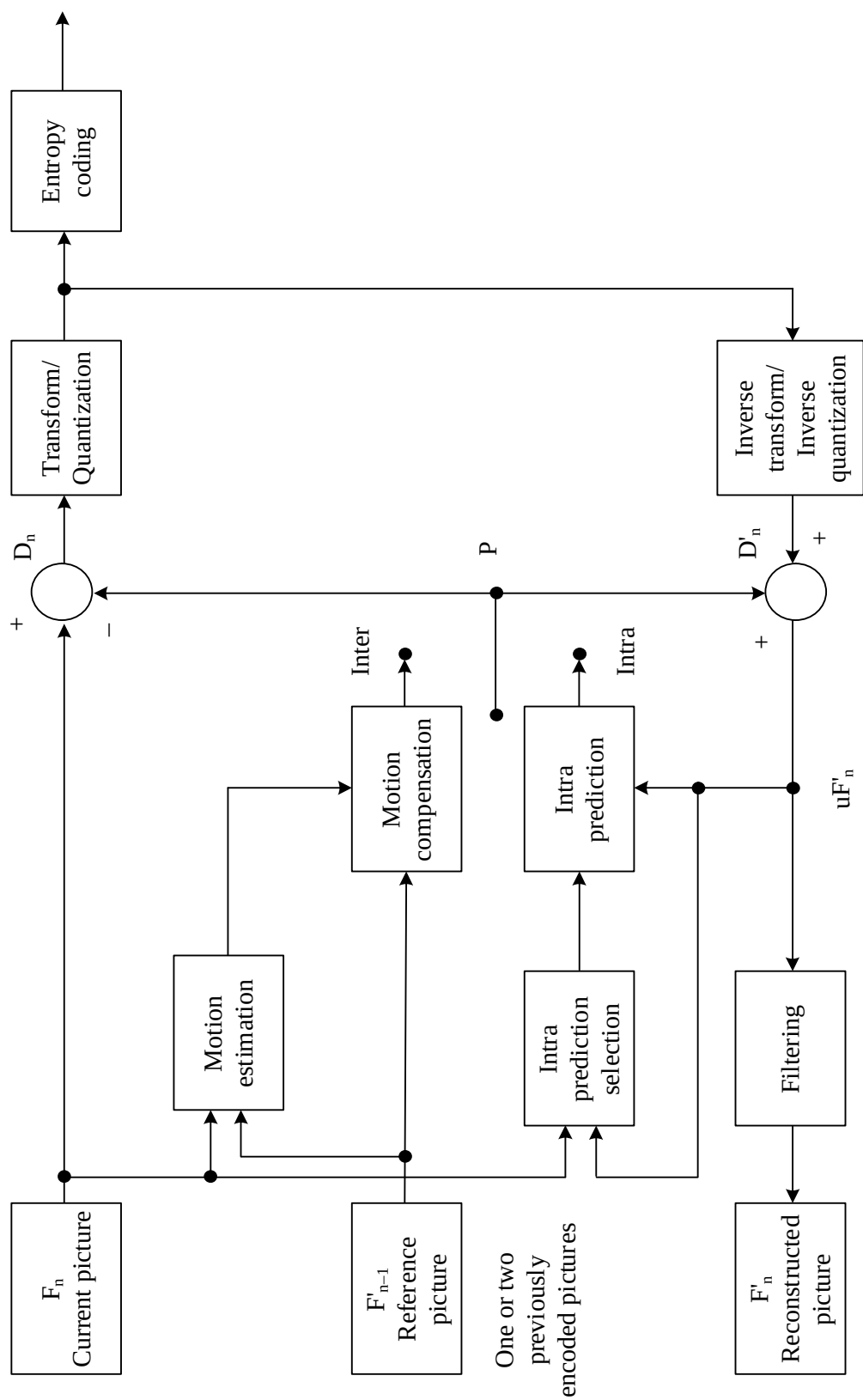


FIG. 1

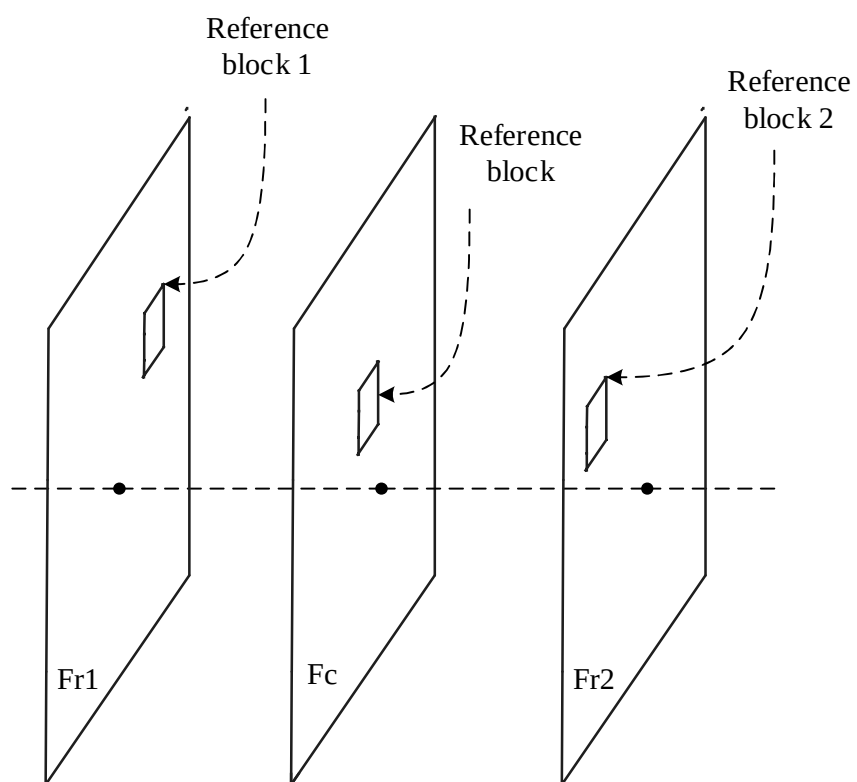


FIG. 2

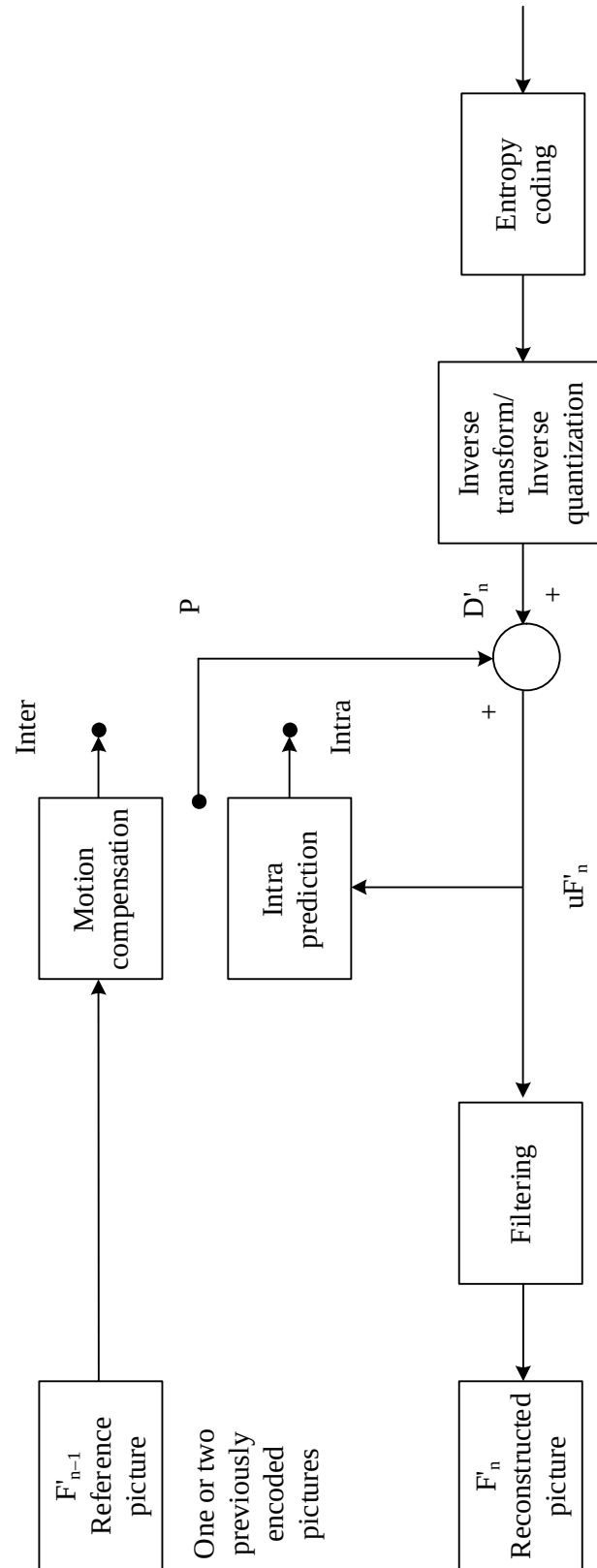


FIG. 3

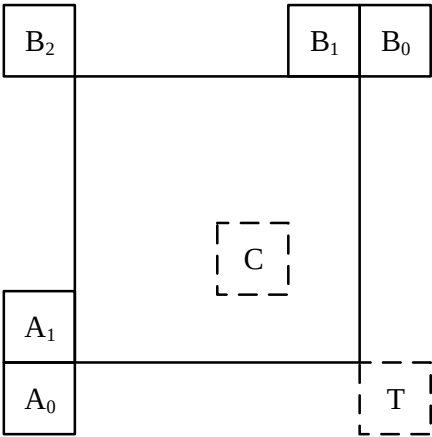


FIG. 4

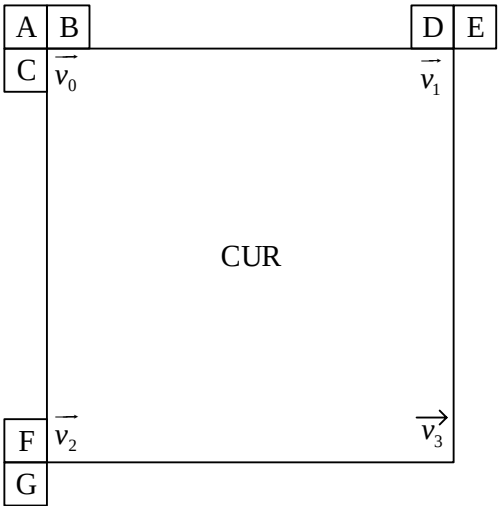


FIG. 5



FIG. 6

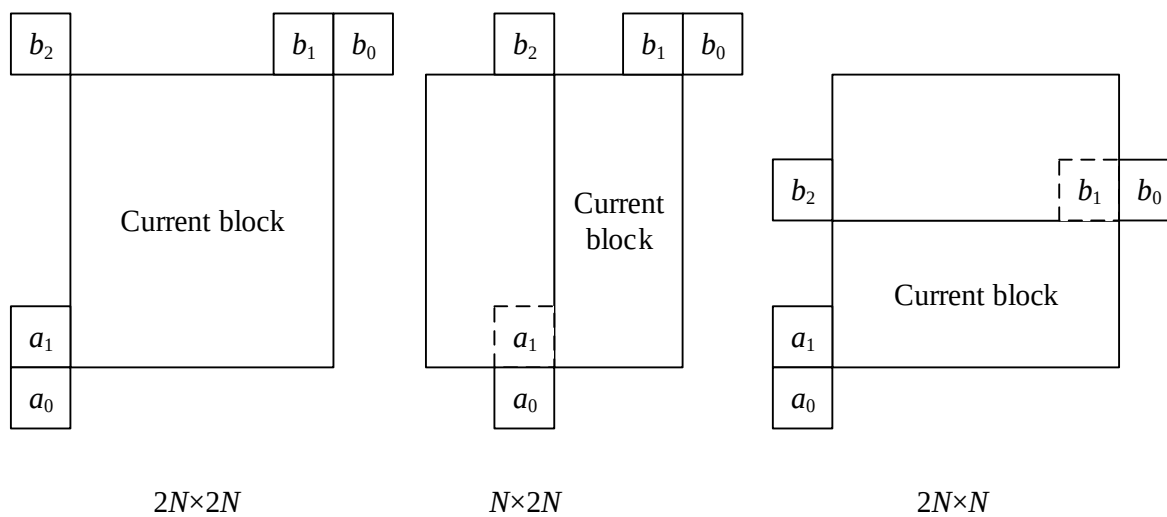


FIG. 7

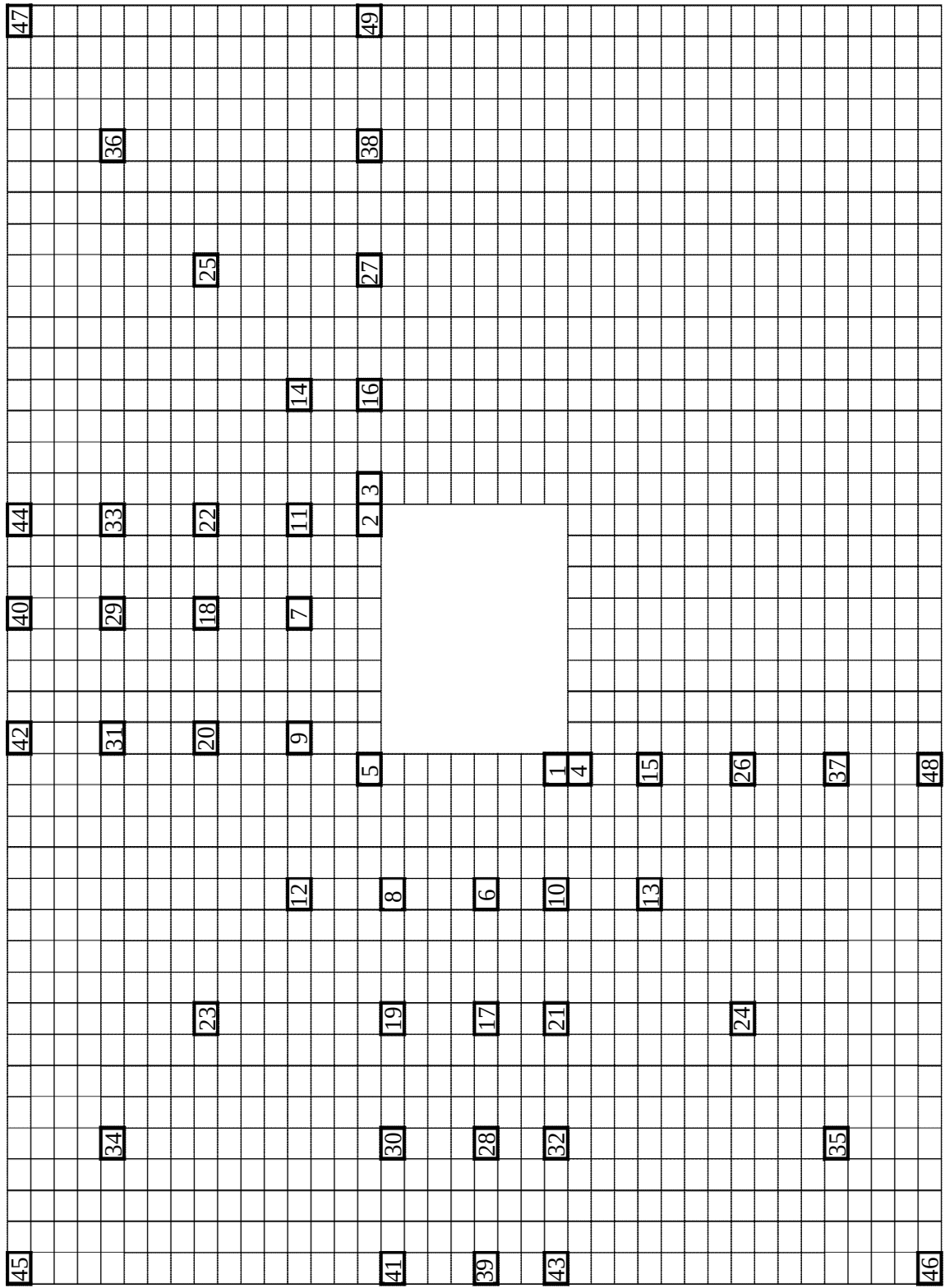


FIG. 8

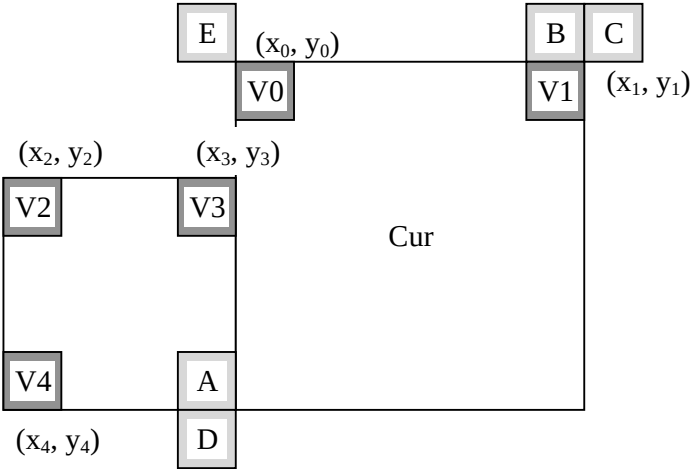


FIG. 9

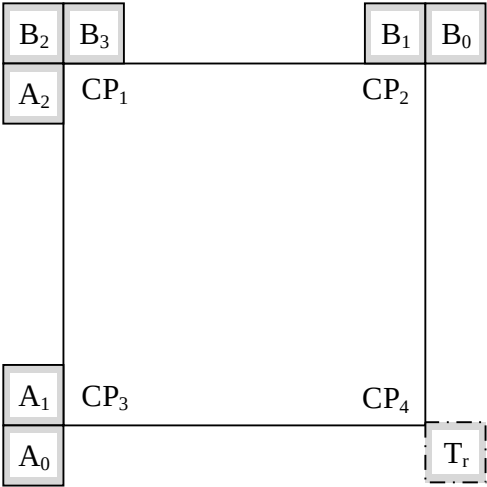


FIG. 10

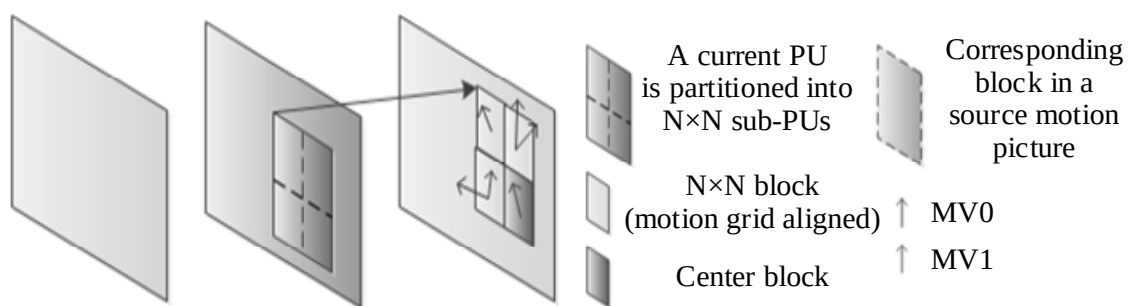


FIG. 11

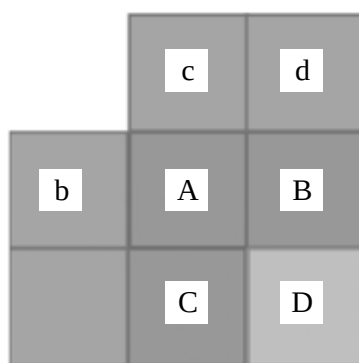


FIG. 12

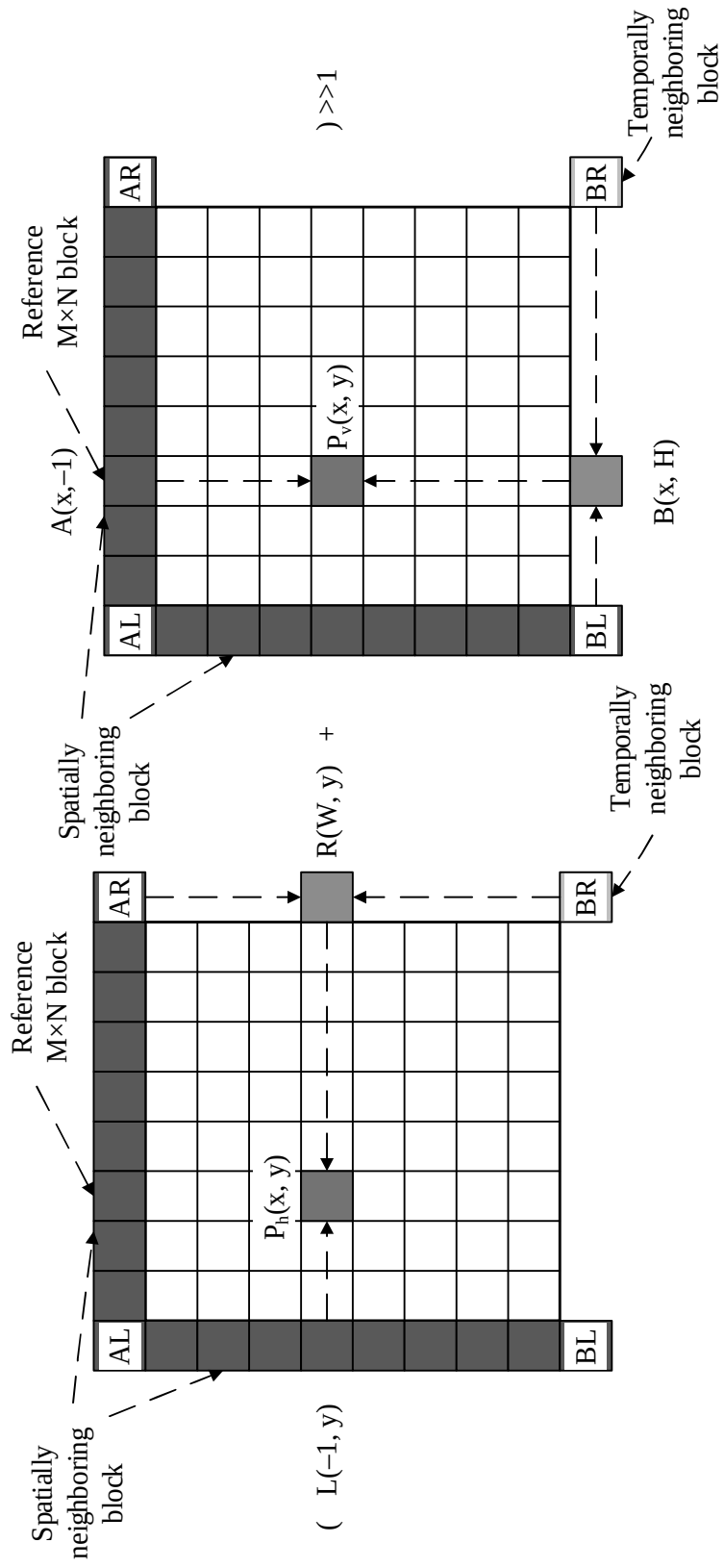


FIG. 13

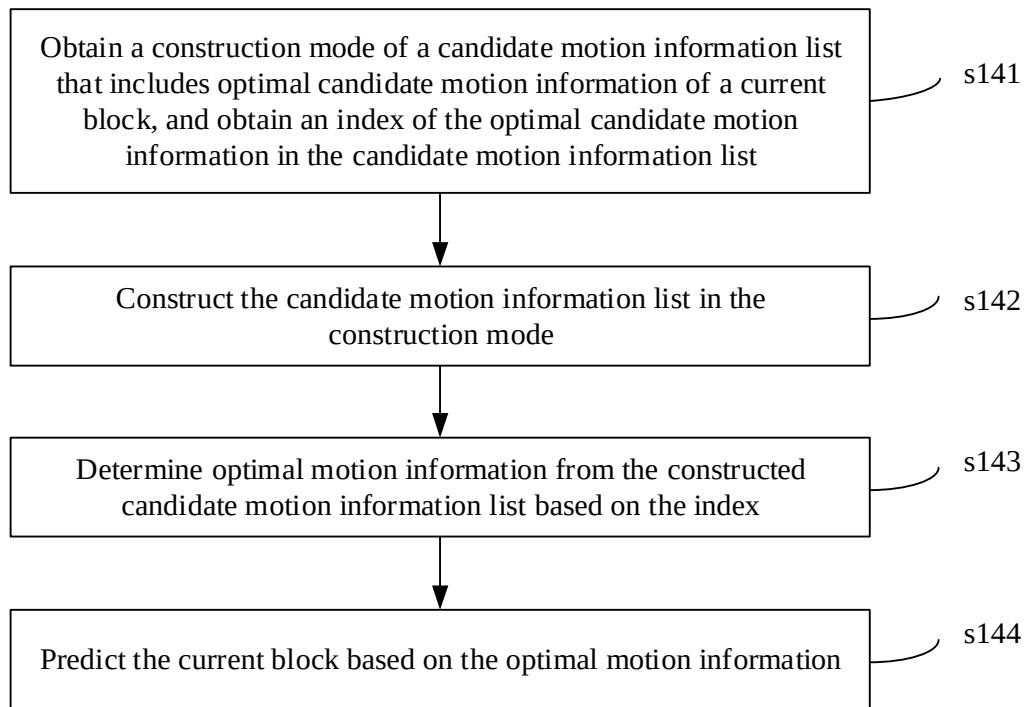


FIG. 14

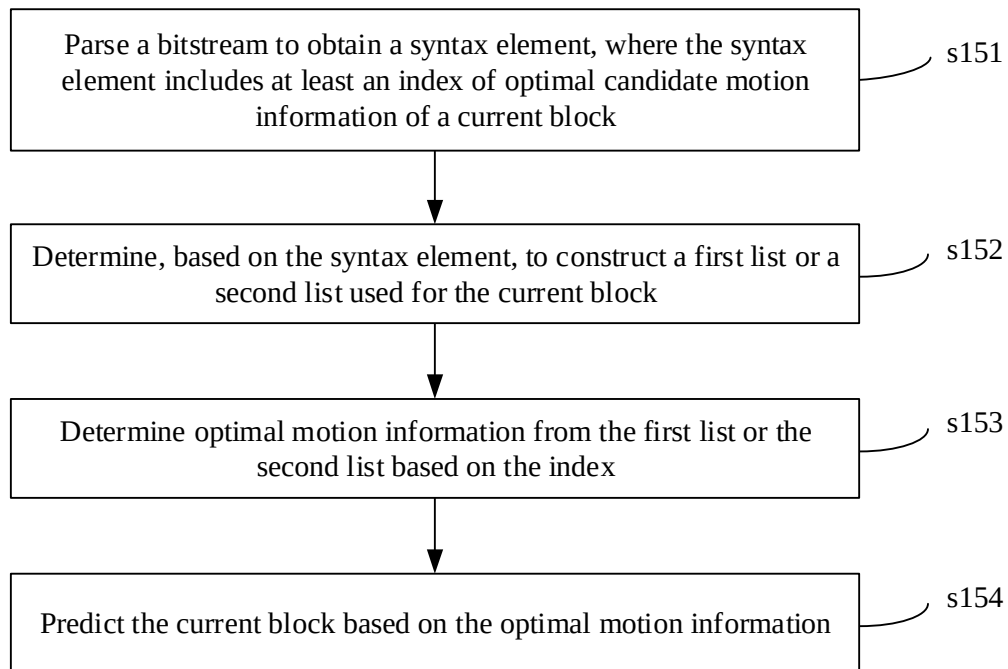


FIG. 15

	Descriptor
if (CuPredMode[x0][y0] == MODE_IBC) {	
if(MaxNumMergeCand > 1)	
merge_idx[x0][y0]	ae(v)
} else {	
if(sps_mmvd_enabled_flag cbWidth * cbHeight != 32)	
regular_merge_flag[x0][y0]	ae(v)
if (regular_merge_flag[x0][y0] == 1) {	
if(MaxNumMergeCand > 1)	
merge_idx[x0][y0]	ae(v)
} else {	
if(sps_mmvd_enabled_flag && cbWidth * cbHeight != 32)	
mmvd_merge_flag[x0][y0]	ae(v)
if(mmvd_merge_flag[x0][y0] == 1) {	
if(MaxNumMergeCand > 1)	
mmvd_cand_flag[x0][y0]	ae(v)
mmvd_distance_idx[x0][y0]	ae(v)
mmvd_direction_idx[x0][y0]	ae(v)
} else {	
if(MaxNumSubblockMergeCand > 0 && cbWidth >= 8 && cbHeight >= 8)	
merge_subblock_flag[x0][y0]	ae(v)
if(merge_subblock_flag[x0][y0] == 1) {	
if(MaxNumSubblockMergeCand > 1)	
merge_subblock_idx[x0][y0]	ae(v)
} else {	

	Descriptor
<pre> if(sps_ciip_enabled_flag && cu_skip_flag[x0][y0] == 0 && (cbWidth*cbHeight) >= 64 && cbWidth < 128 && cbHeight < 128) { </pre>	
ciip_flag [x0][y0]	ae(v)
<pre> if(ciip_flag[x0][y0] && MaxNumMergeCand > 1) </pre>	
merge_idx [x0][y0]	ae(v)
<pre> } </pre>	
<pre> if(MergeTriangleFlag[x0][y0]) { </pre>	
merge_triangle_split_dir [x0][y0]	ae(v)
merge_triangle_idx0 [x0][y0]	ae(v)
merge_triangle_idx1 [x0][y0]	ae(v)
<pre> } </pre>	
<pre> } </pre>	
<pre> } </pre>	
<pre> } </pre>	
<pre> } </pre>	
<pre> } </pre>	

FIG. 16

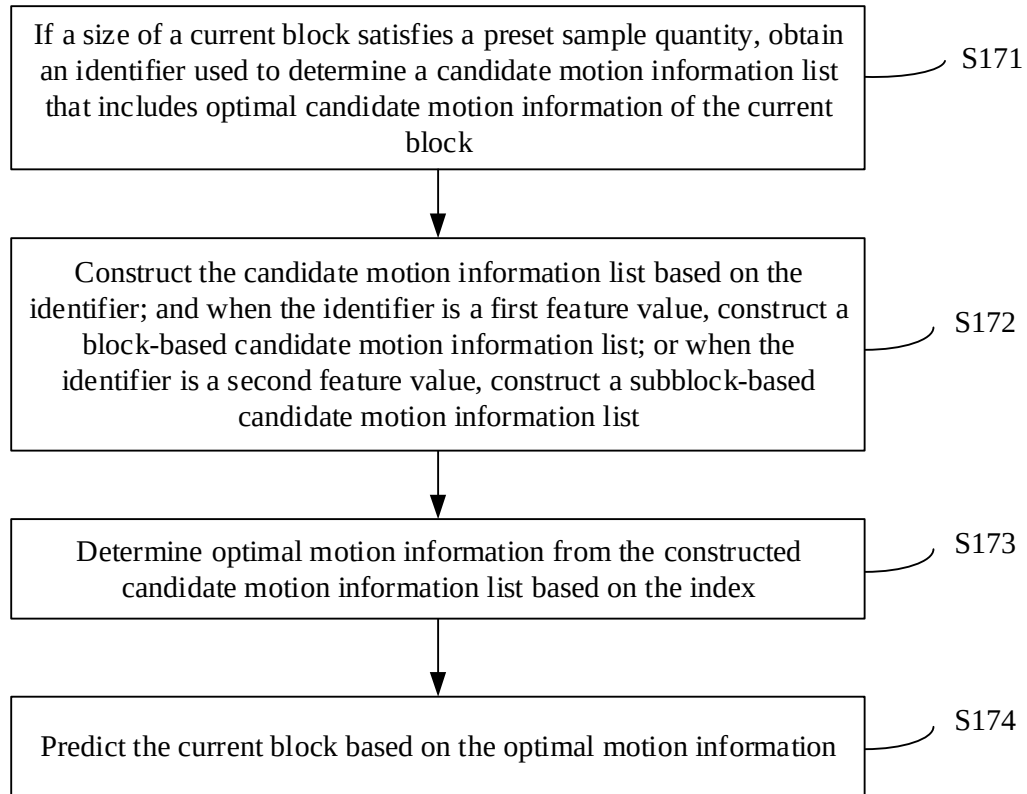


FIG. 17

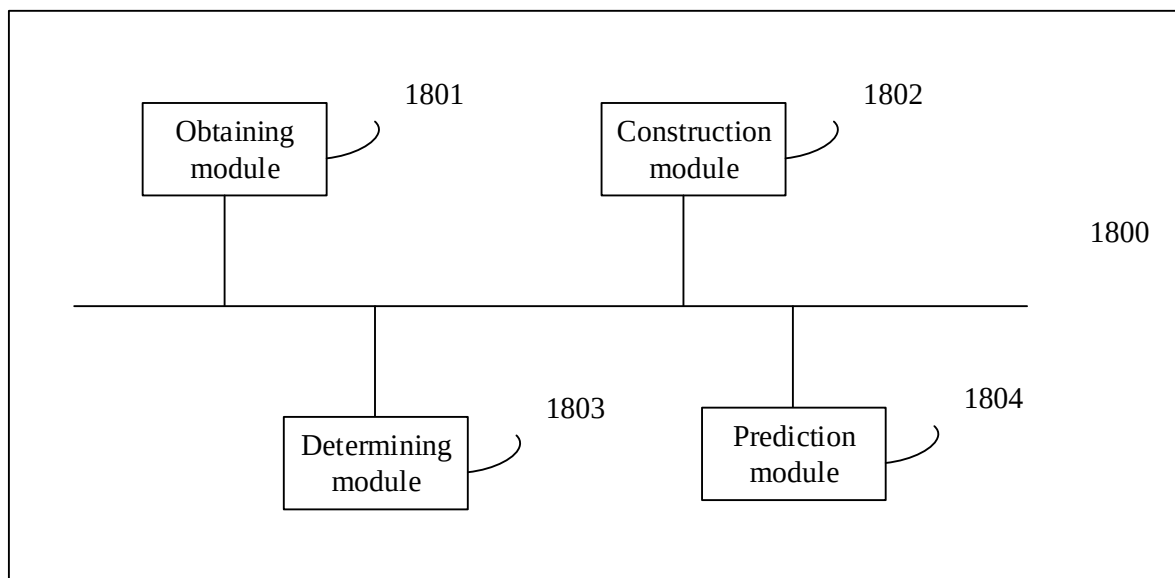


FIG. 18

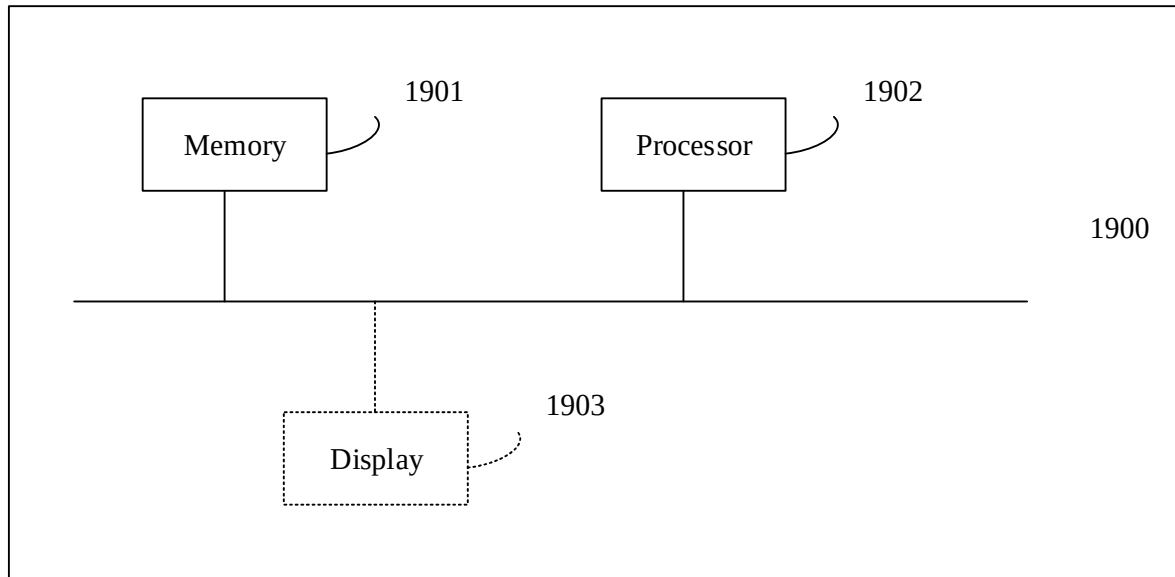


FIG. 19