

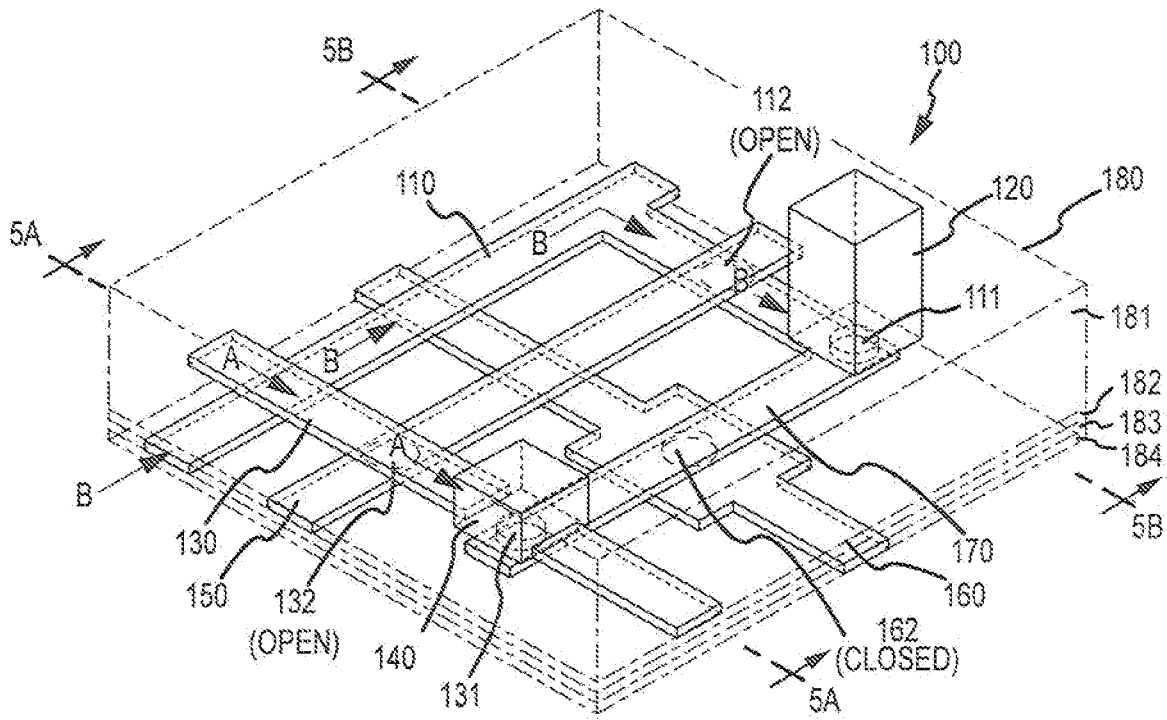


FIG. 1

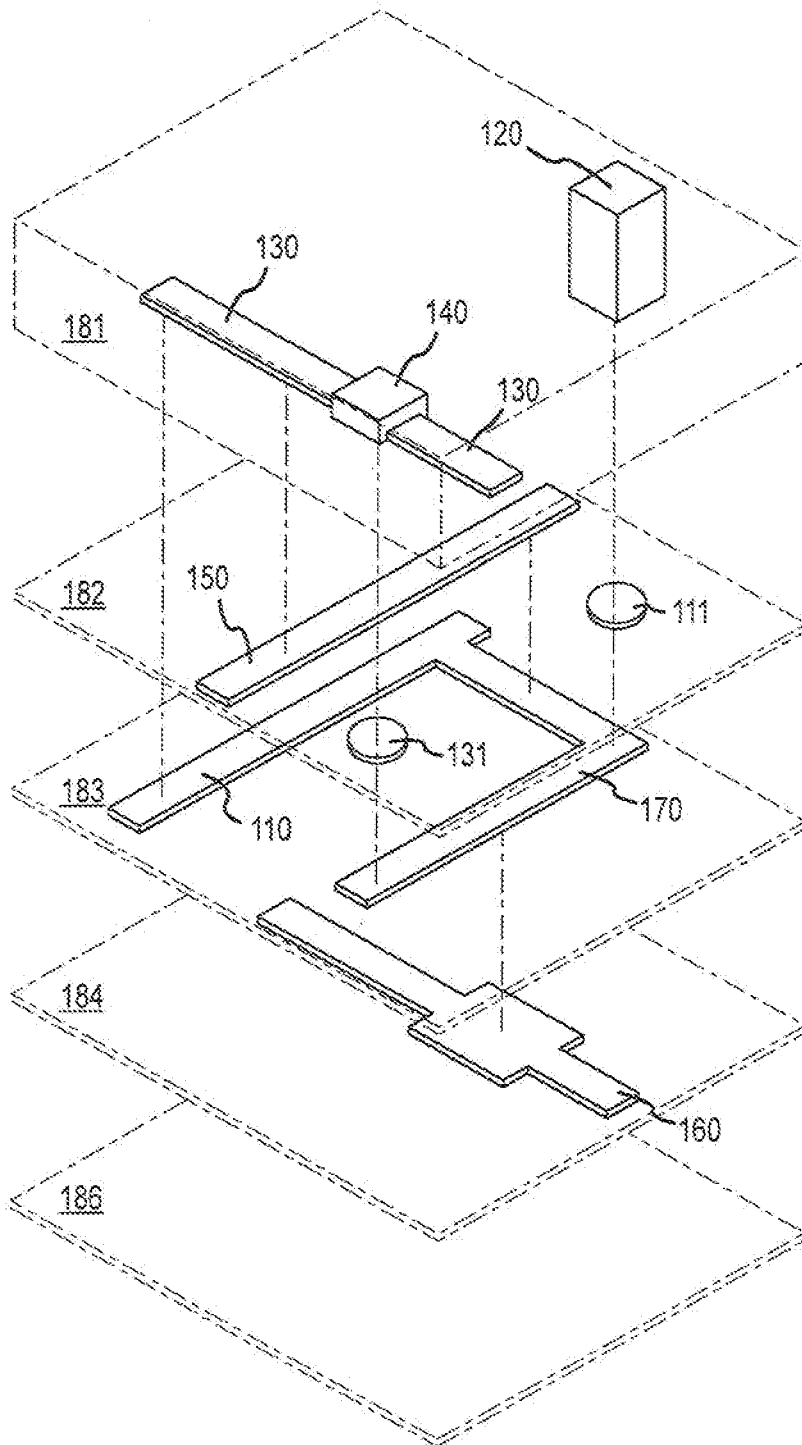


FIG.1A

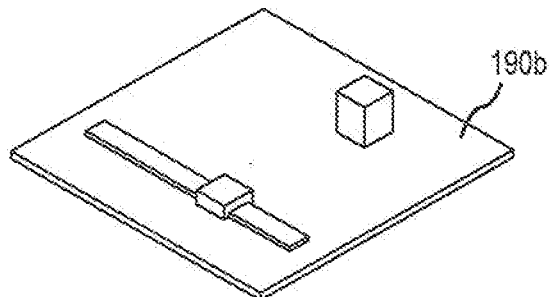


FIG. 1B

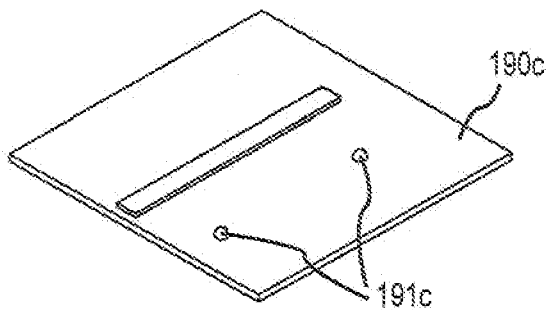


FIG. 1C

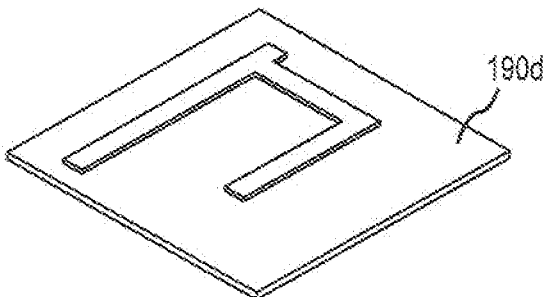


FIG. 1D

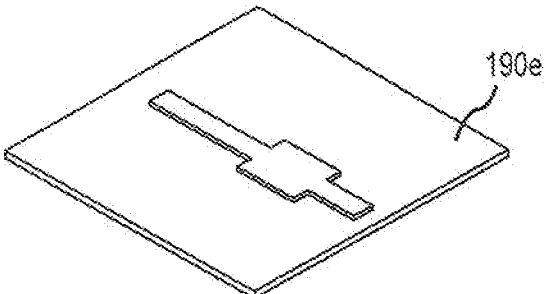


FIG. 1E

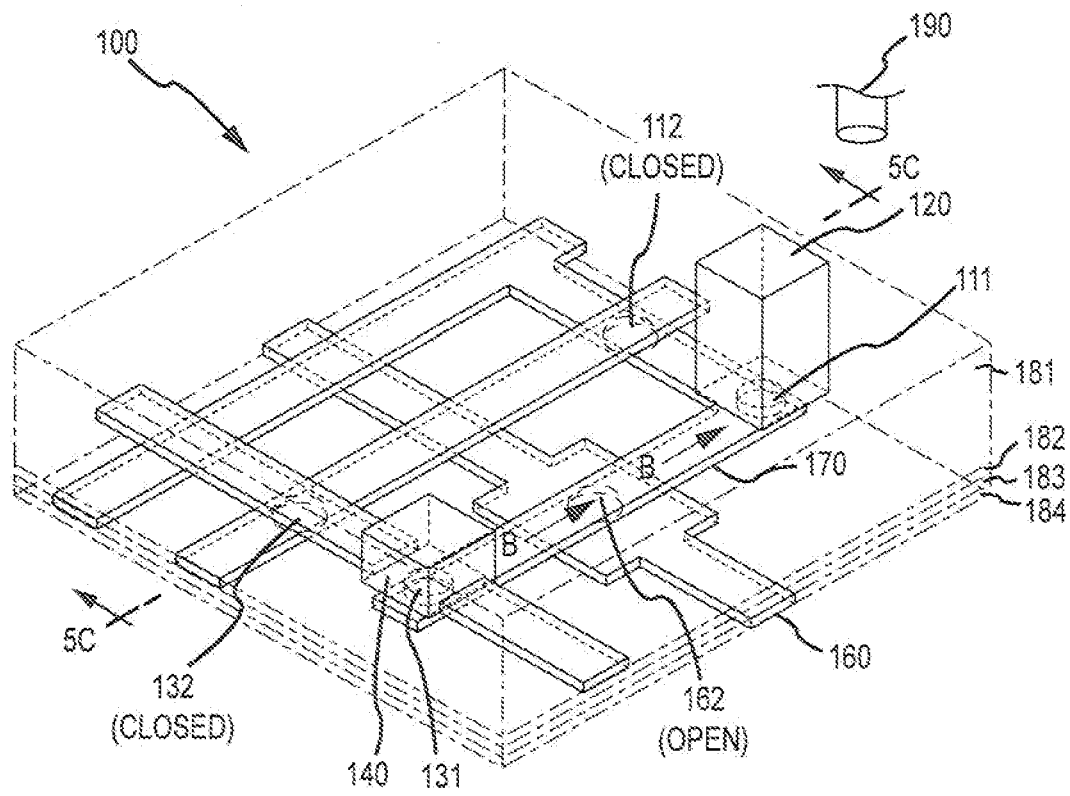
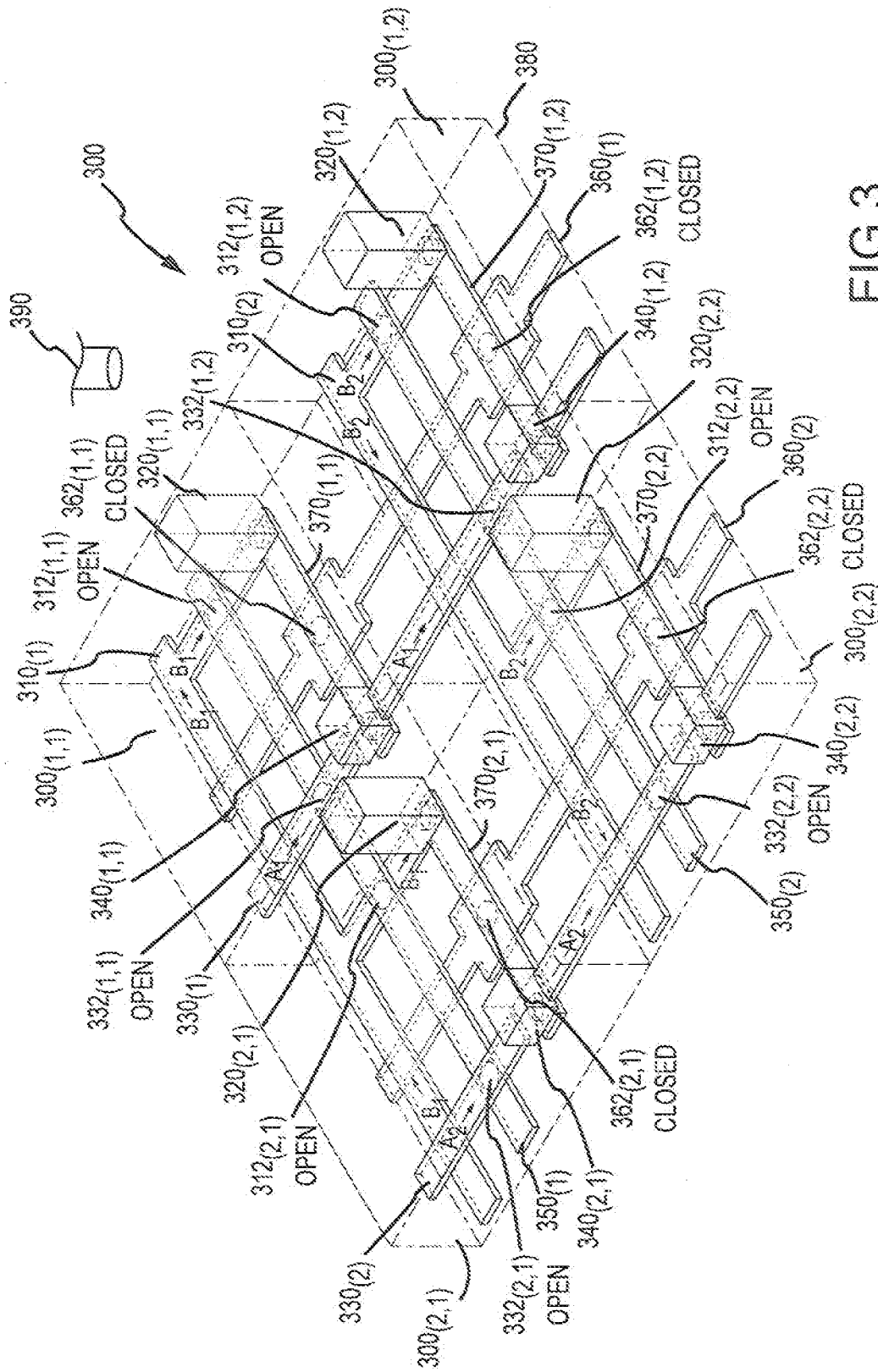
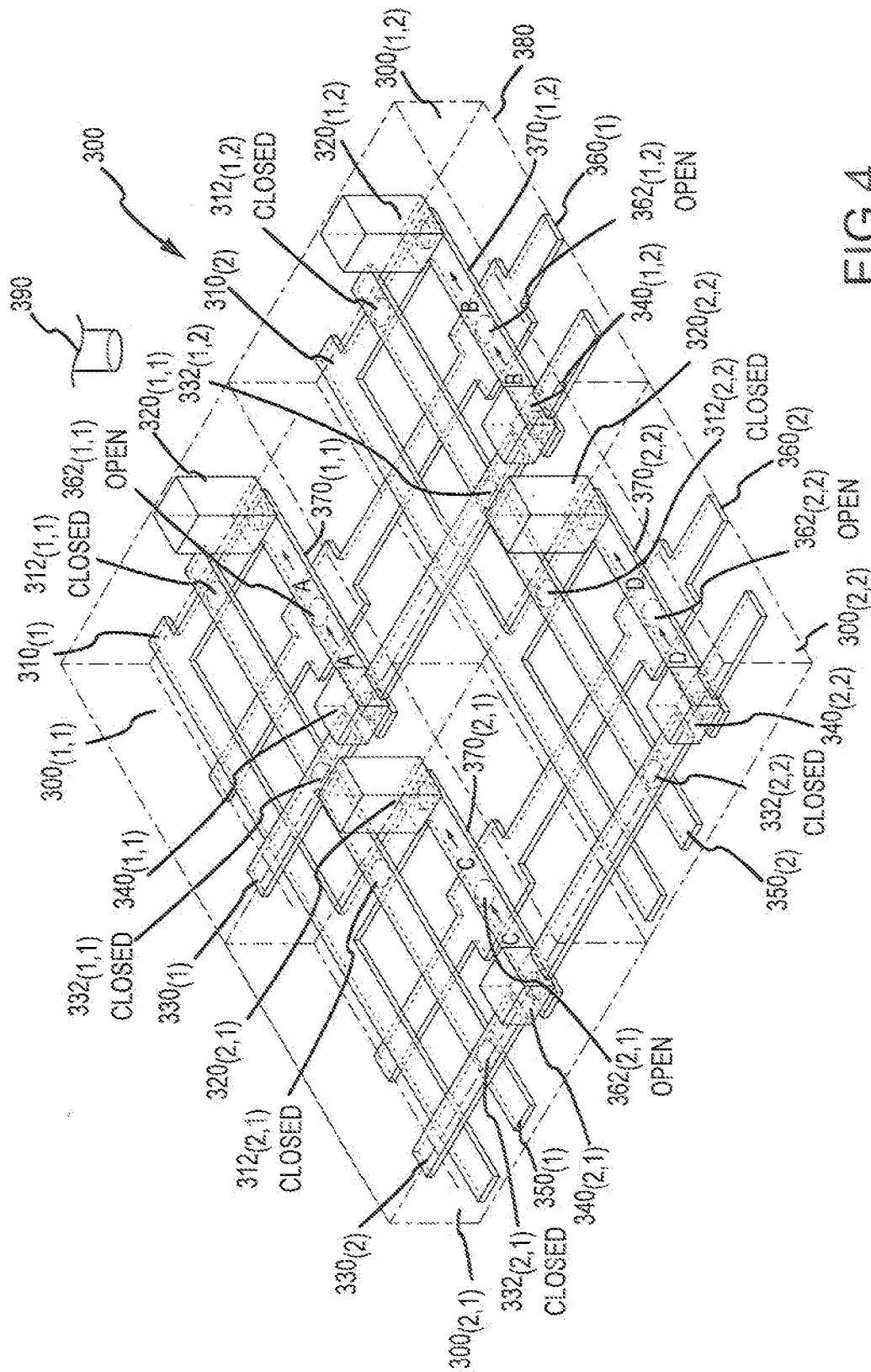
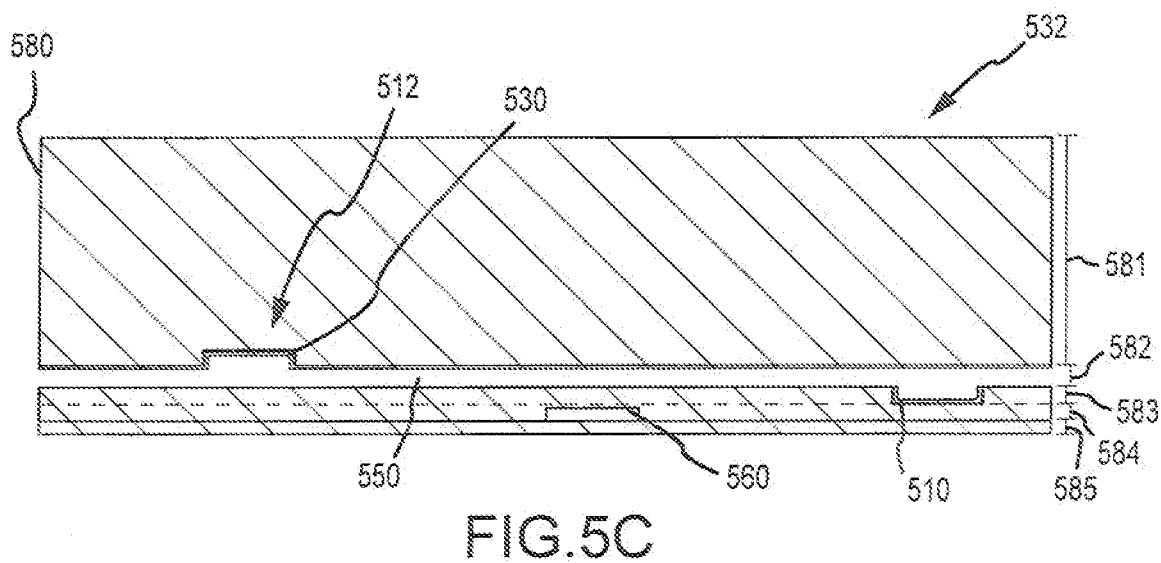
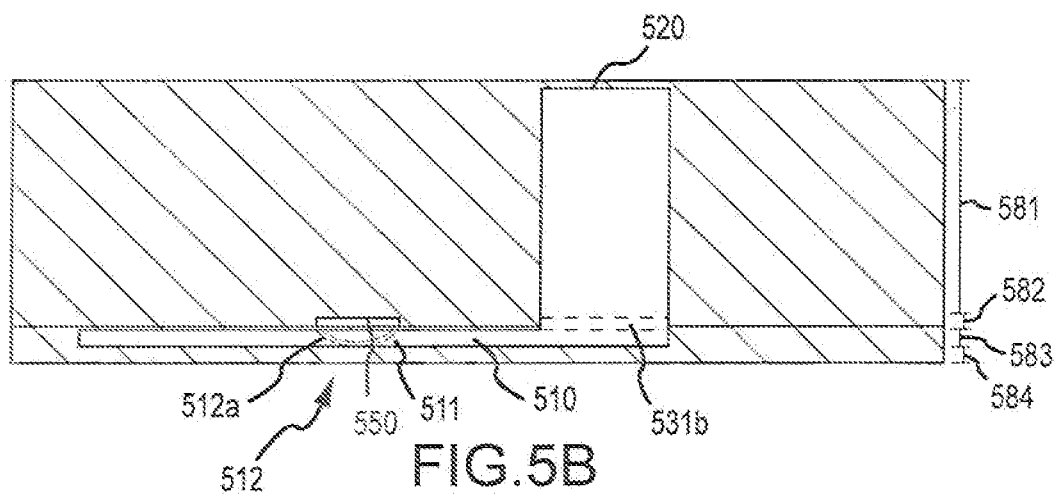
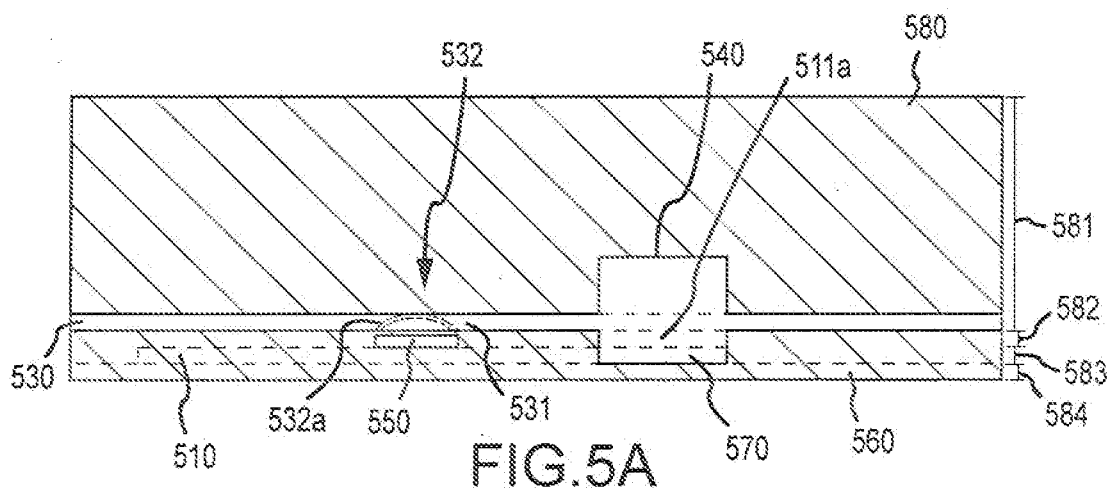


FIG. 2







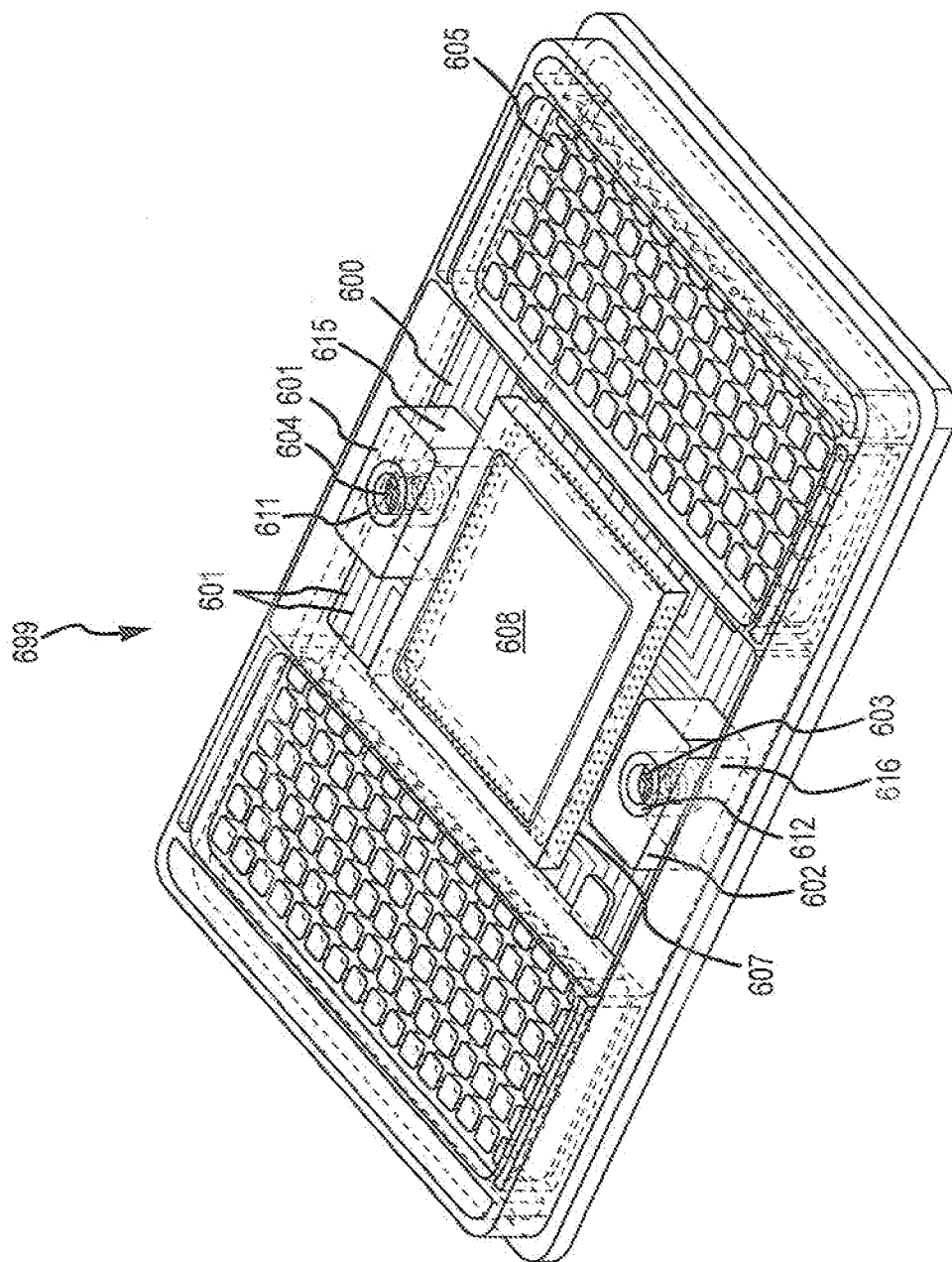


FIG. 6A

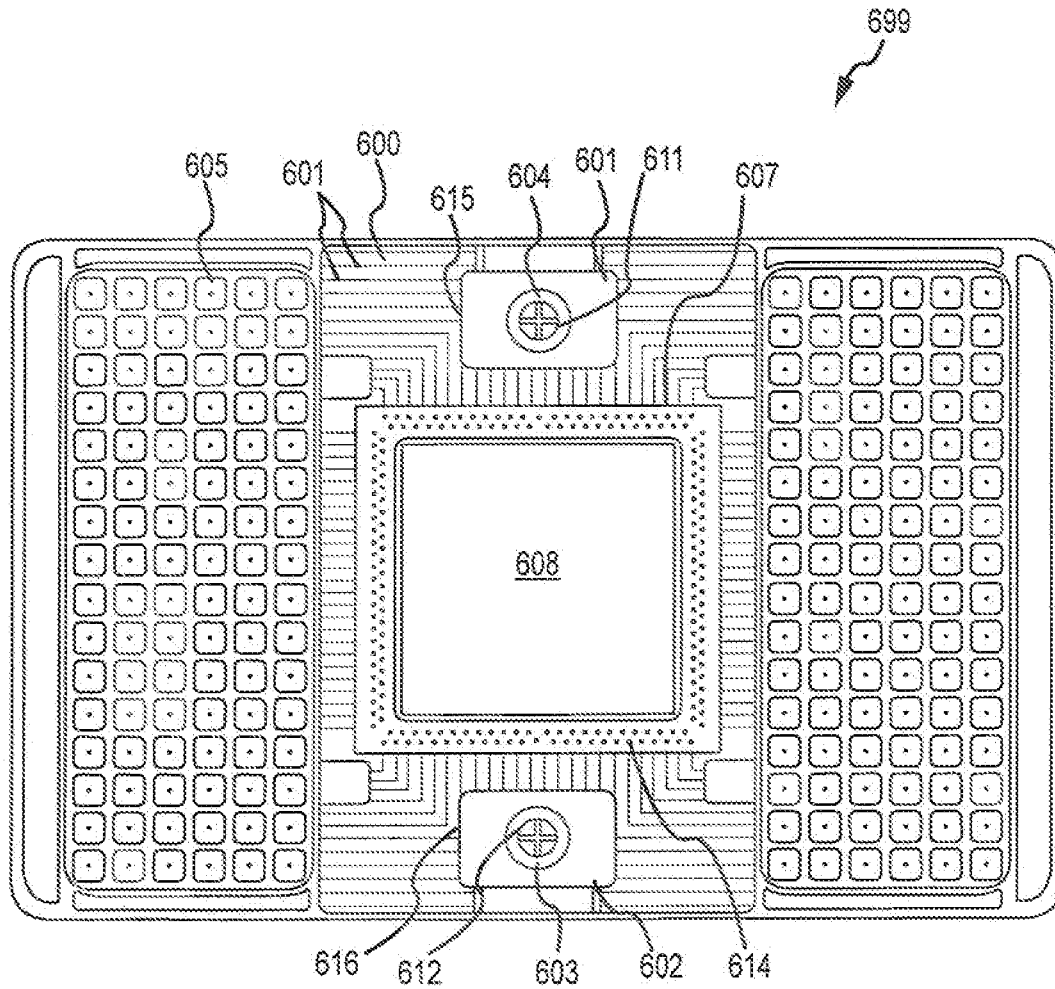


FIG. 6B

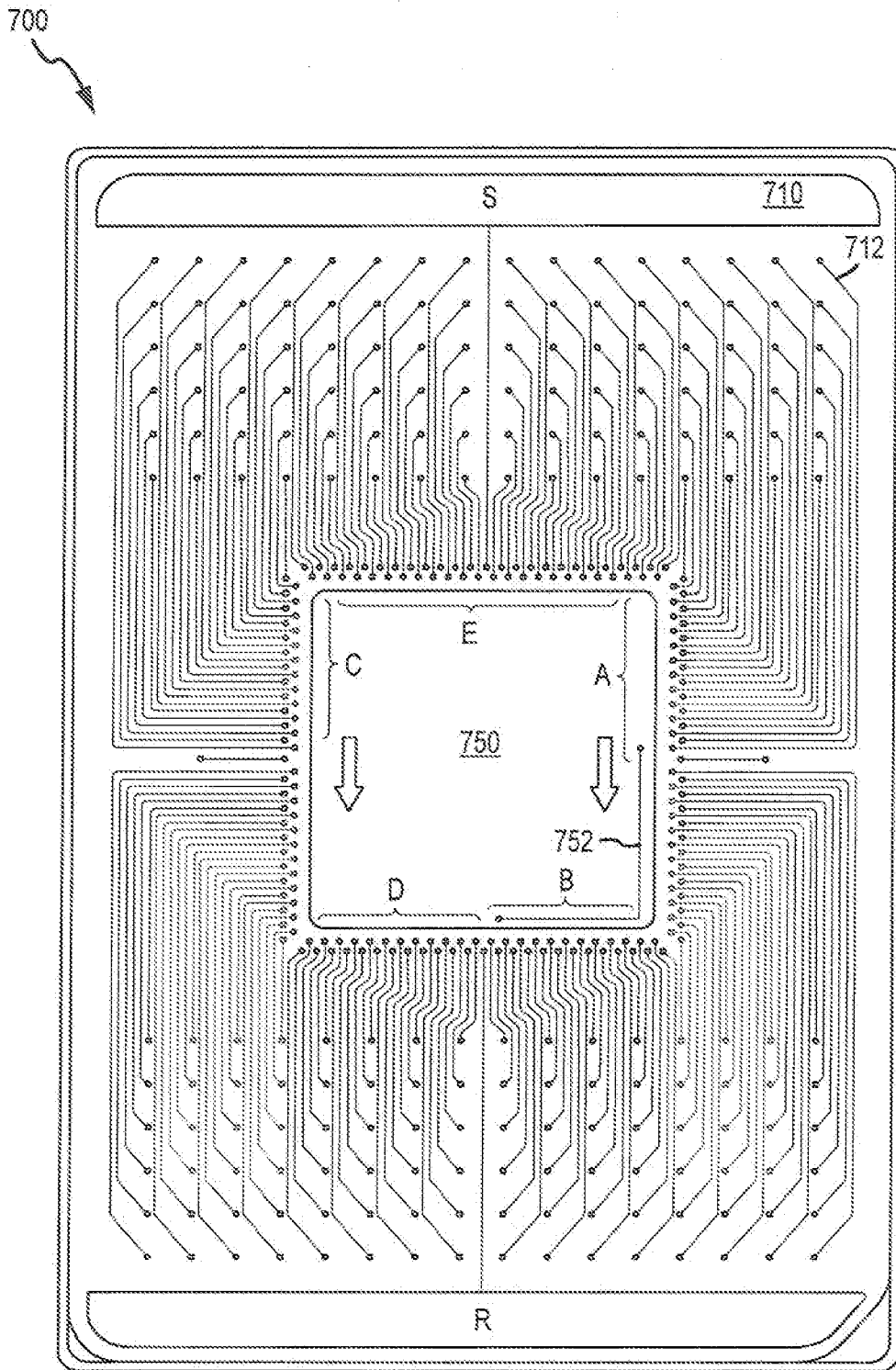


FIG.7

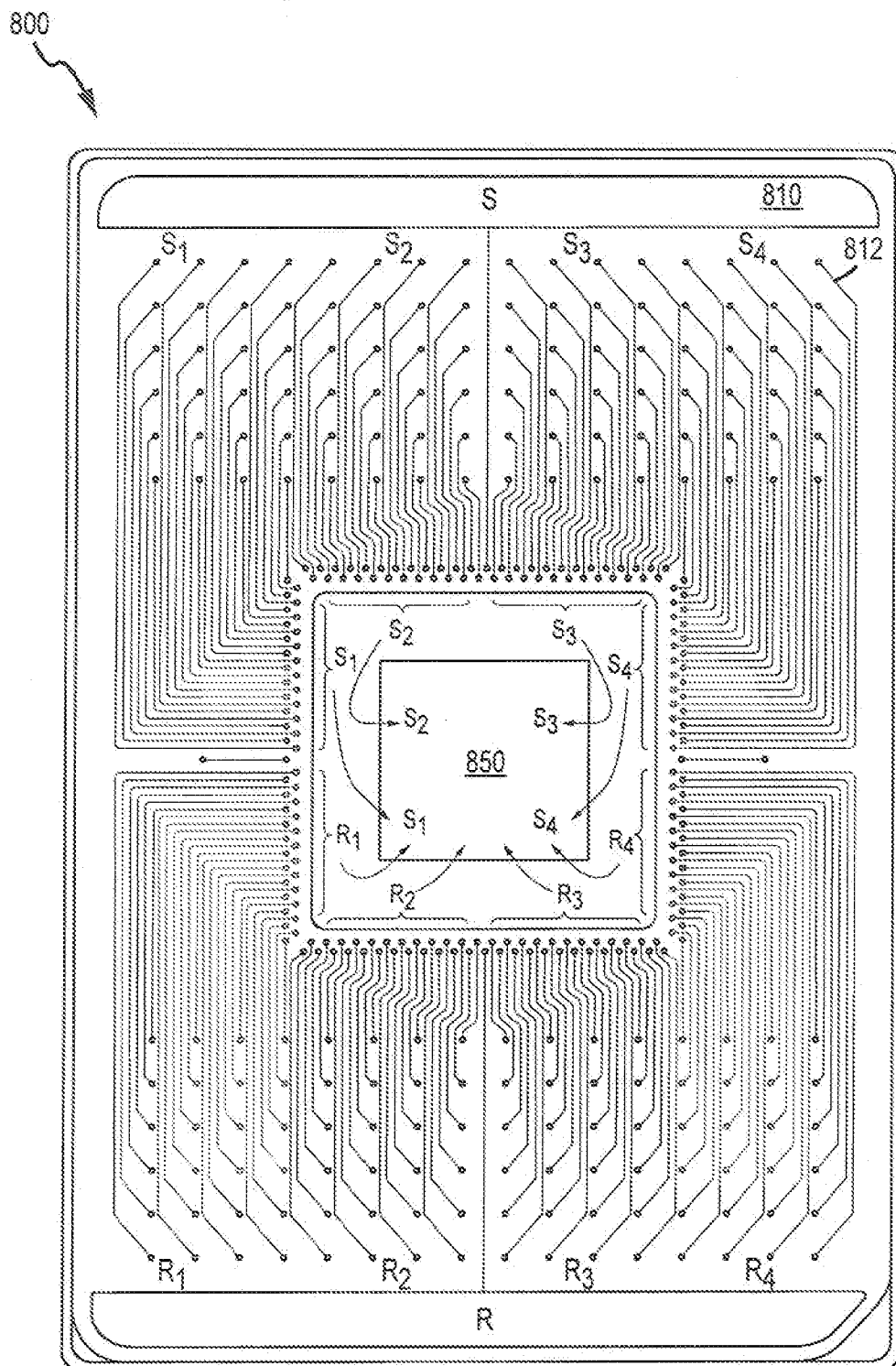


FIG.8

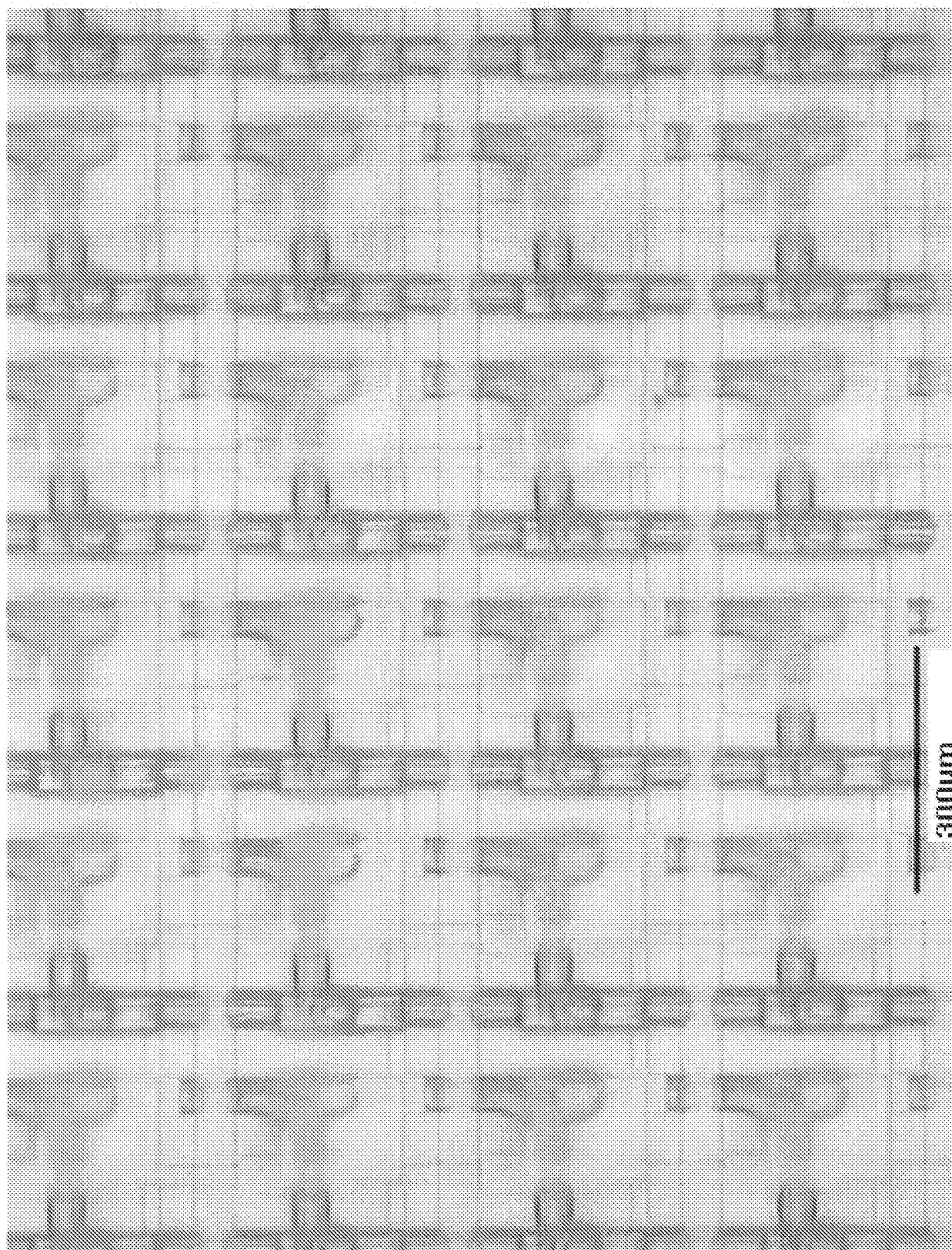


FIG. 9

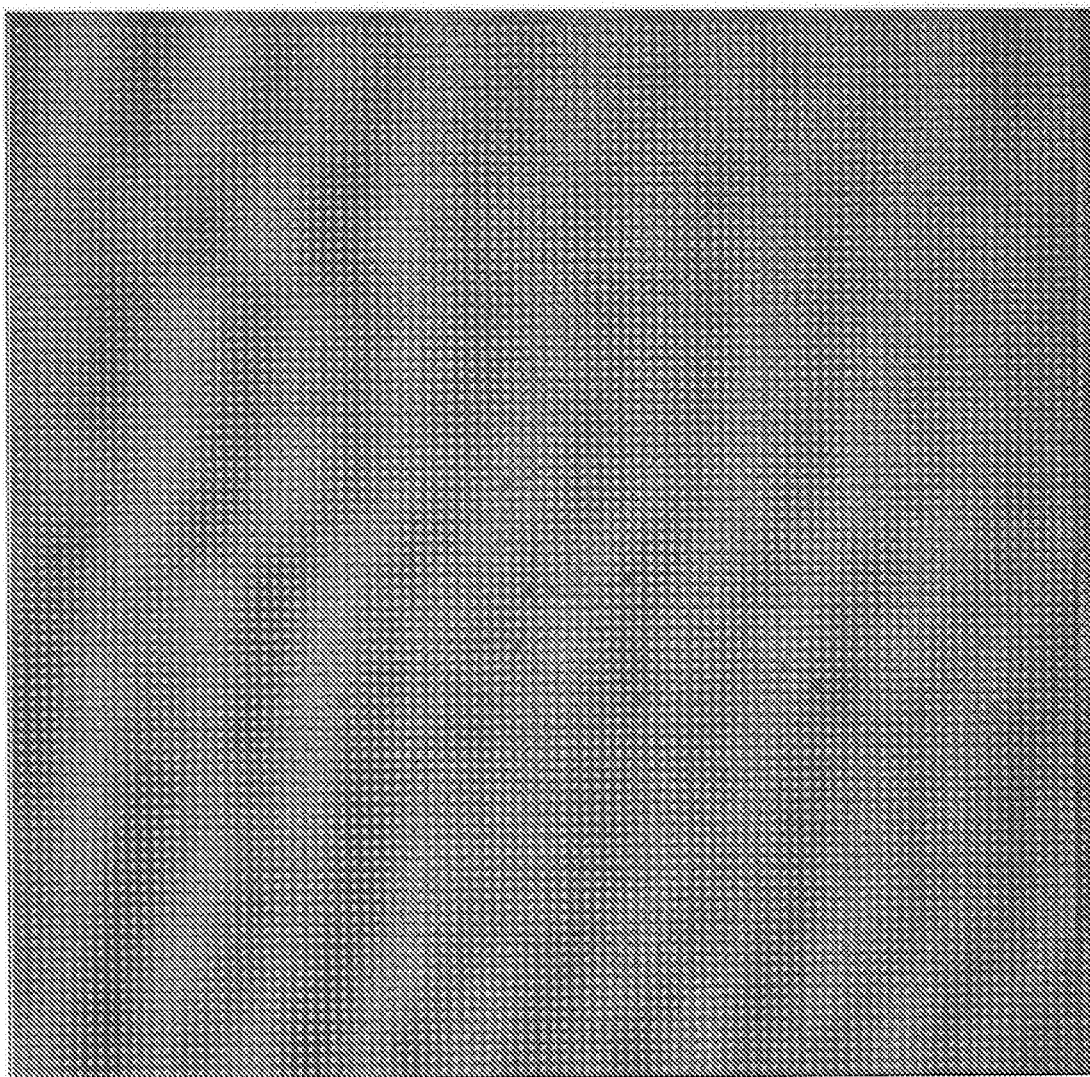


FIG. 10

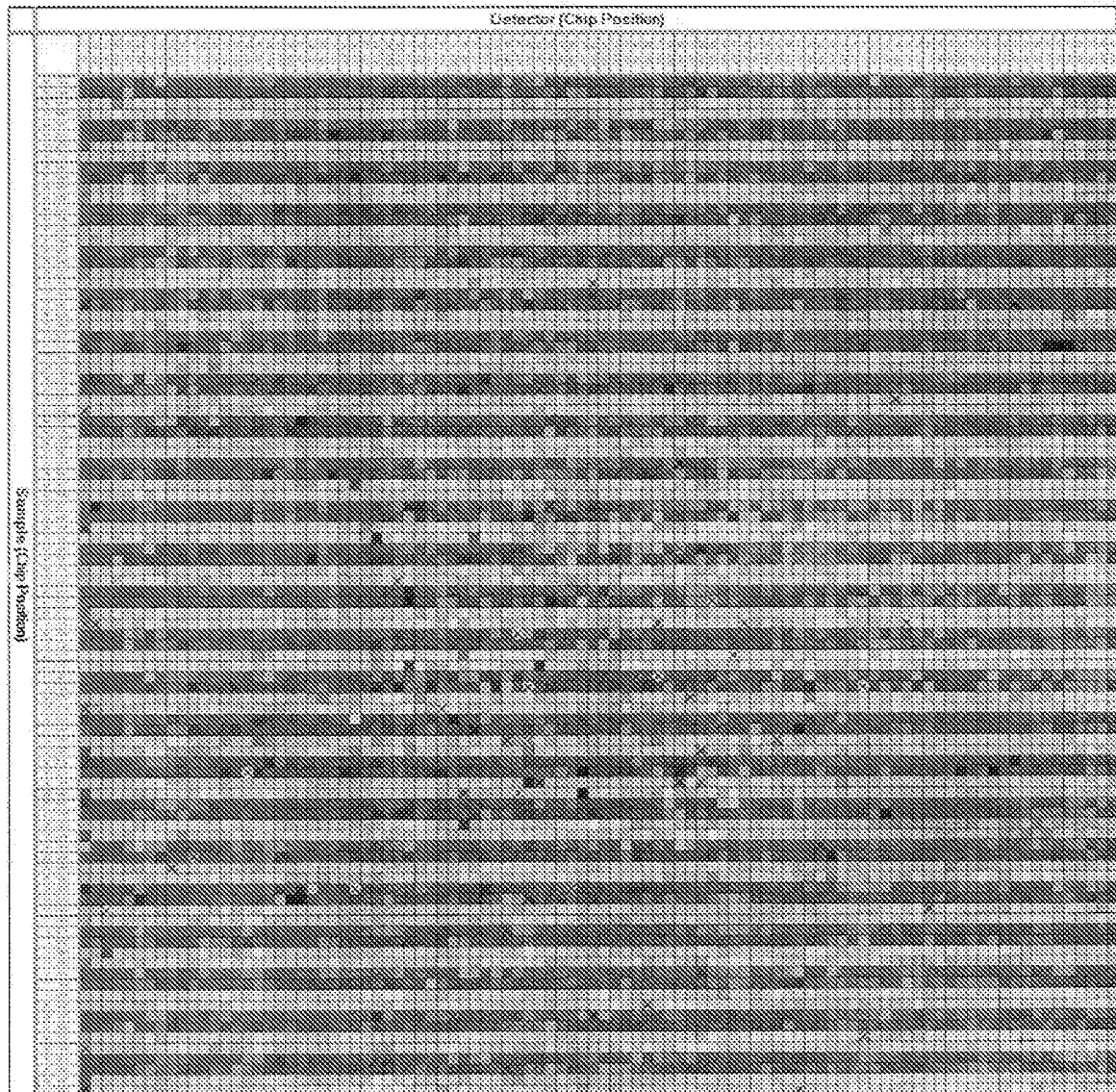


FIG. 11

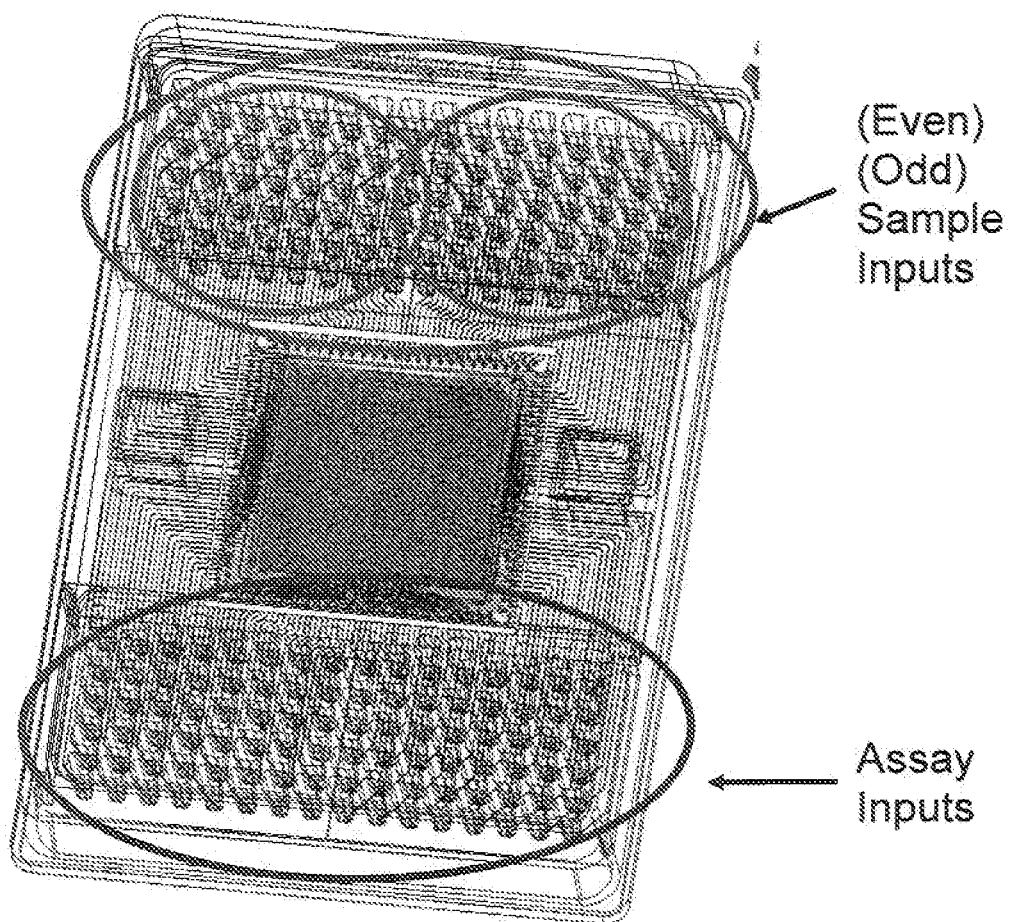
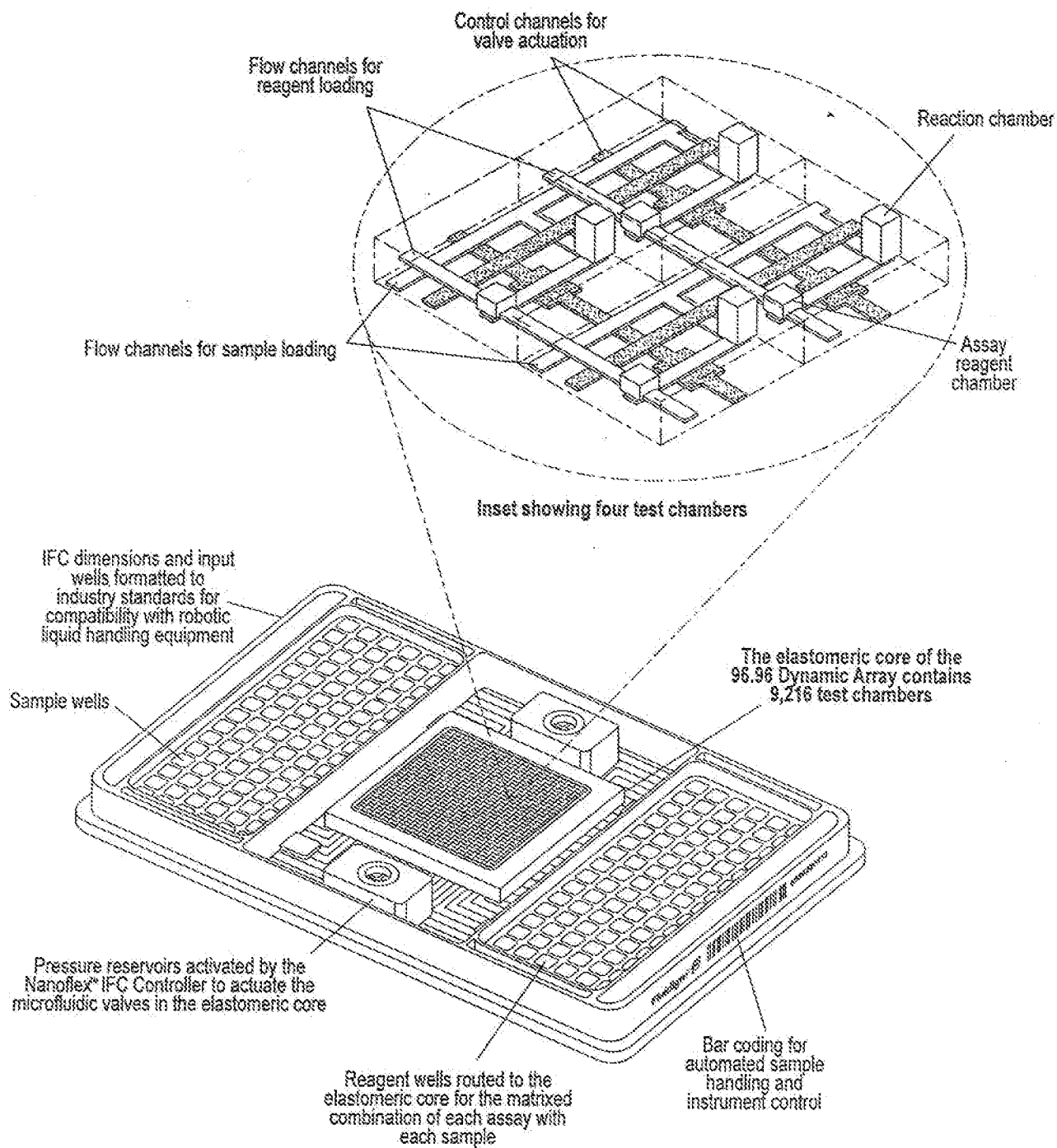


FIG. 13



Dynamic Array Schematic

FIG. 14