

Fig. 1a

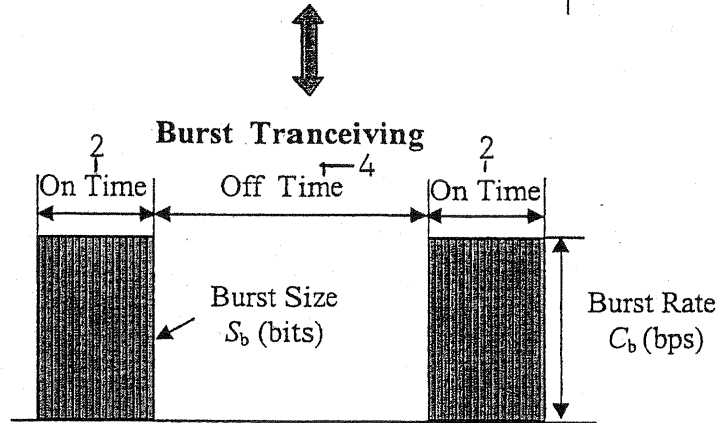


Fig. 1b

Fig. 1: Illustration of the TDM based power saving concept.

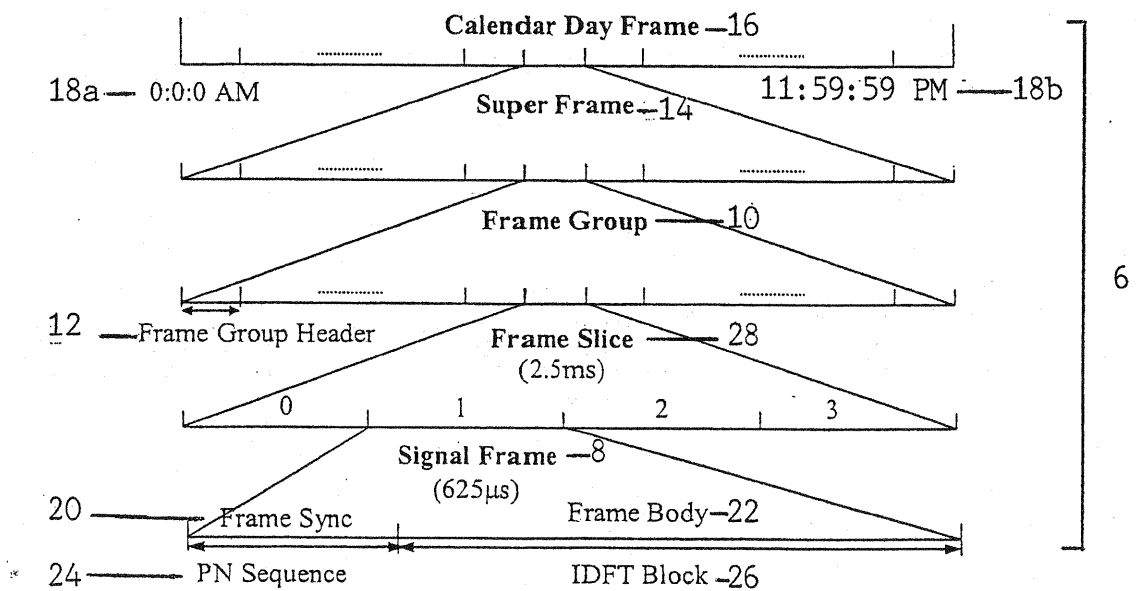


Fig. 2: Hierarchical Frame Structure of DMB-T.

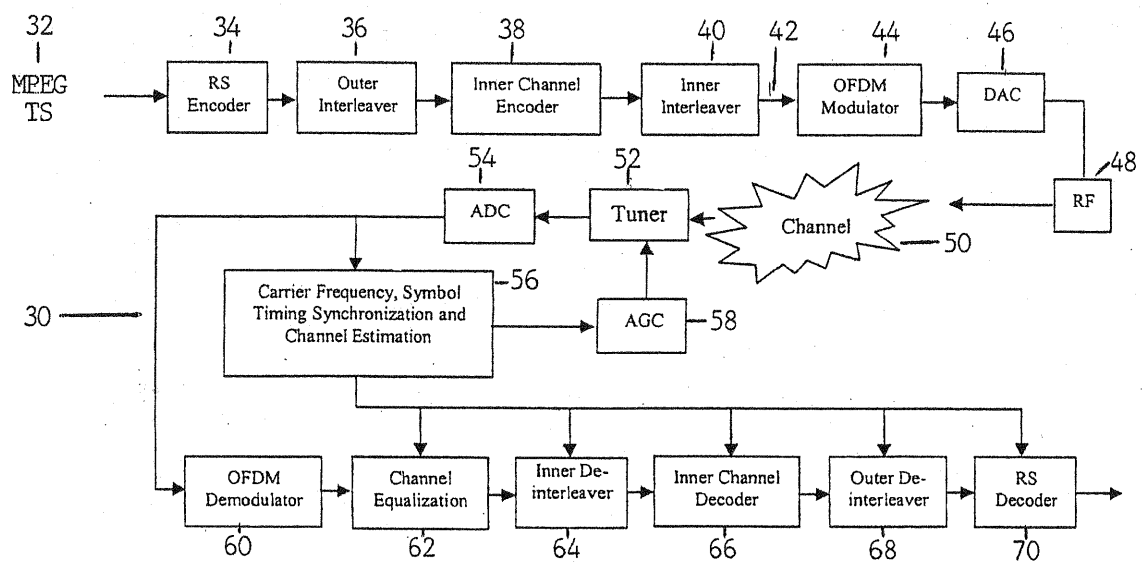


Fig. 3: Simplified block diagram of a typical DTT transceiver.

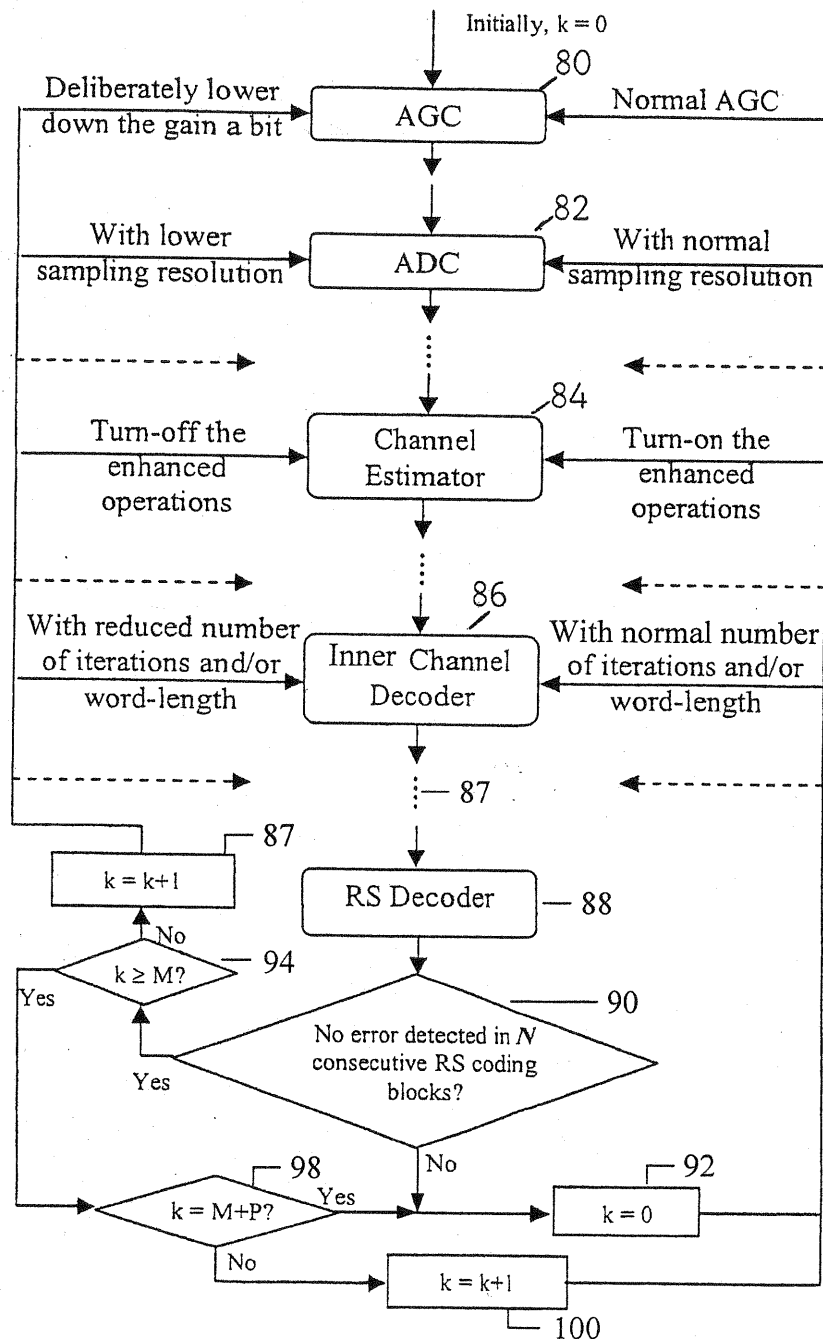


Fig. 4: Illustration of the proposed power reduction scheme.