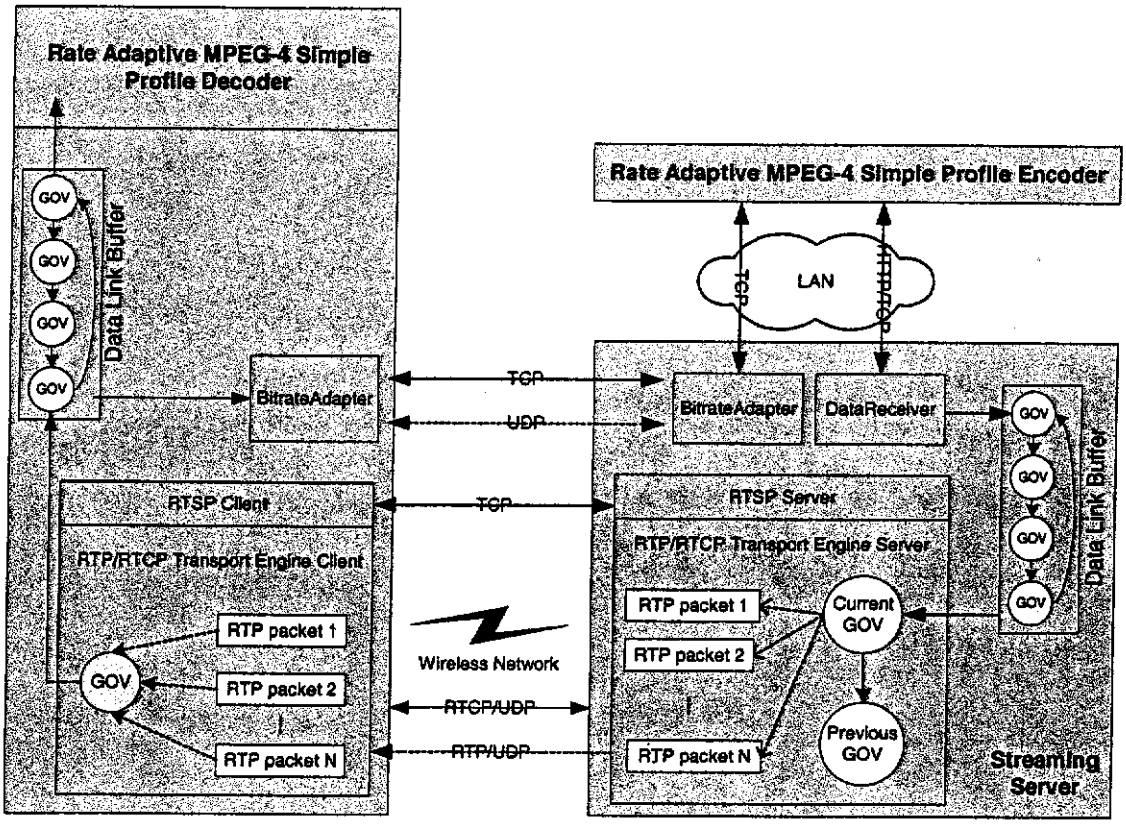


FIG. 1 Diagram showing the system architecture of the present invention.

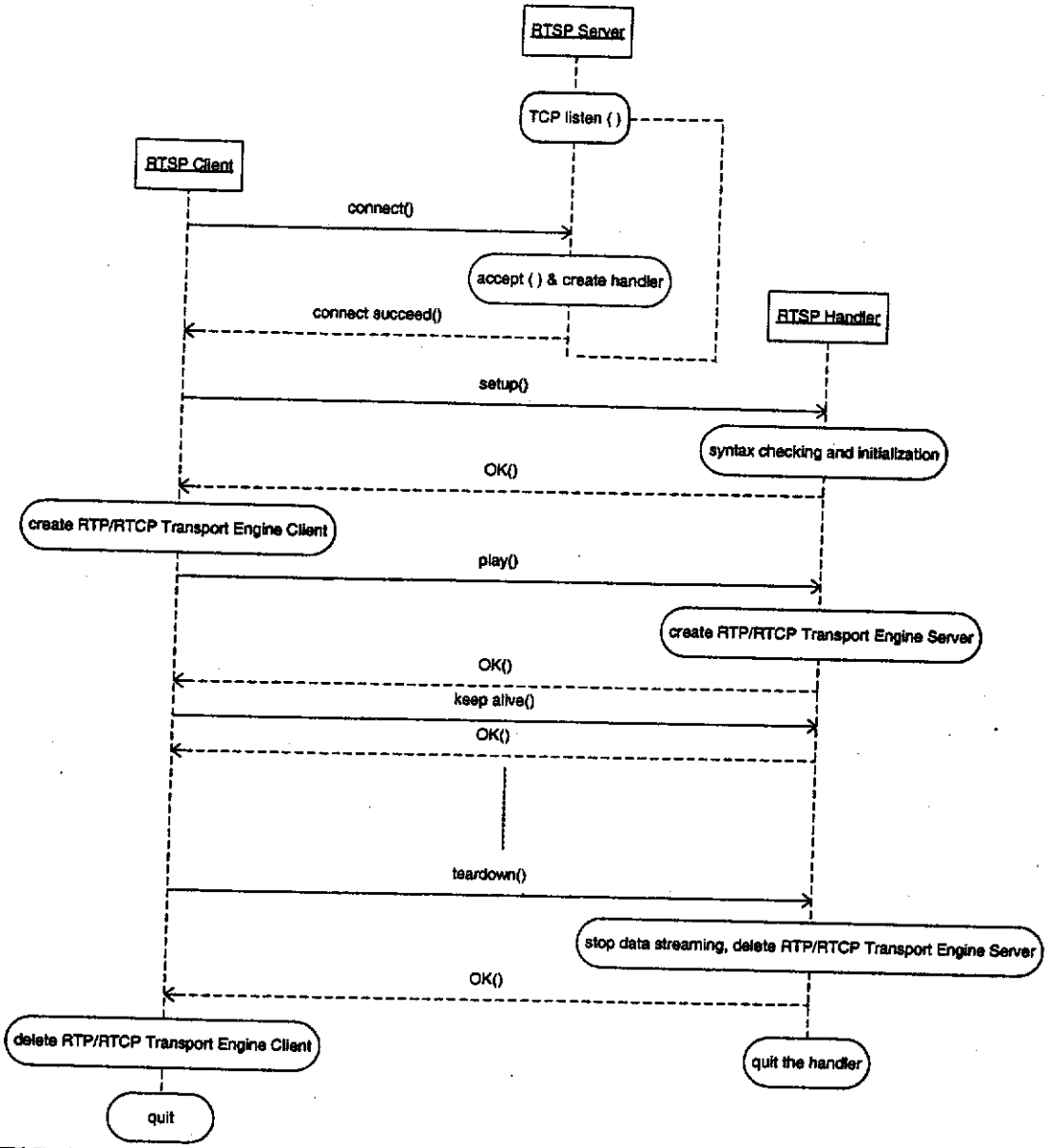


FIG. 2 Diagram showing the overview of the RTSP session procedure.

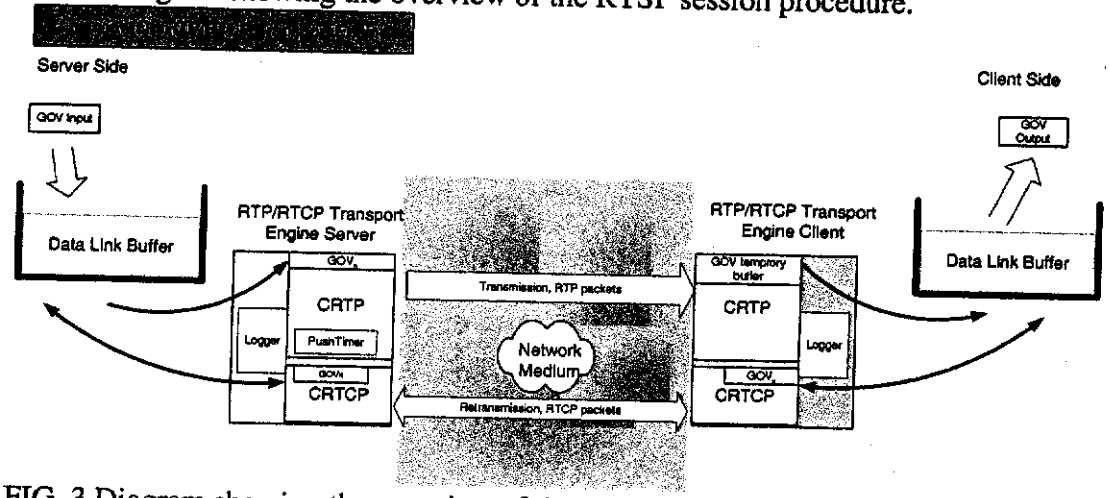


FIG. 3 Diagram showing the overview of the RTP/RTCP Transport Engine.

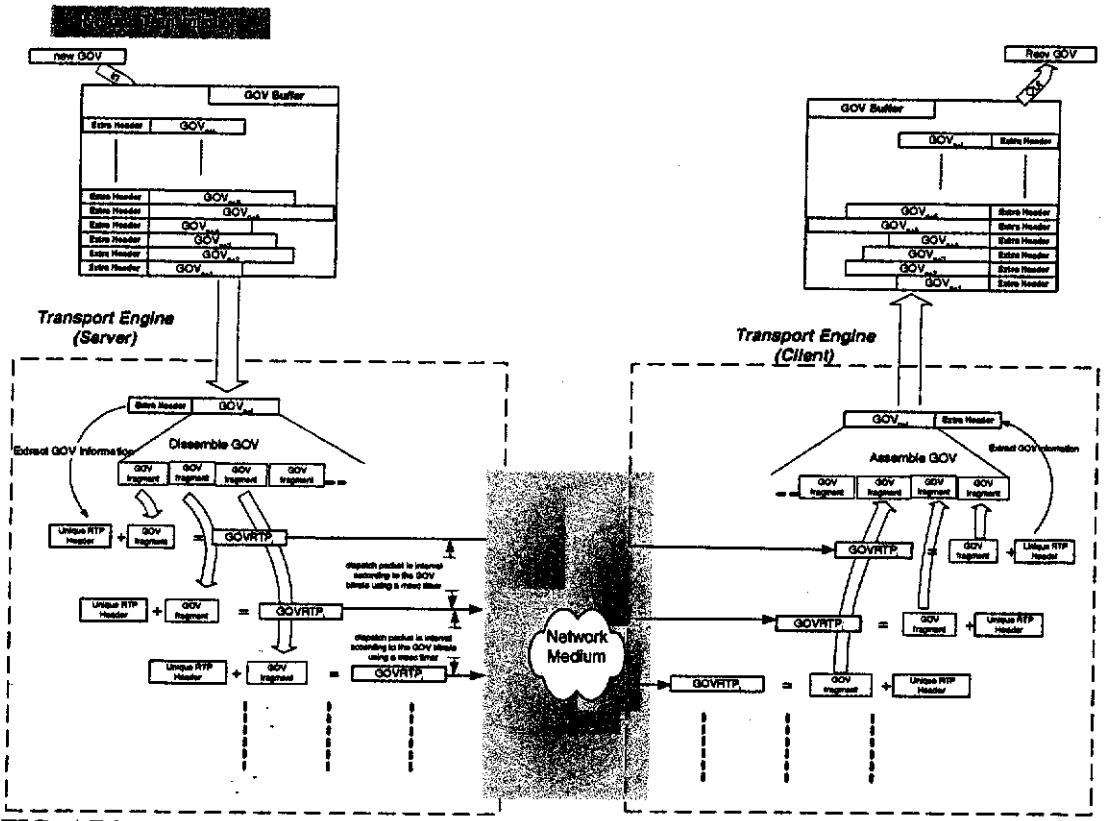


FIG. 4 Diagram showing the overview of the normal data transmission between the RTP/RTCP Transport Engines.

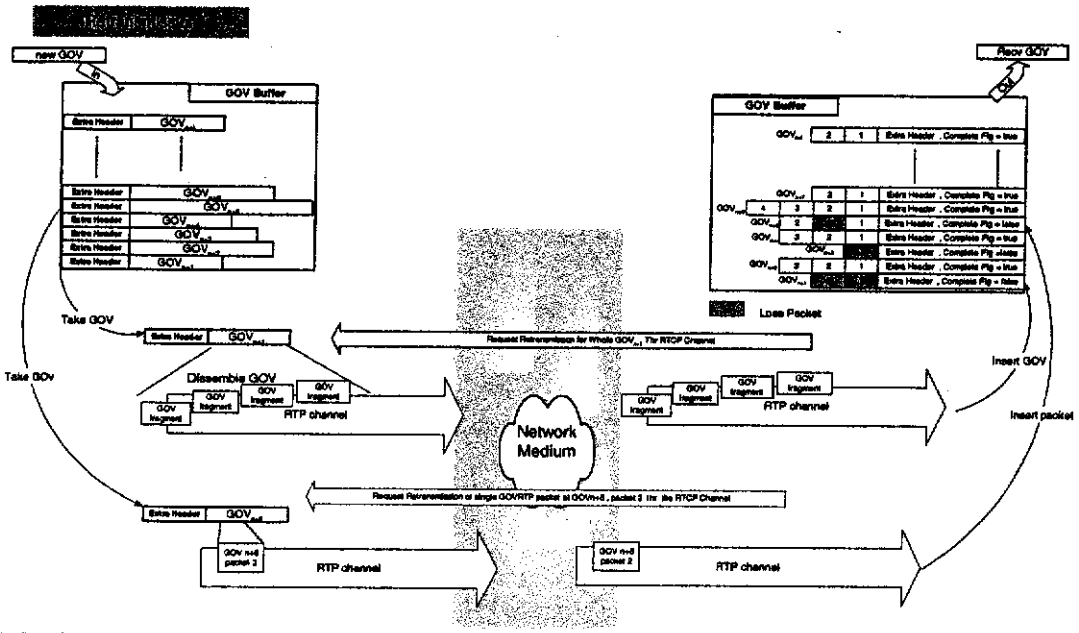


FIG. 5 Diagram showing the overview of the data retransmission between the RTP/RTCP Transport Engines.

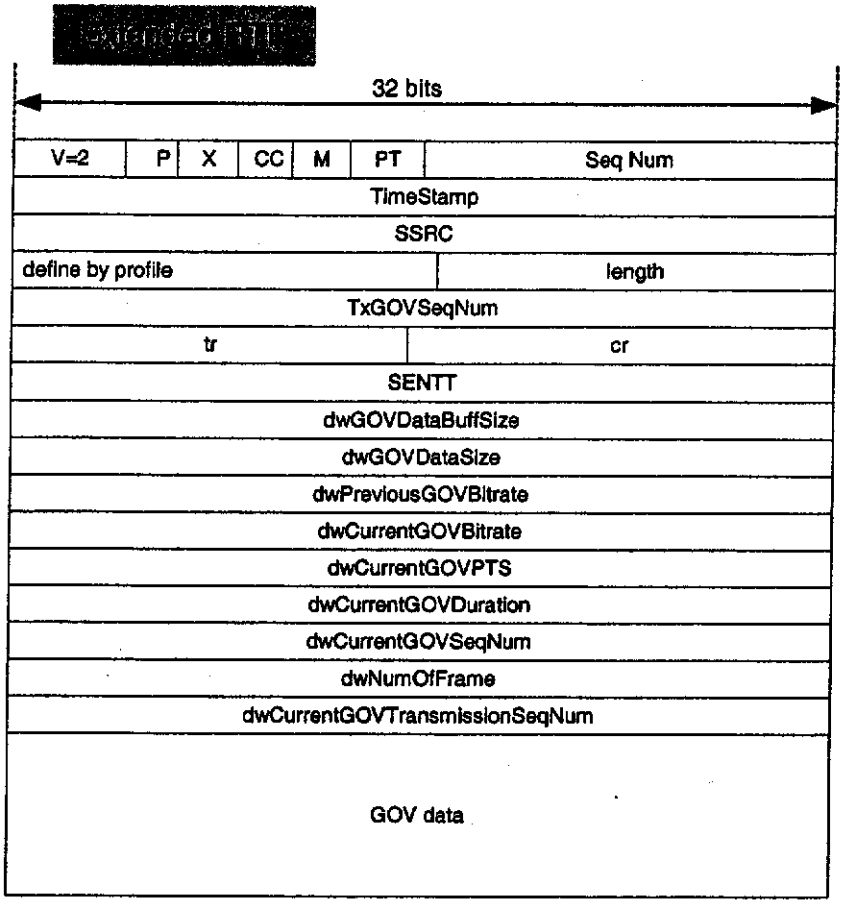


FIG. 6 Diagram showing the structure of the Extended RTP Packet.

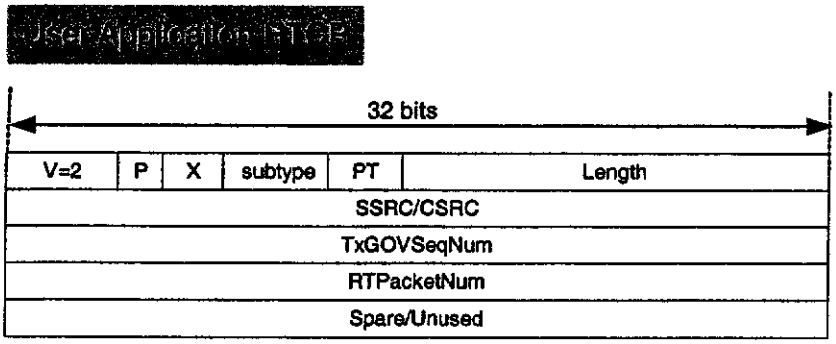


FIG. 7 Diagram showing the structure of the User Application RTCP Packet.

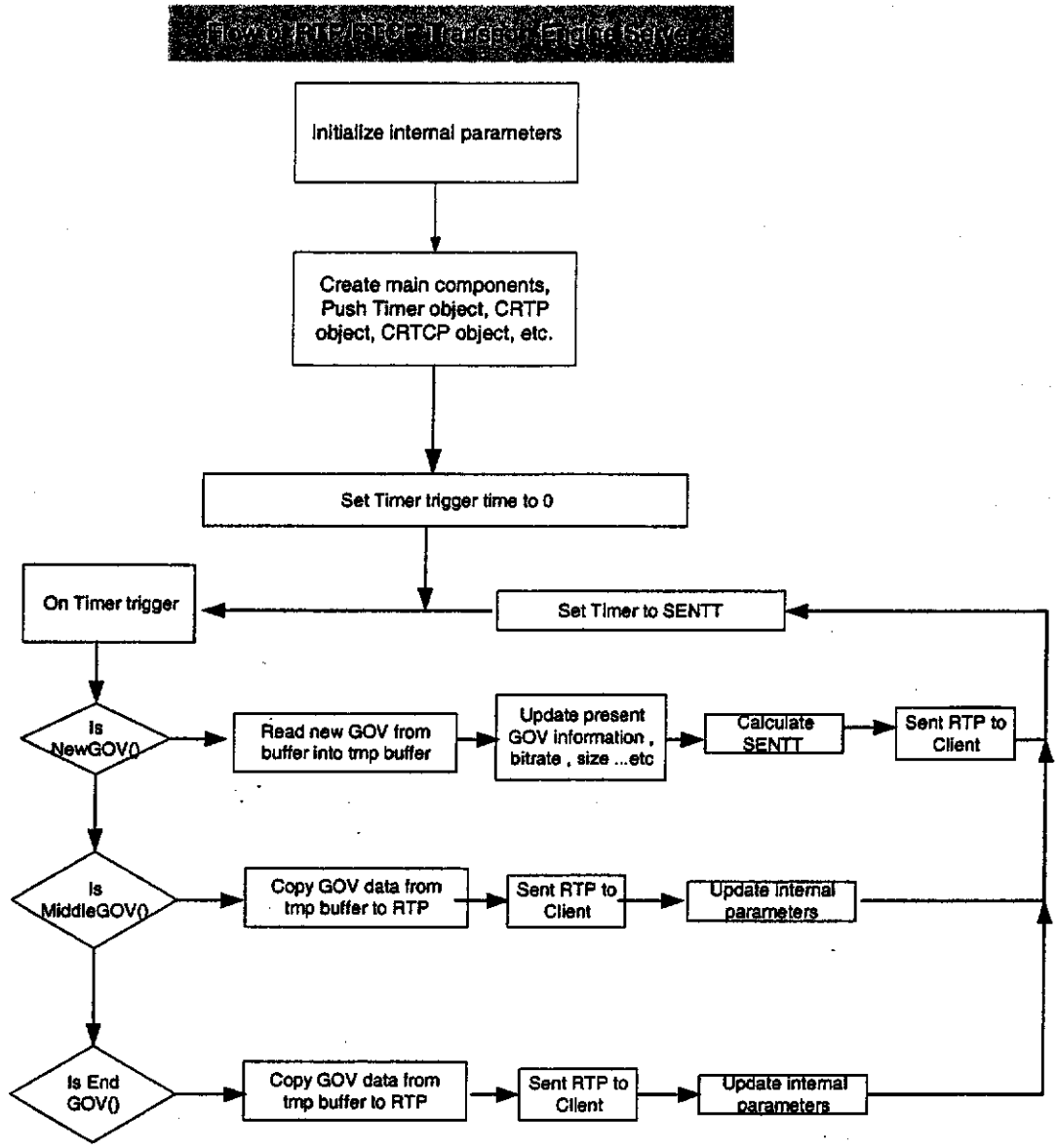


FIG. 8 An operation flowchart schematically showing the processing operation of a RTP/RTCP Transport Engine Server.

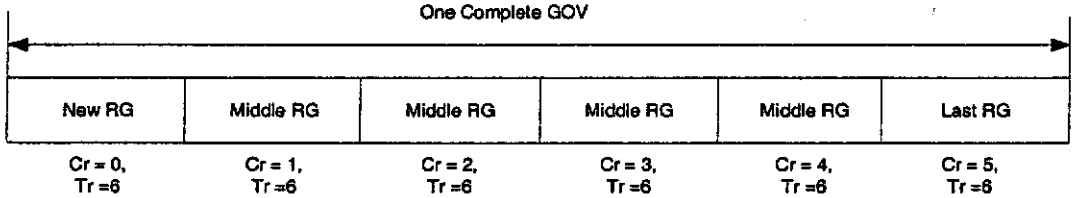


FIG. 9 A diagram showing the structure of a complete GOV with RG (RTP GOV).

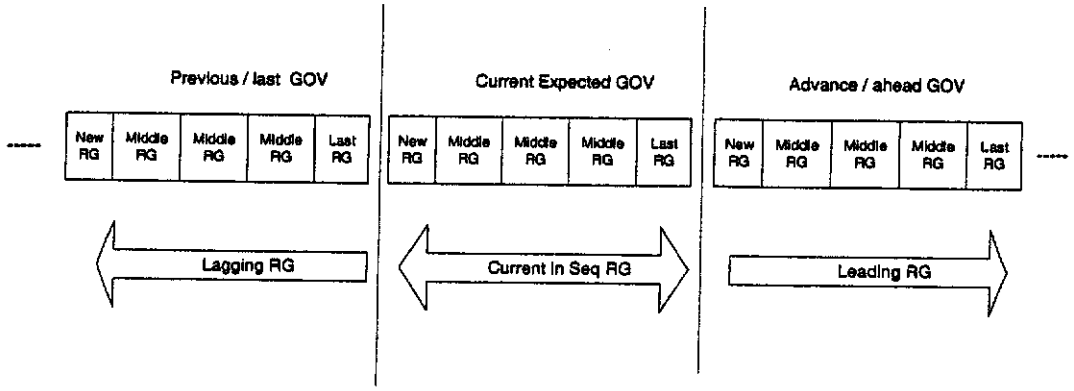


FIG. 10 A diagram showing the classification of three different RGs regarding the UDP out-of-sequence problem.

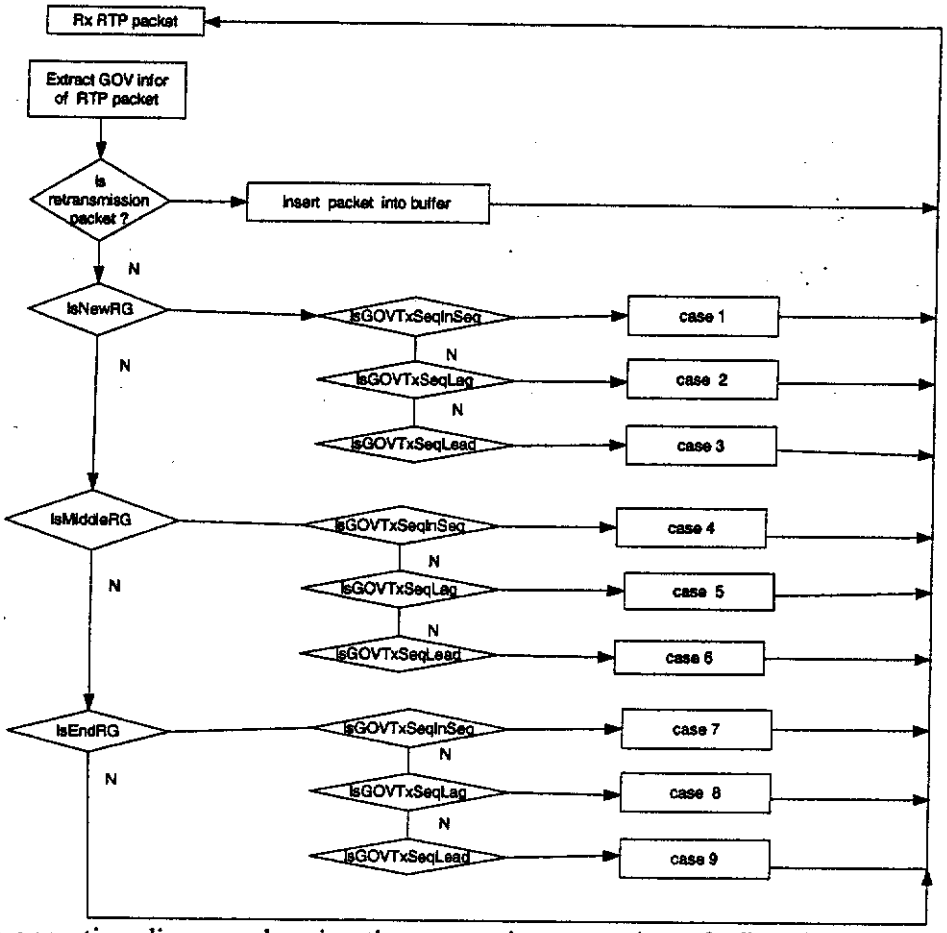


FIG. 11 An operation diagram showing the processing operation of a RTP/RTCP Transport Engine Client.

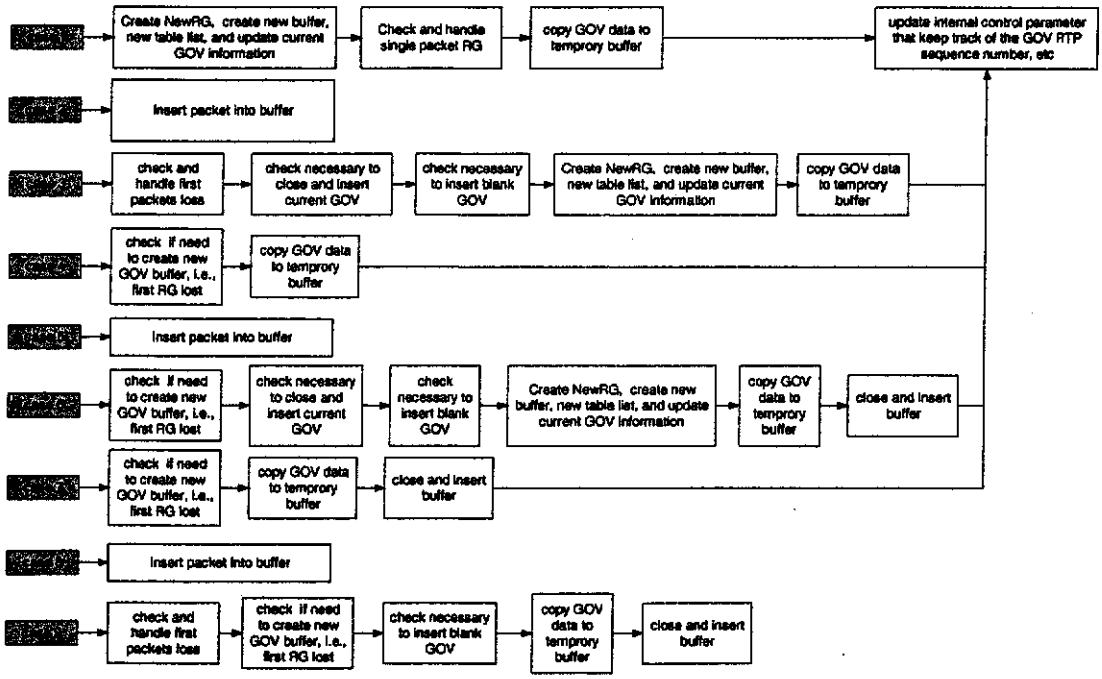


FIG. 12 An operation diagram listing the main nine different processes of a RTP/RTCP Transport Engine Client upon receiving a RTP packet.

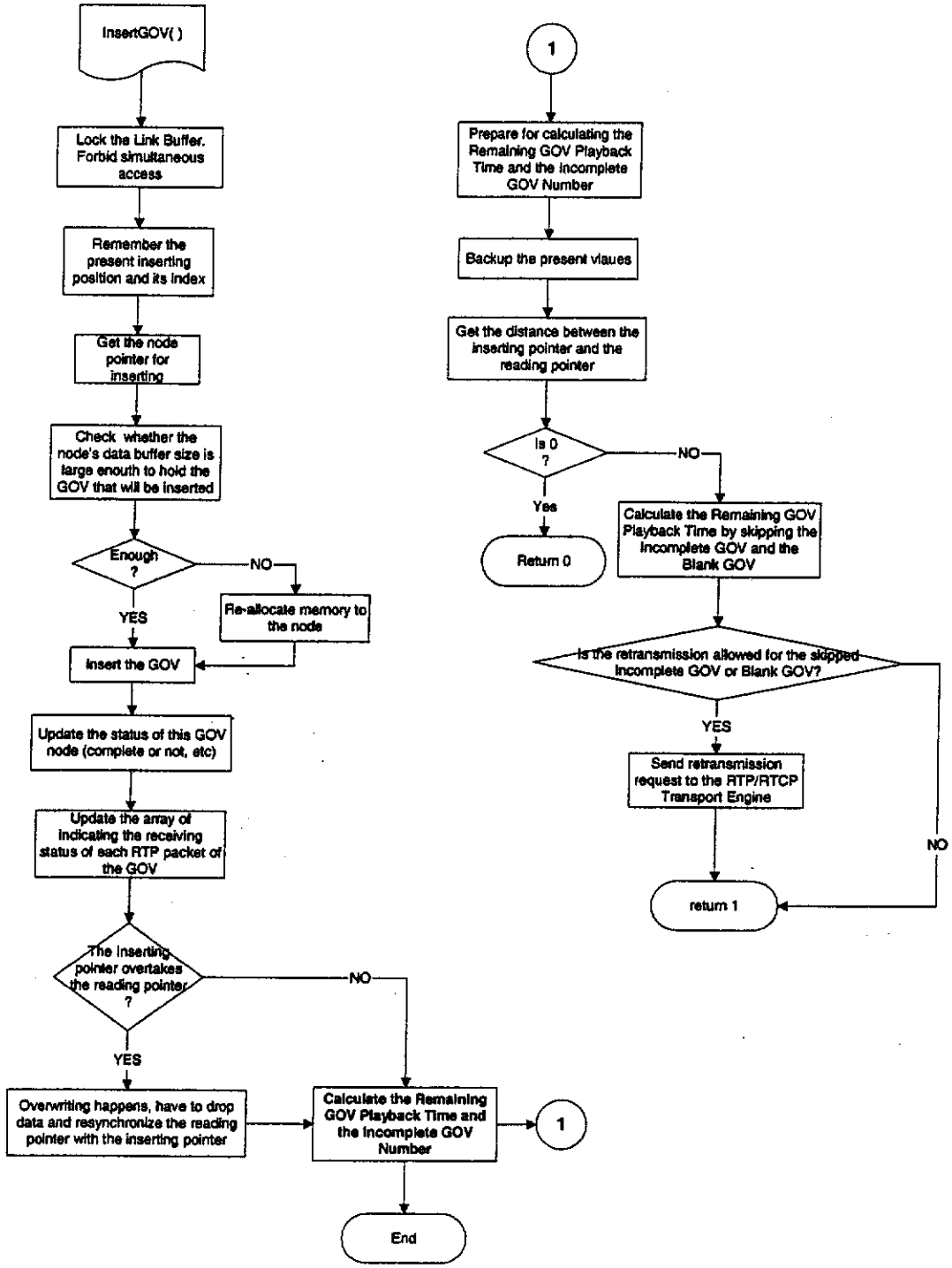


FIG. 13 A flowchart showing the process of GOV insertion in the Data Link Buffer (Client).

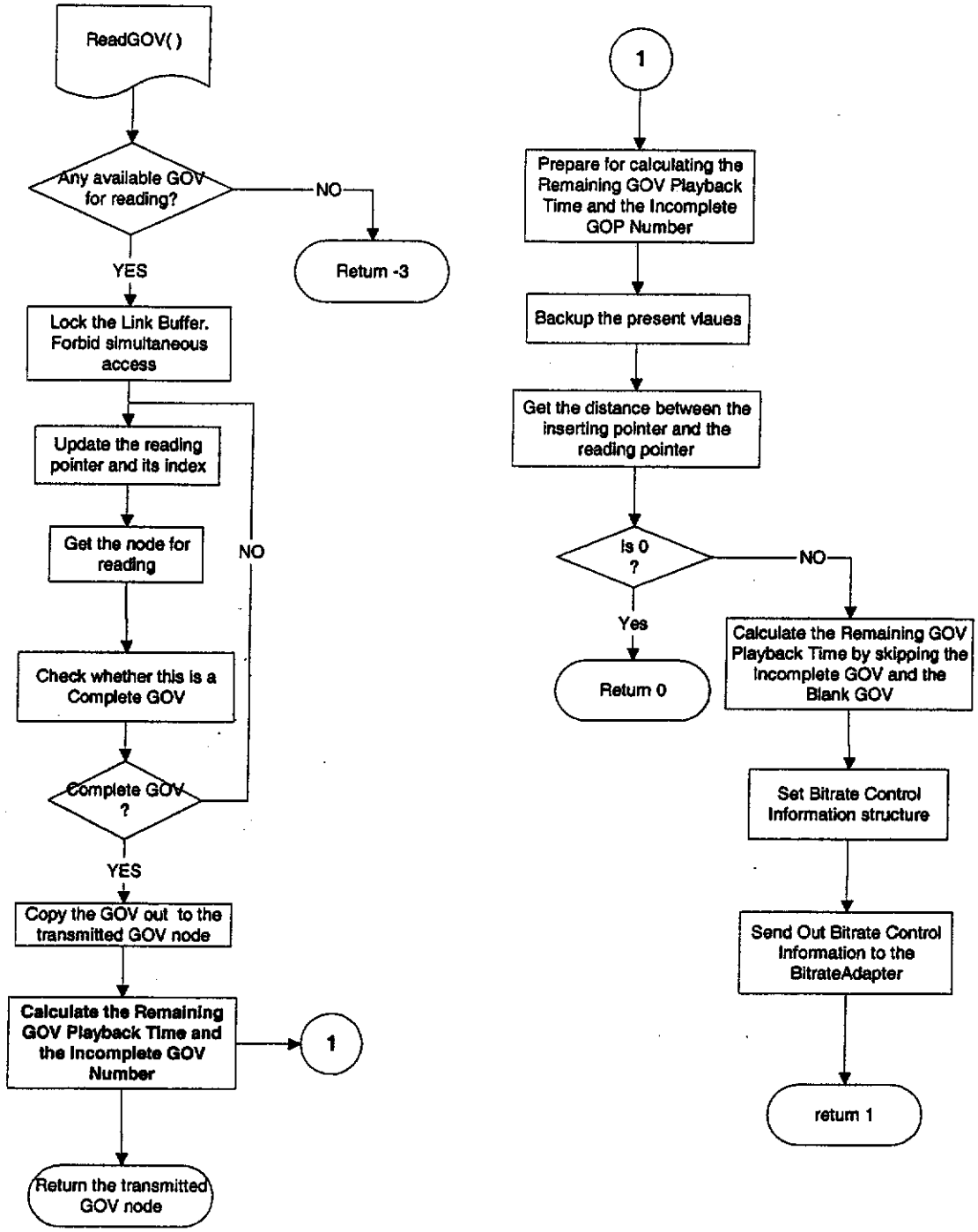


FIG. 14 A flowchart showing the process of GOV reading in the Data Link Buffer (Client).

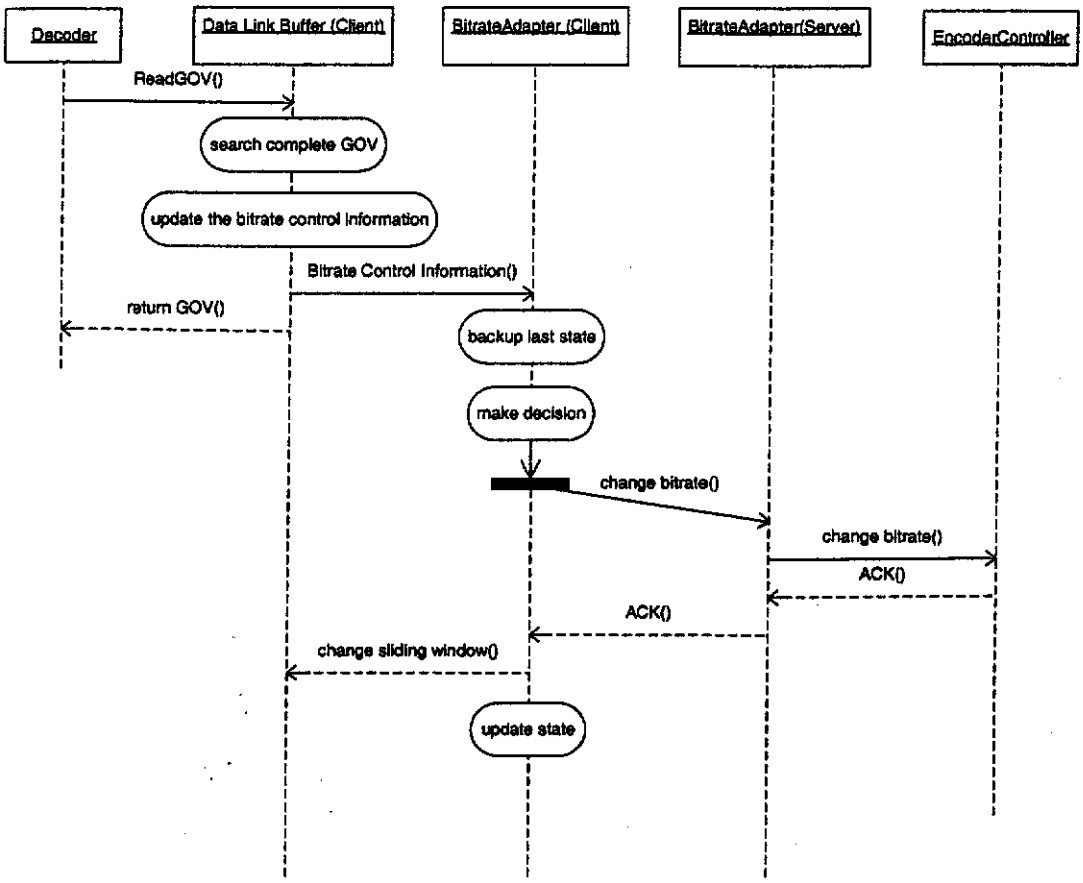


FIG. 15 A time sequence diagram showing the Bitrate Control message flows in the current invention.

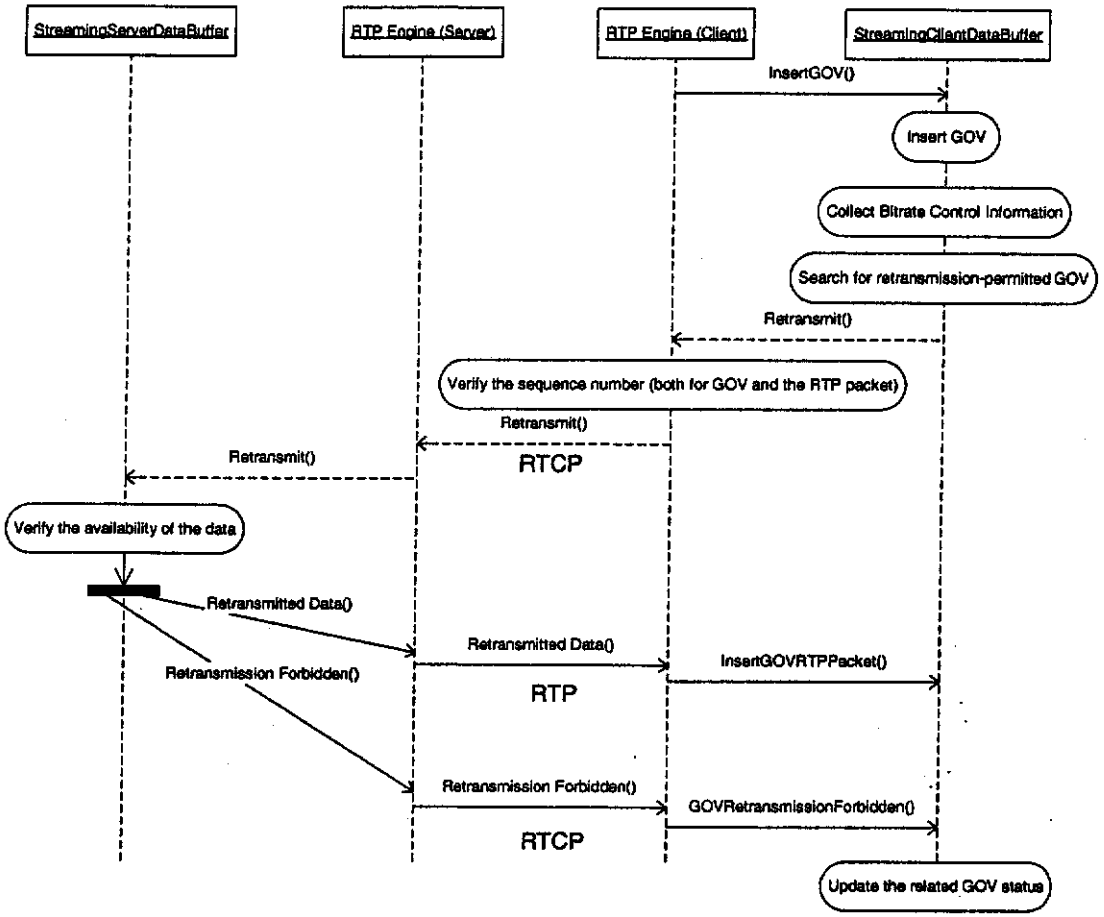


FIG. 16 A time sequence diagram showing the Retransmission message flows in the current invention.

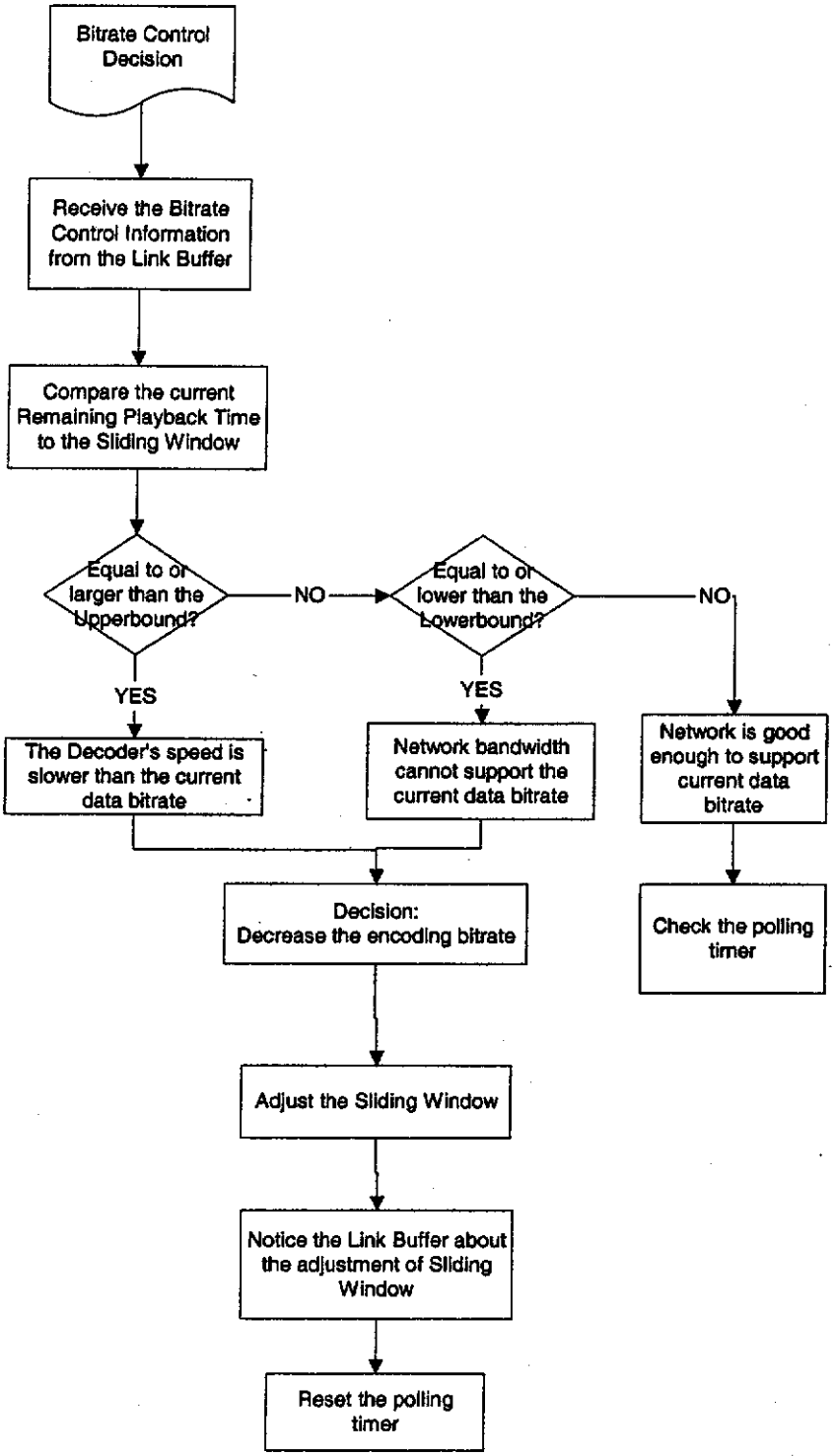


FIG. 17 A diagram showing the process of Bitrate Control Decision making.

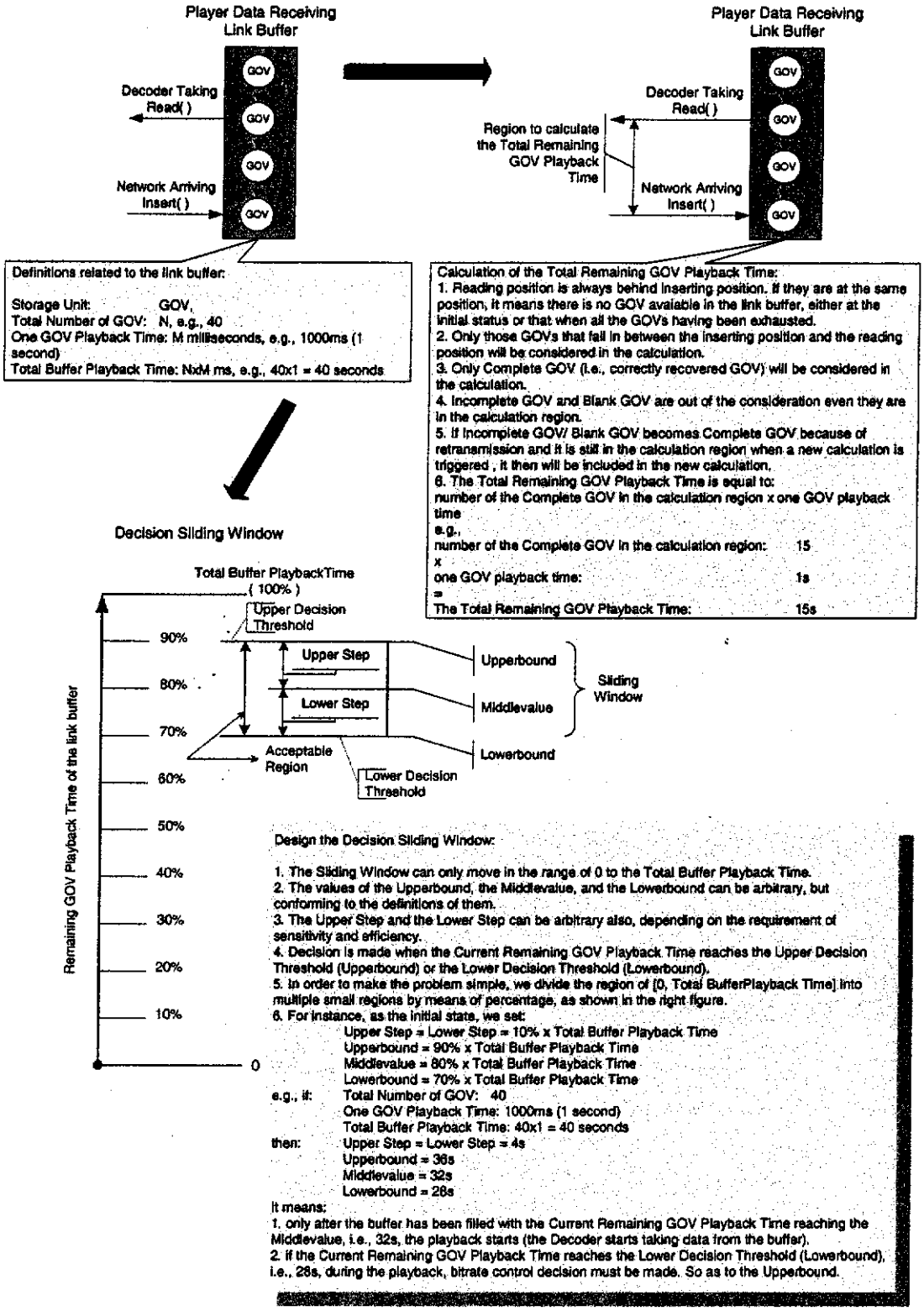


FIG. 18 An illustrative diagram showing the basic definitions of the Bitrate Control mechanism.

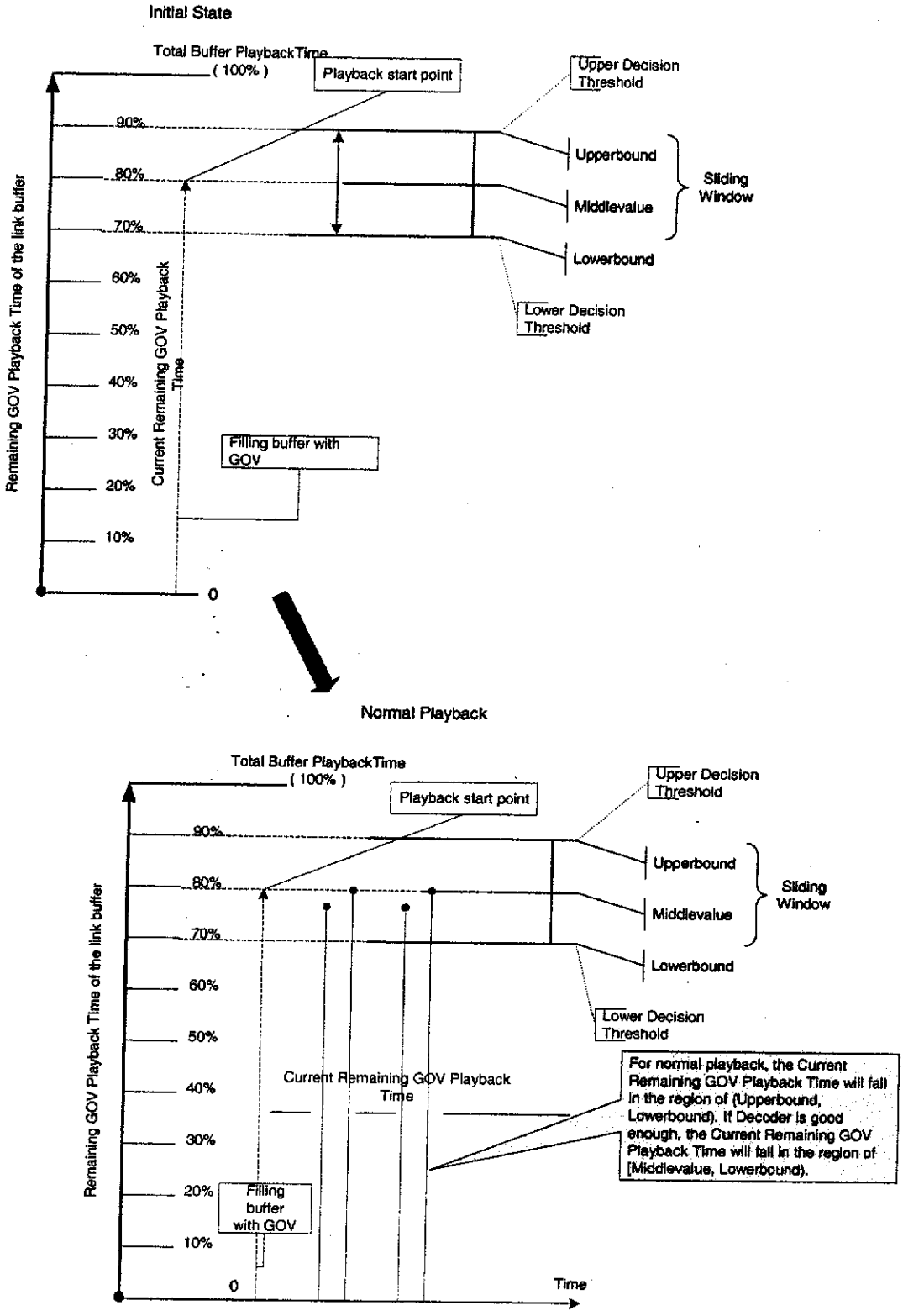


FIG. 19 An illustrative diagram showing the normal playback scenario of the Bitrate Control mechanism.

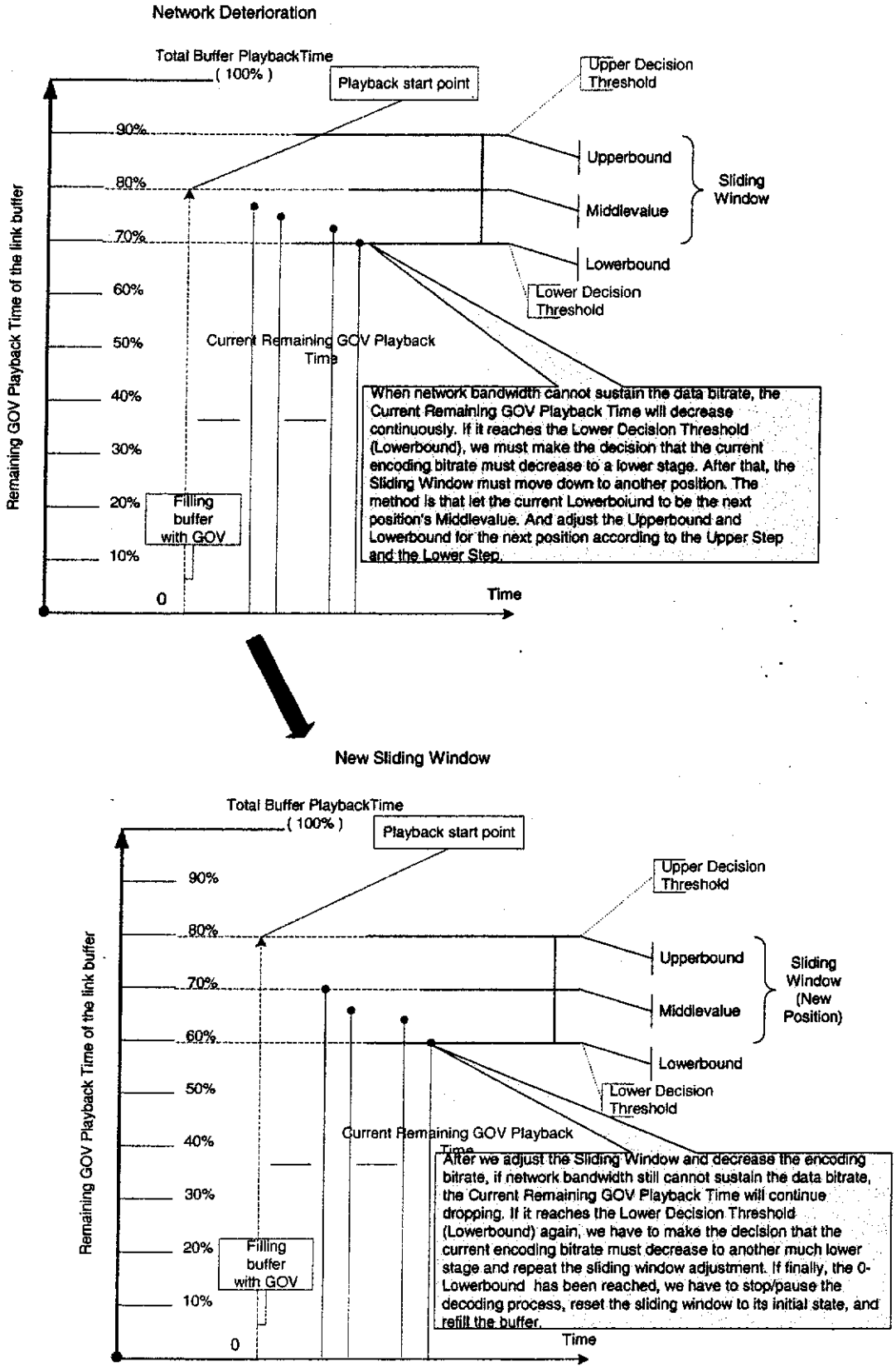


FIG. 20 An illustrative diagram showing the network deterioration scenario of the Bitrate Control mechanism.

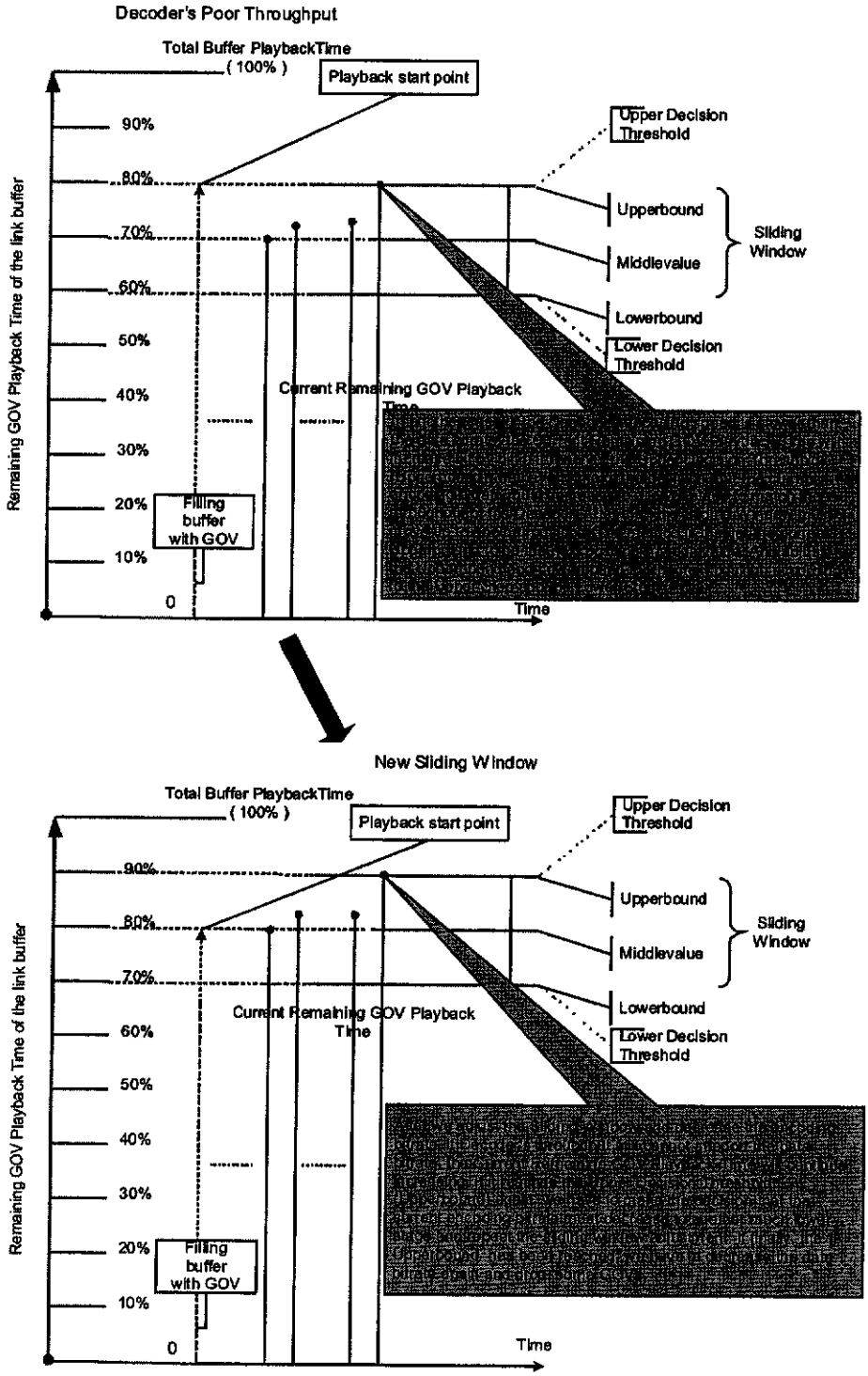


FIG. 21 An illustrative diagram showing the Decoder's poor throughput scenario of the Bitrate Control mechanism.

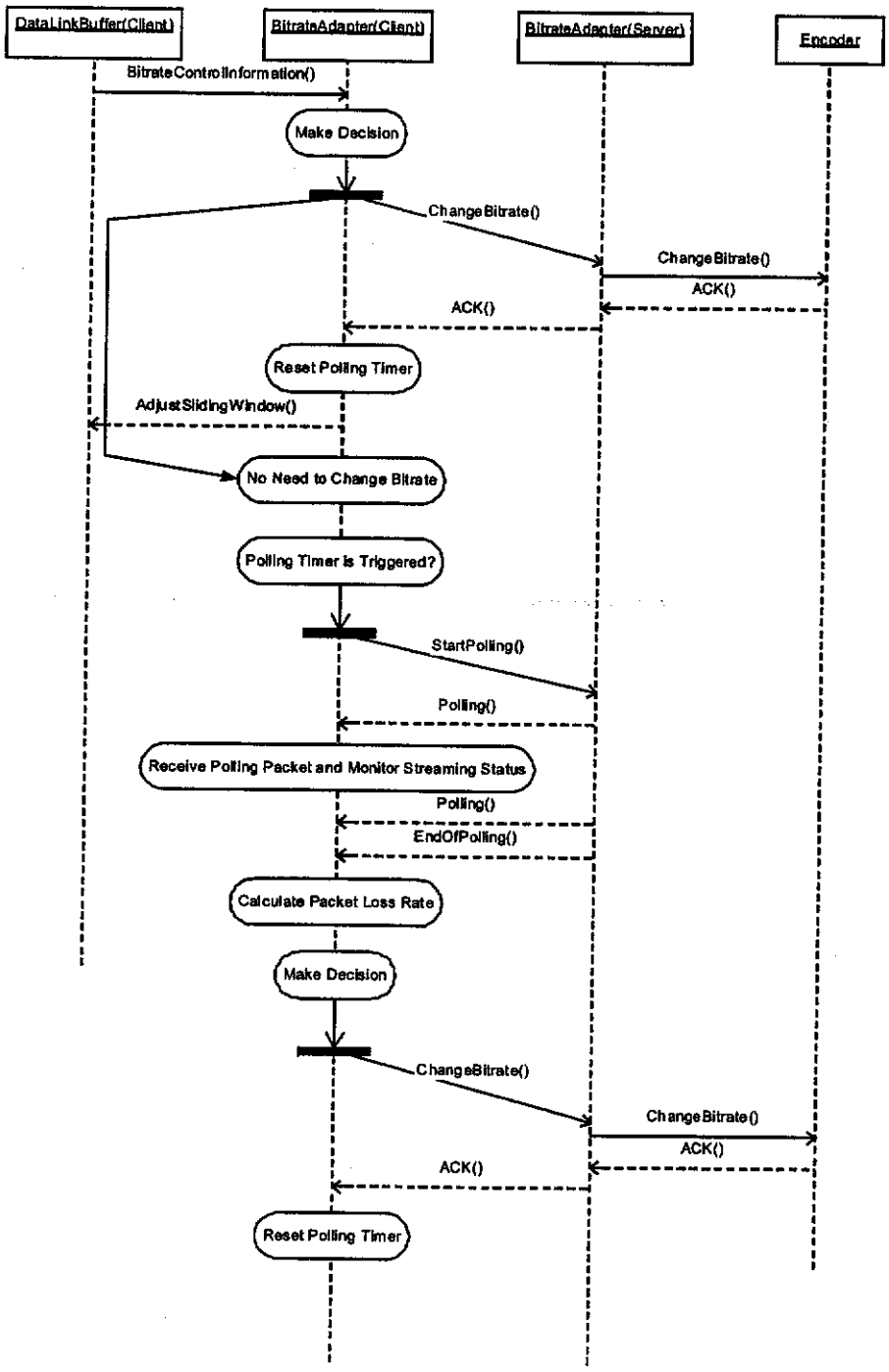


FIG. 22 A time sequence diagram showing the polling process.

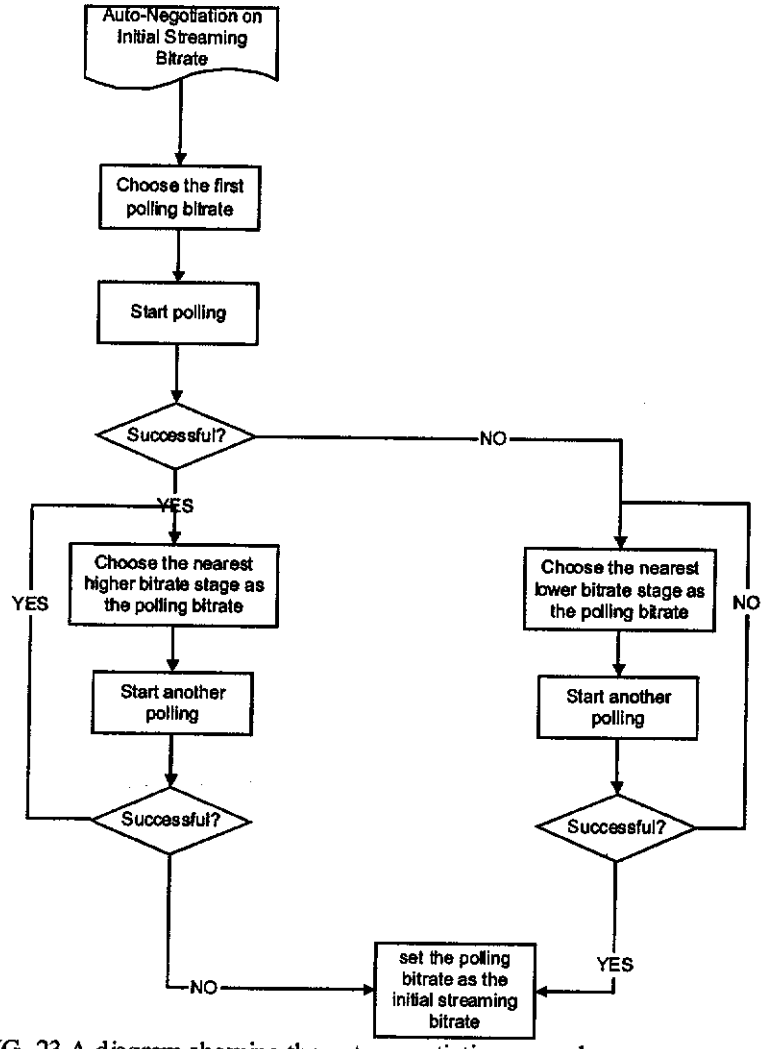


FIG. 23 A diagram showing the auto-negotiation procedure.