



图 1

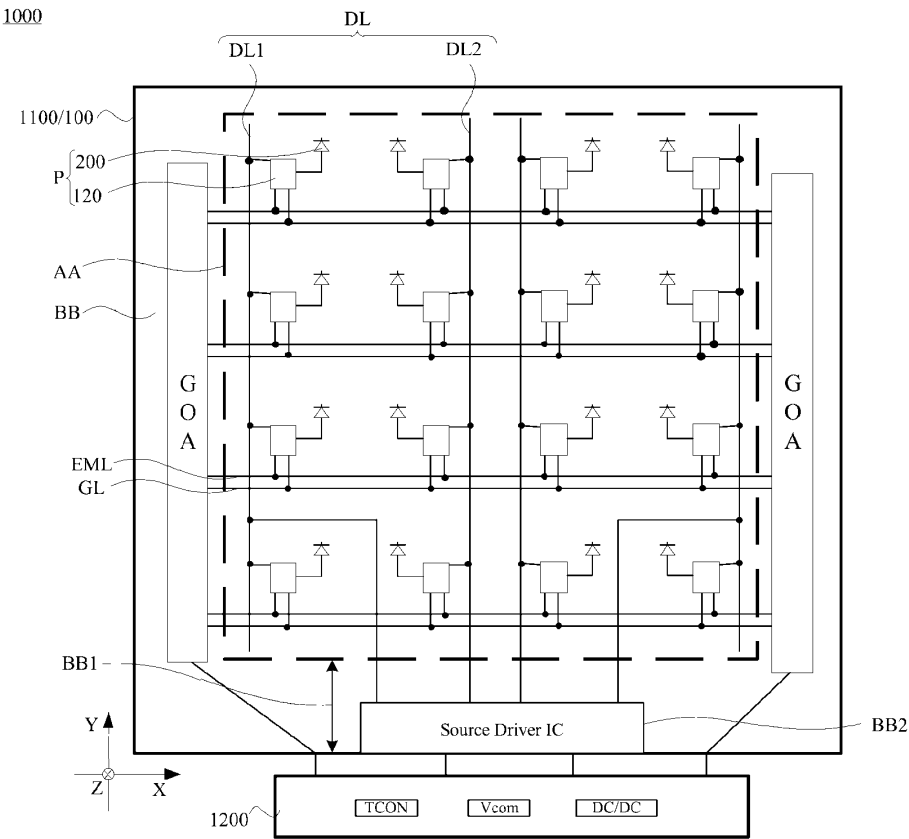


图 2

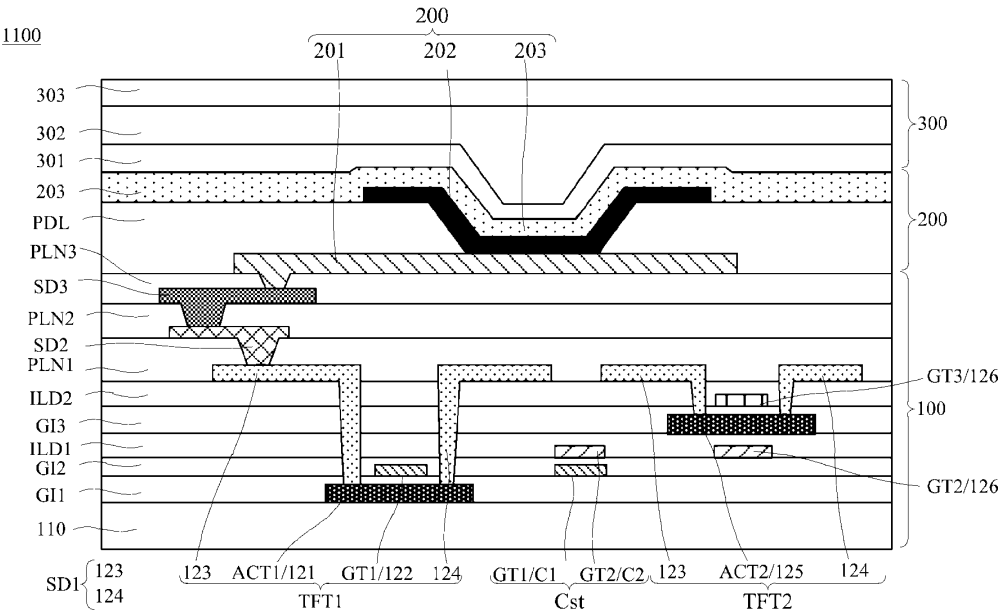


图 3

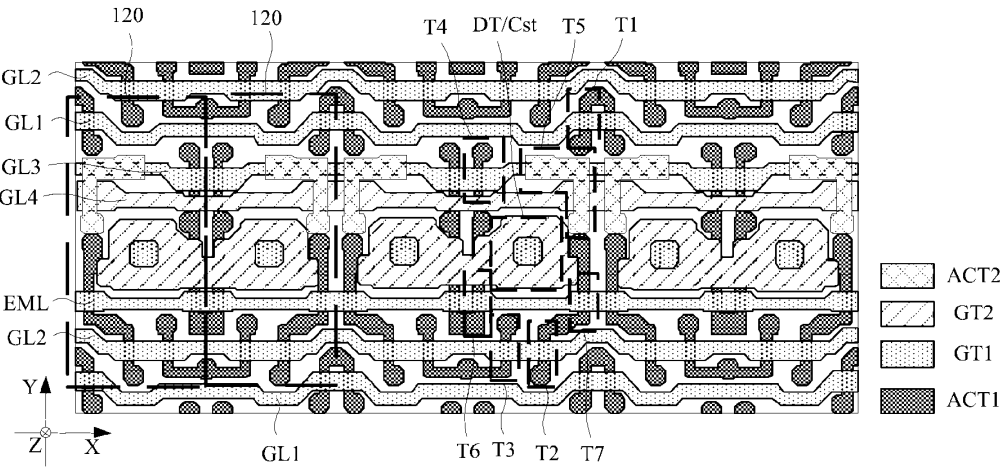


图 4

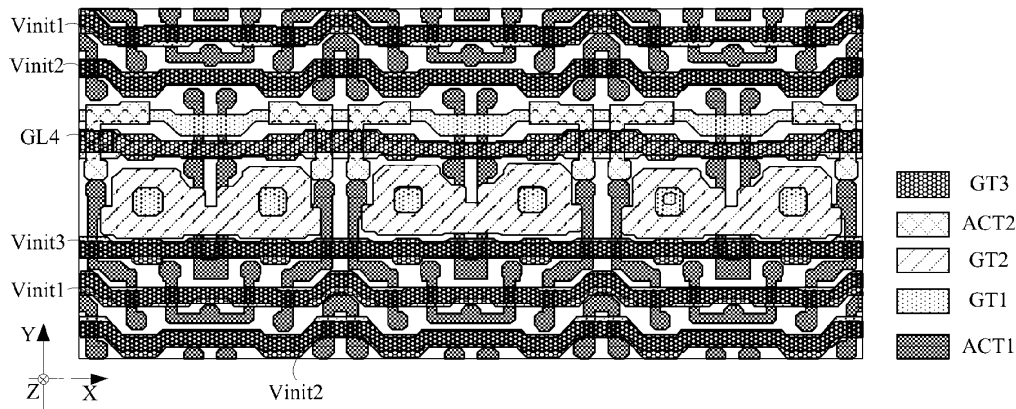


图 5

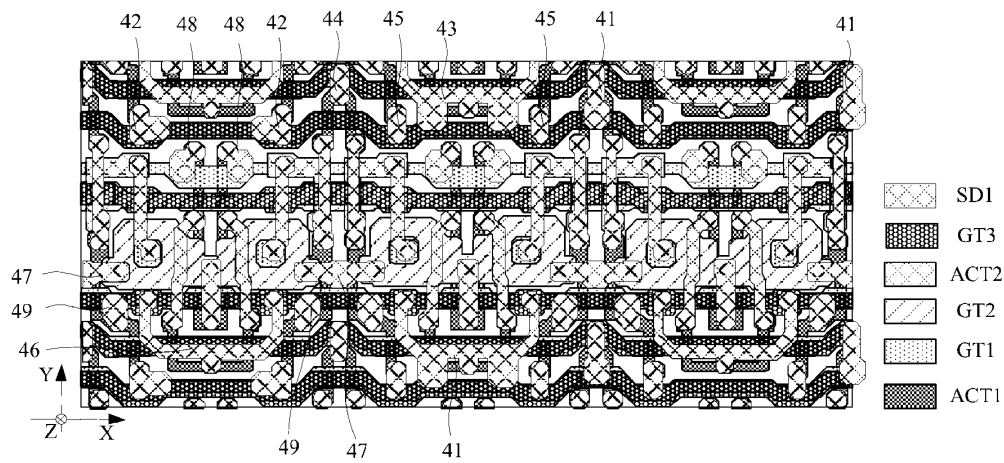


图 6

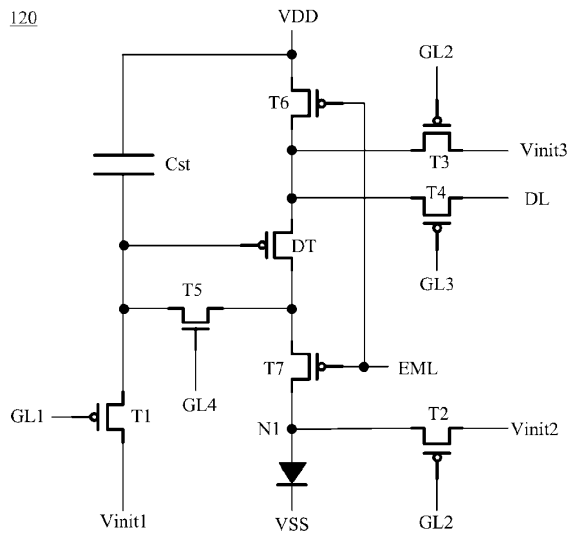


图 7

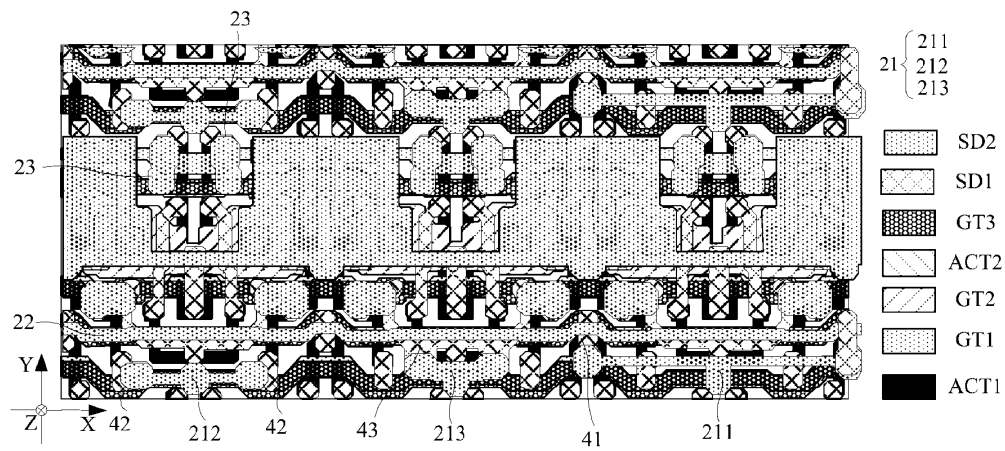


图 8

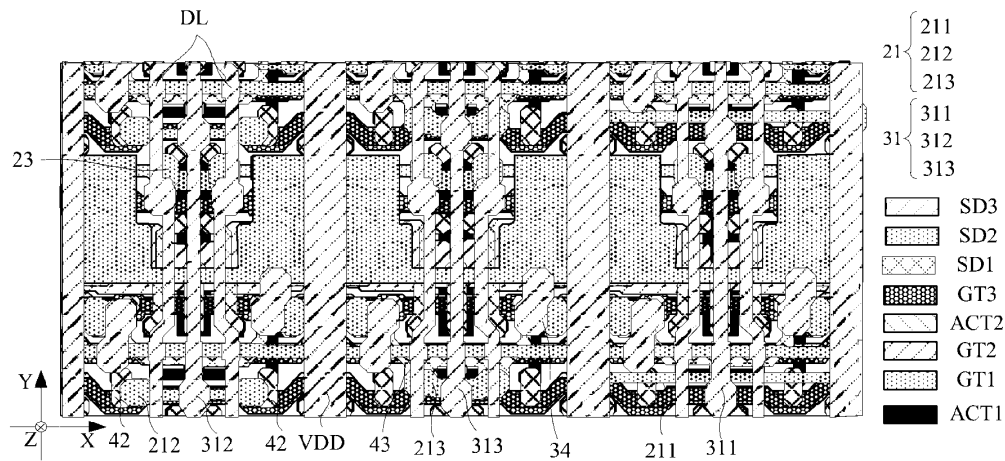


图 9

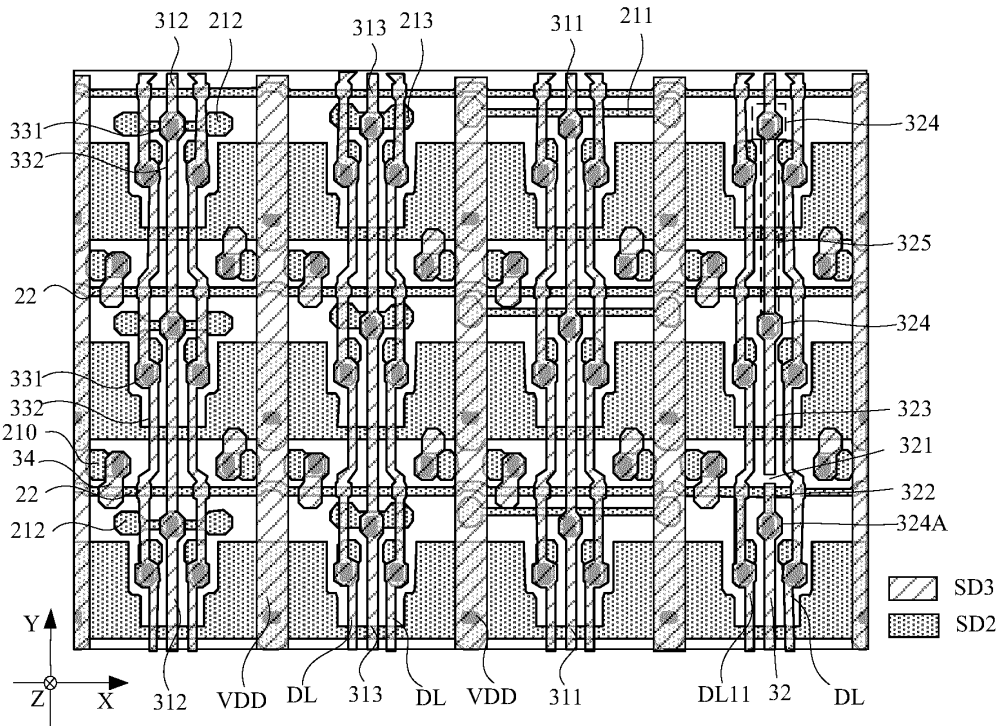


图 10

110

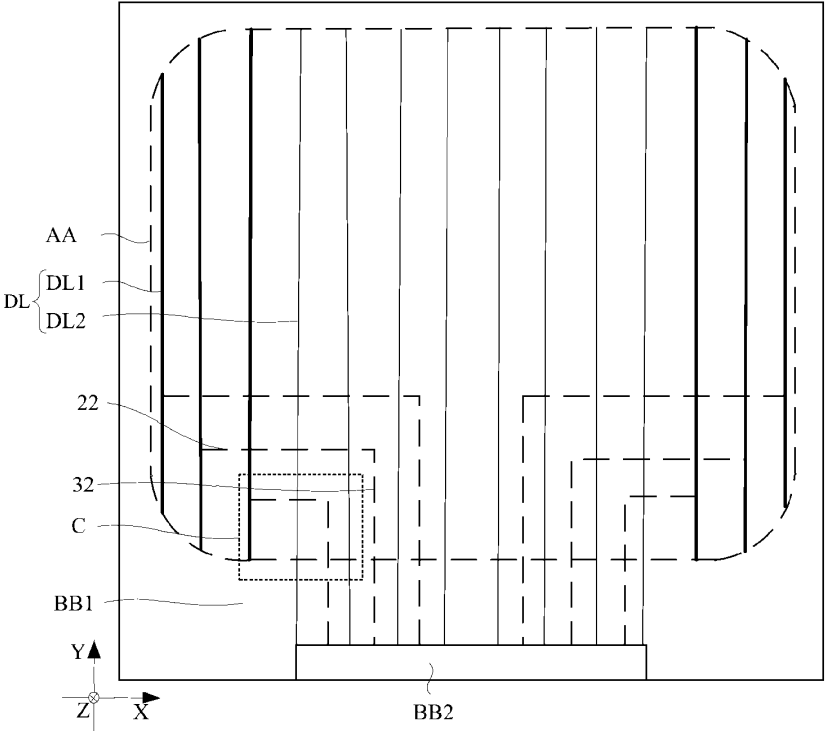


图 11

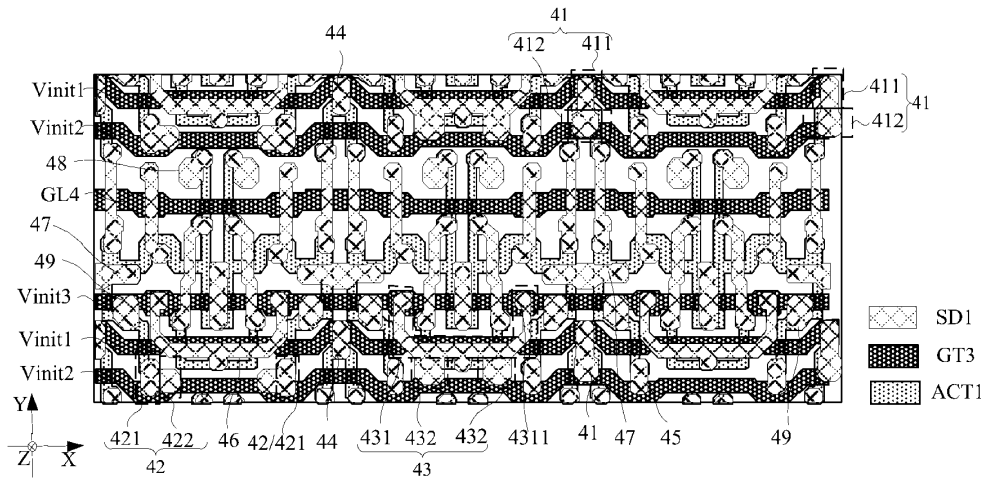


图 12

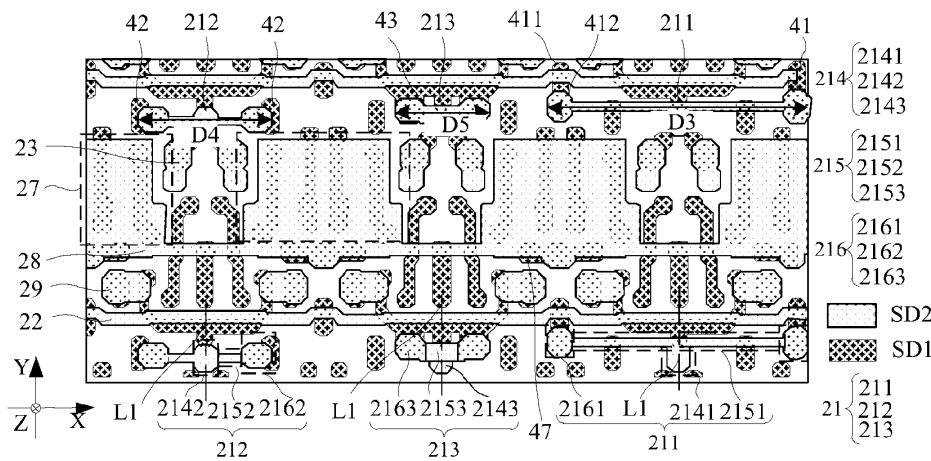


图 13

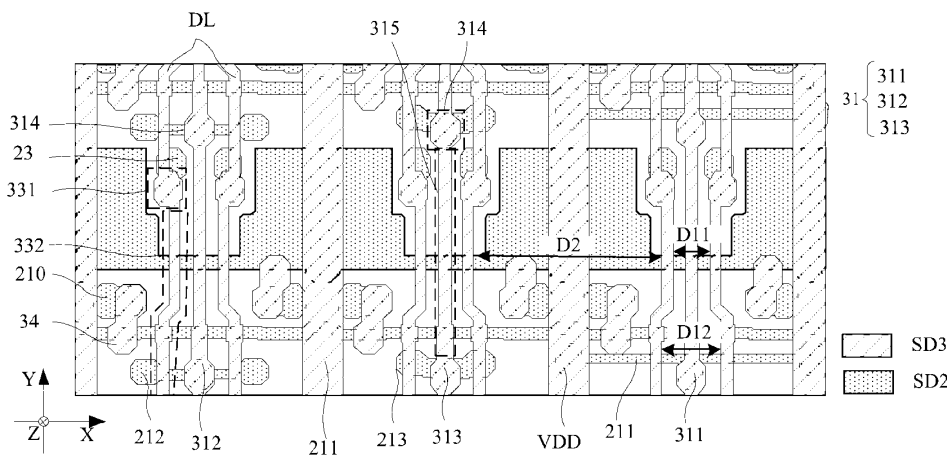


图 14

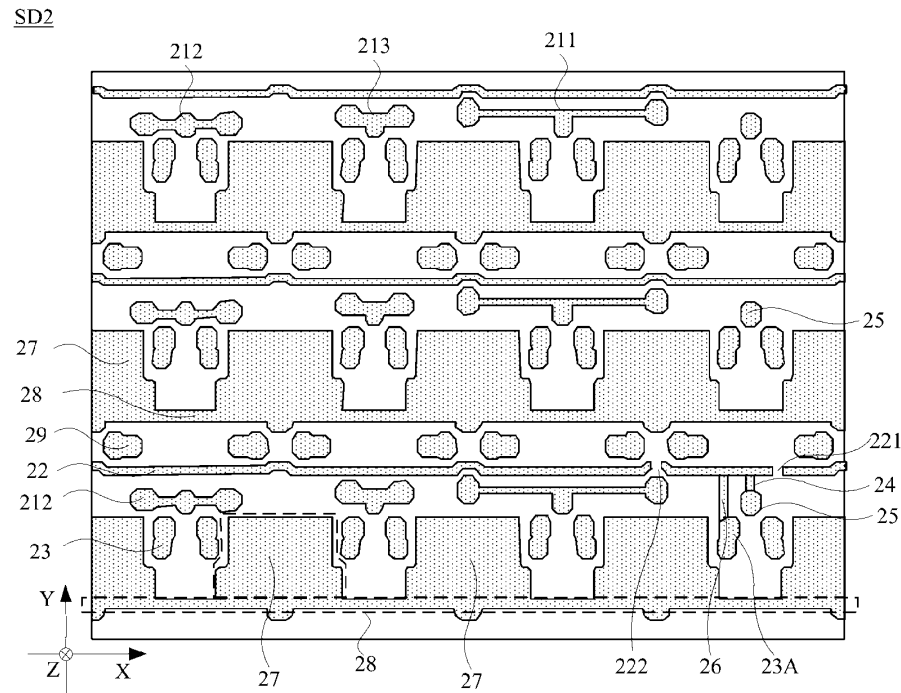


图 15

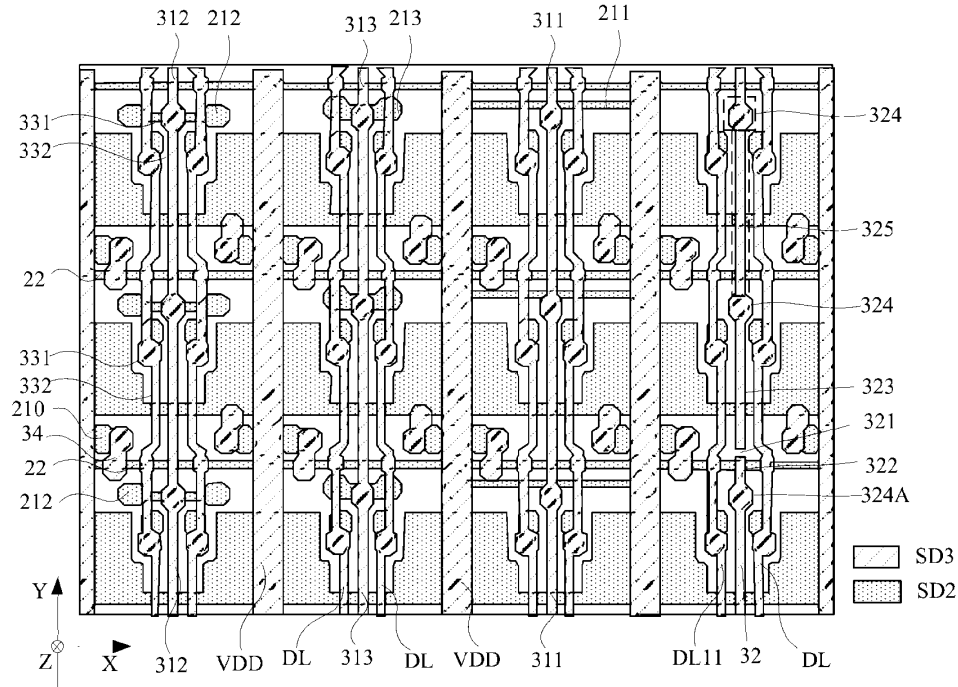


图 16

SD2

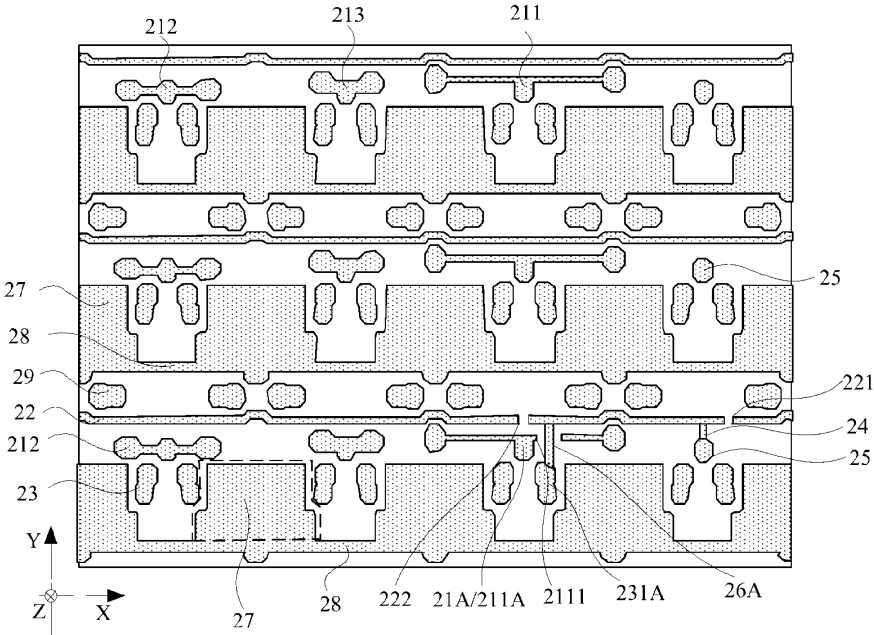


图 17

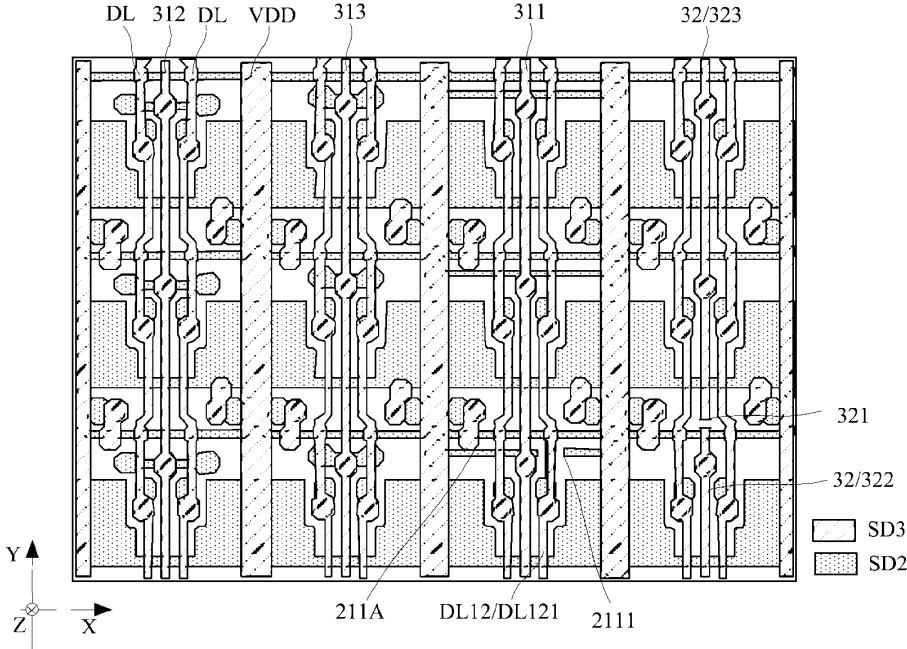


图 18

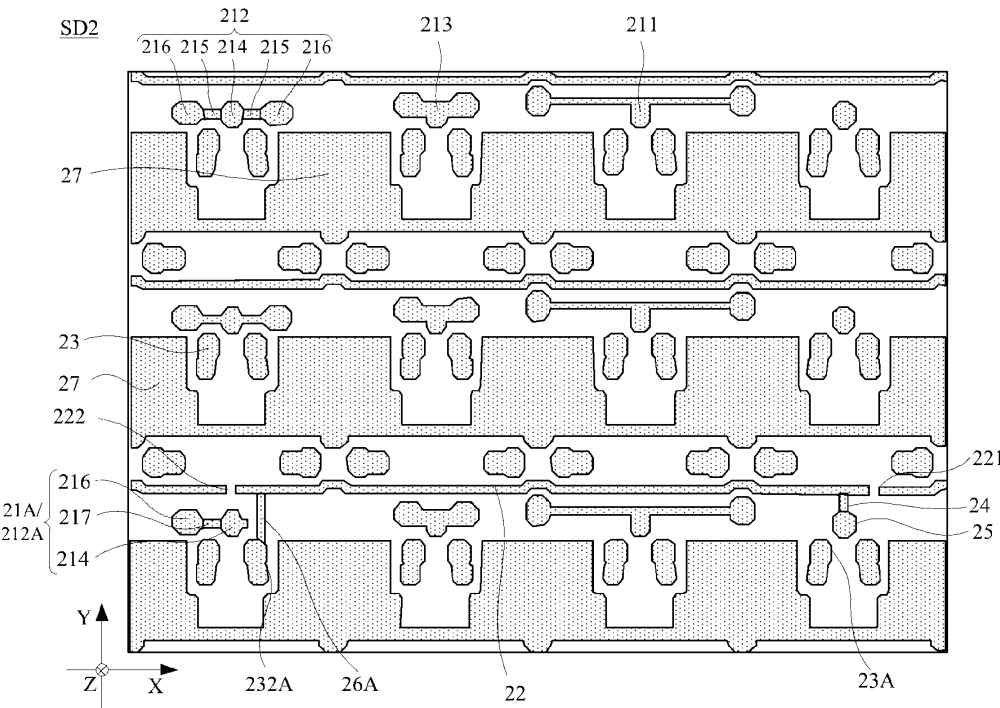


图 19

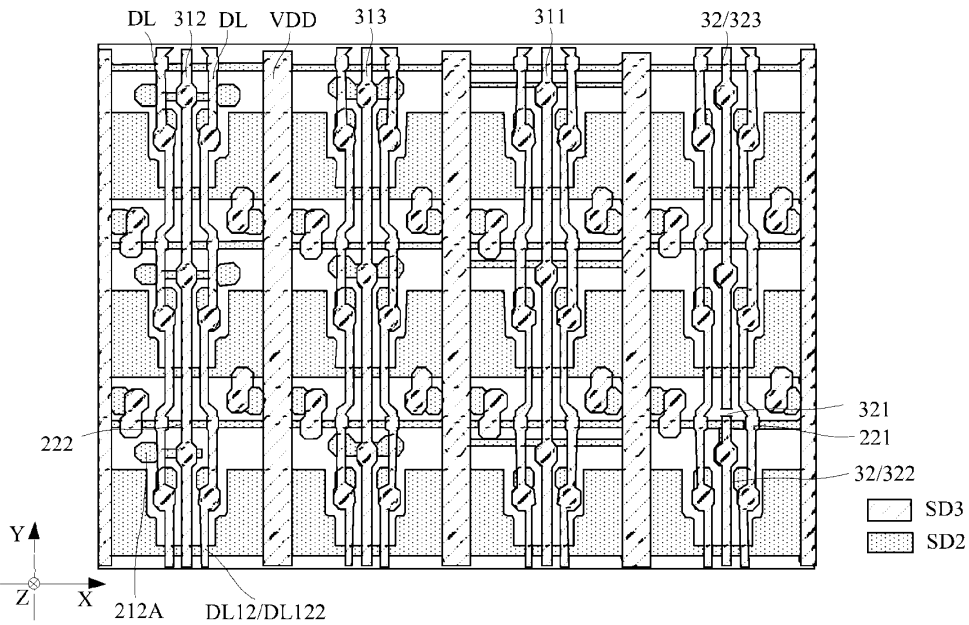


图 20

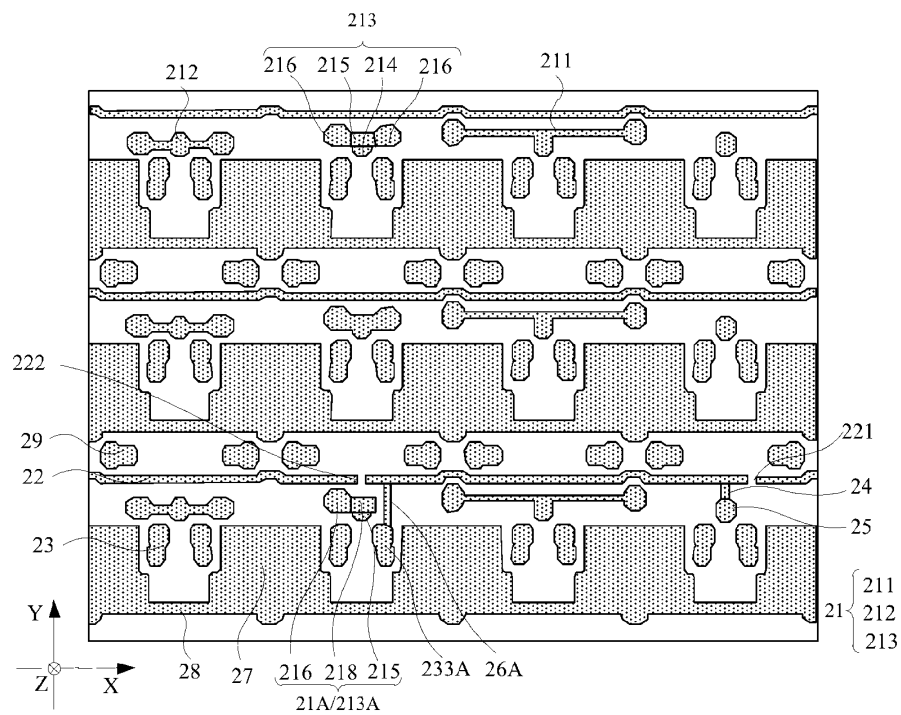


图 21

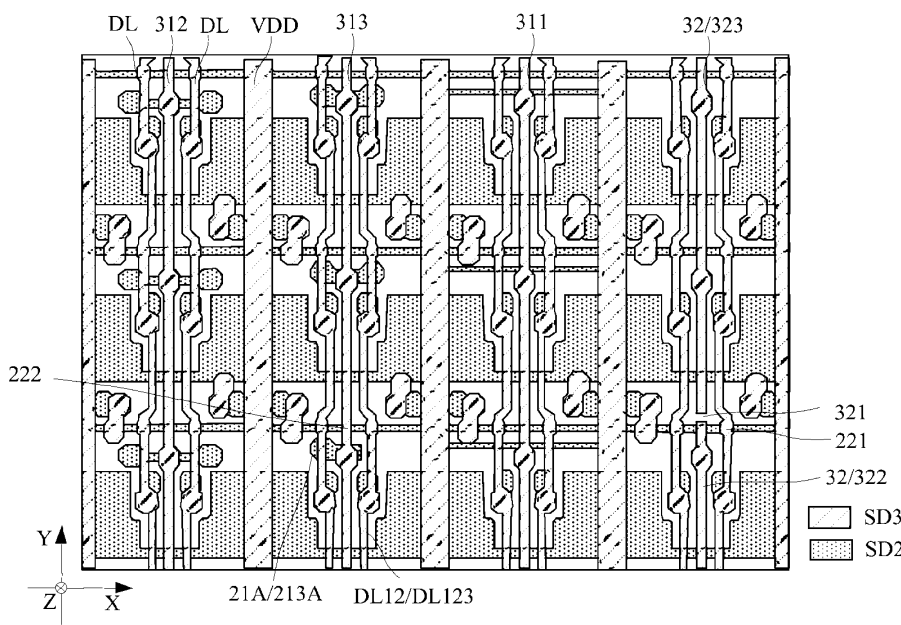


图 22



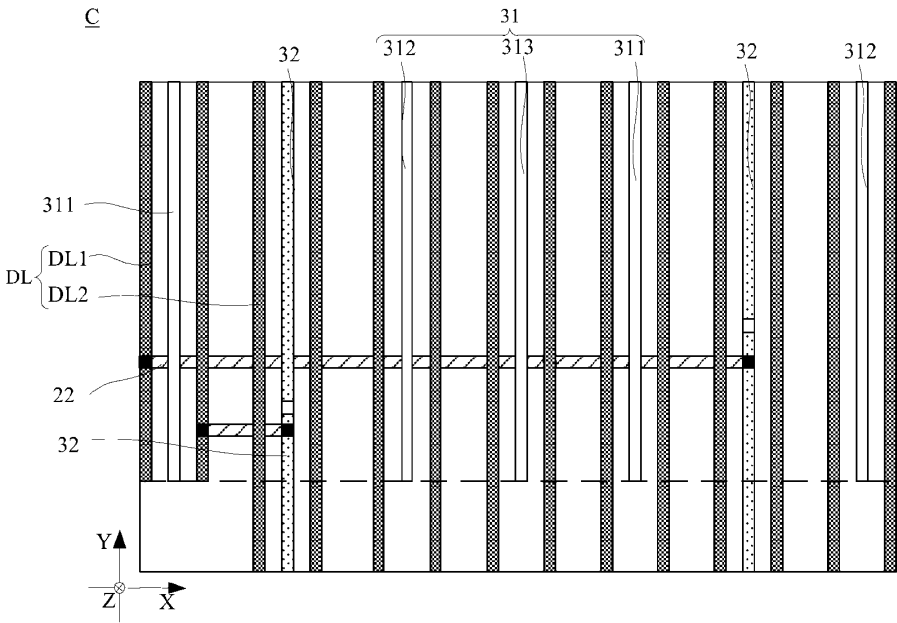


图 25