

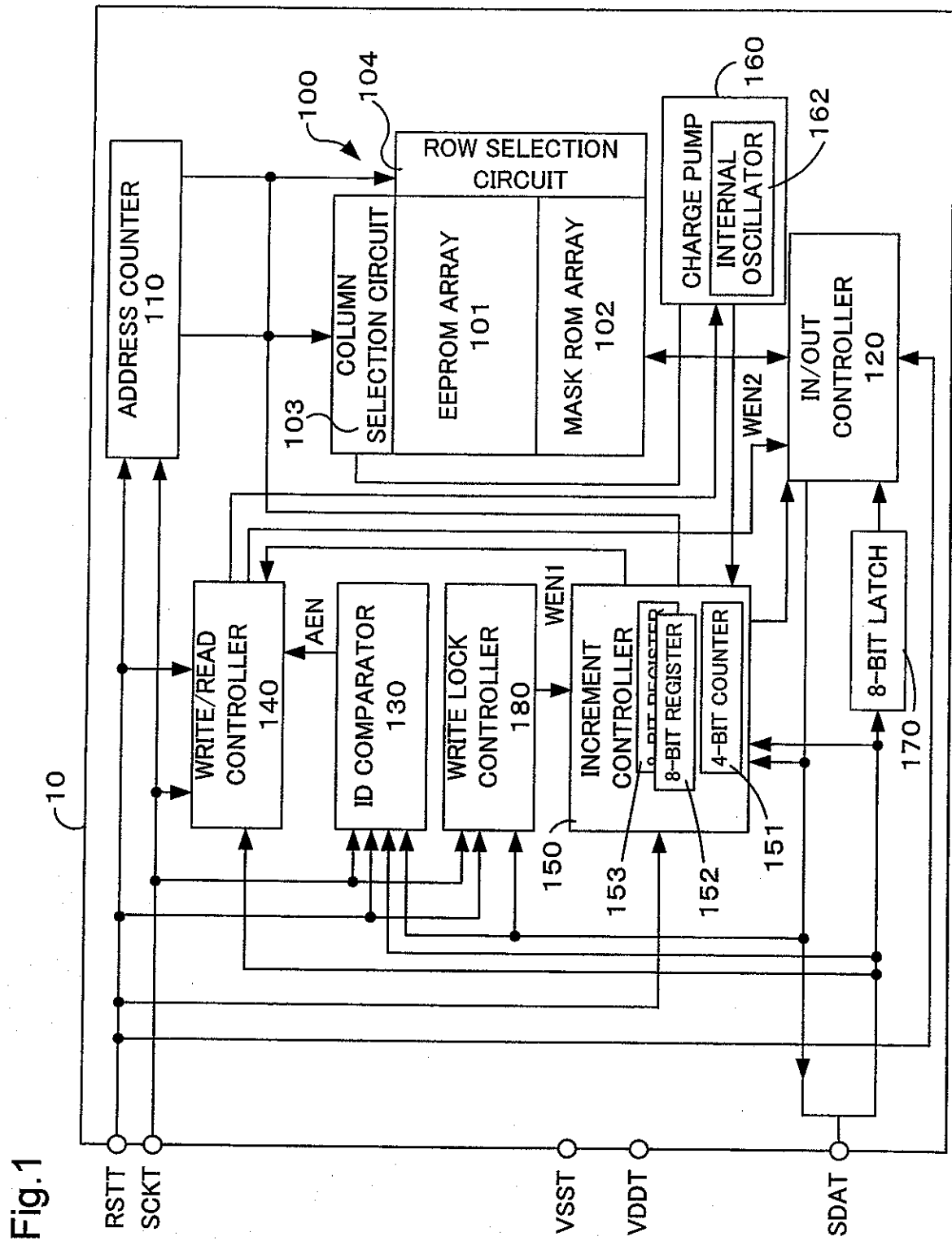


Fig.2

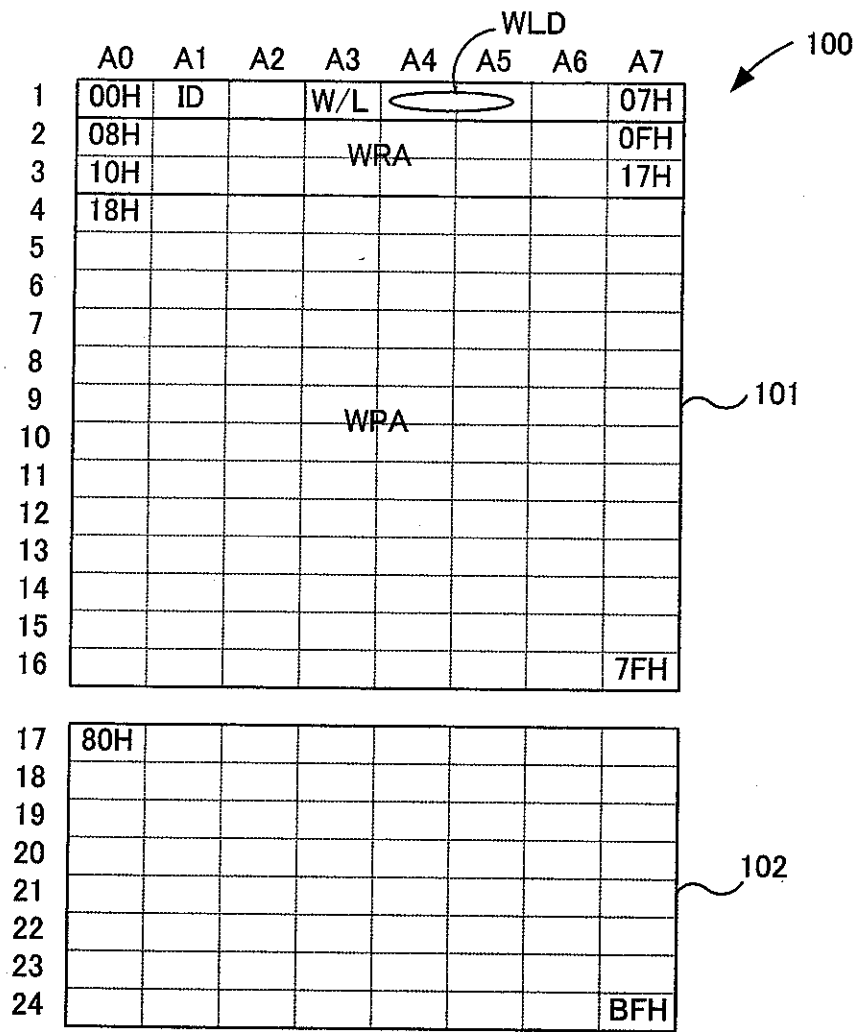


Fig.3

WRITE LOCK AREA	04H	05H
3rd BYTE ONWARD	X	1
7th BYTE ONWARD	1	0
8th BYTE ONWARD	1	1

Fig.4

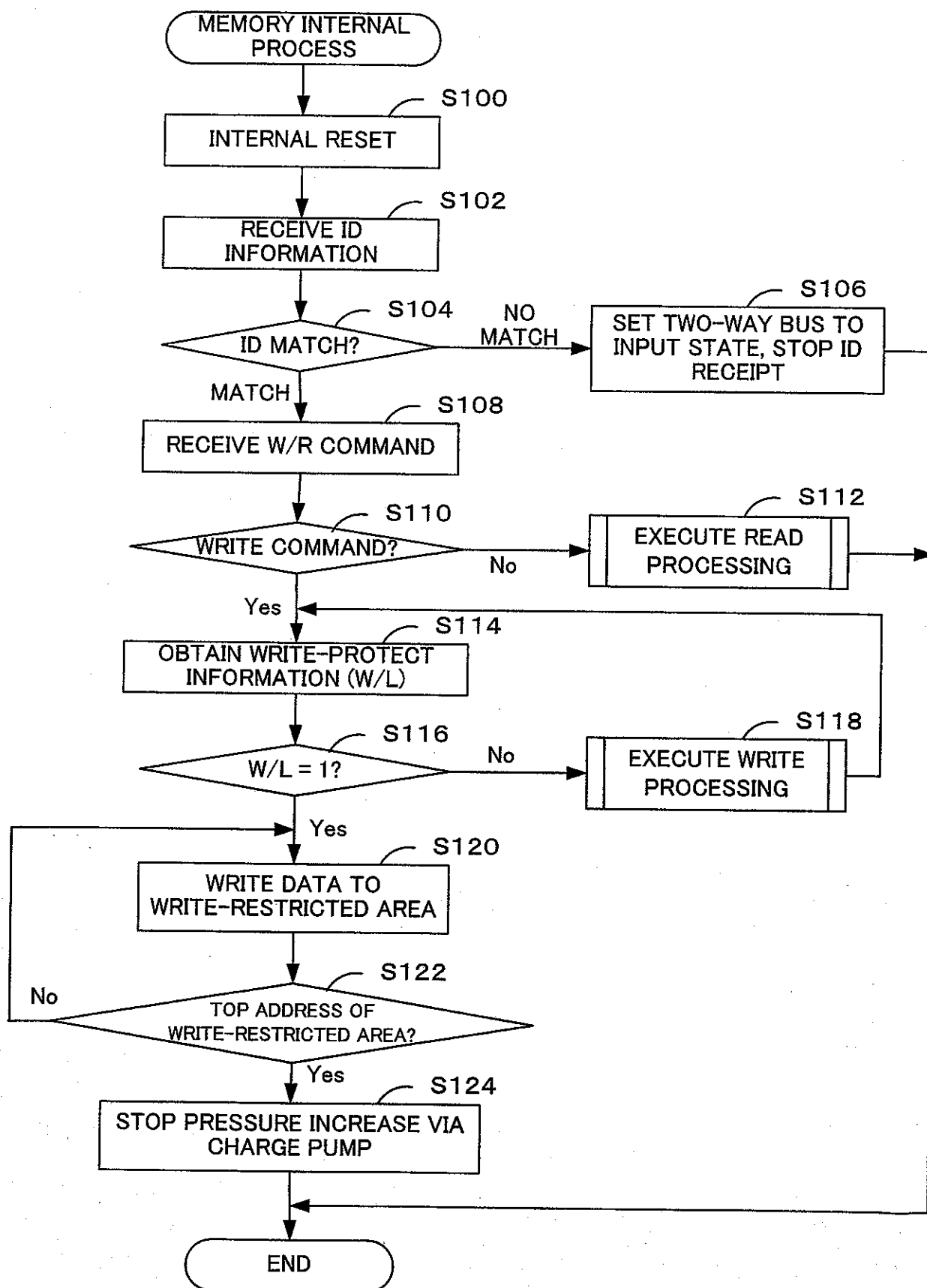


Fig.5

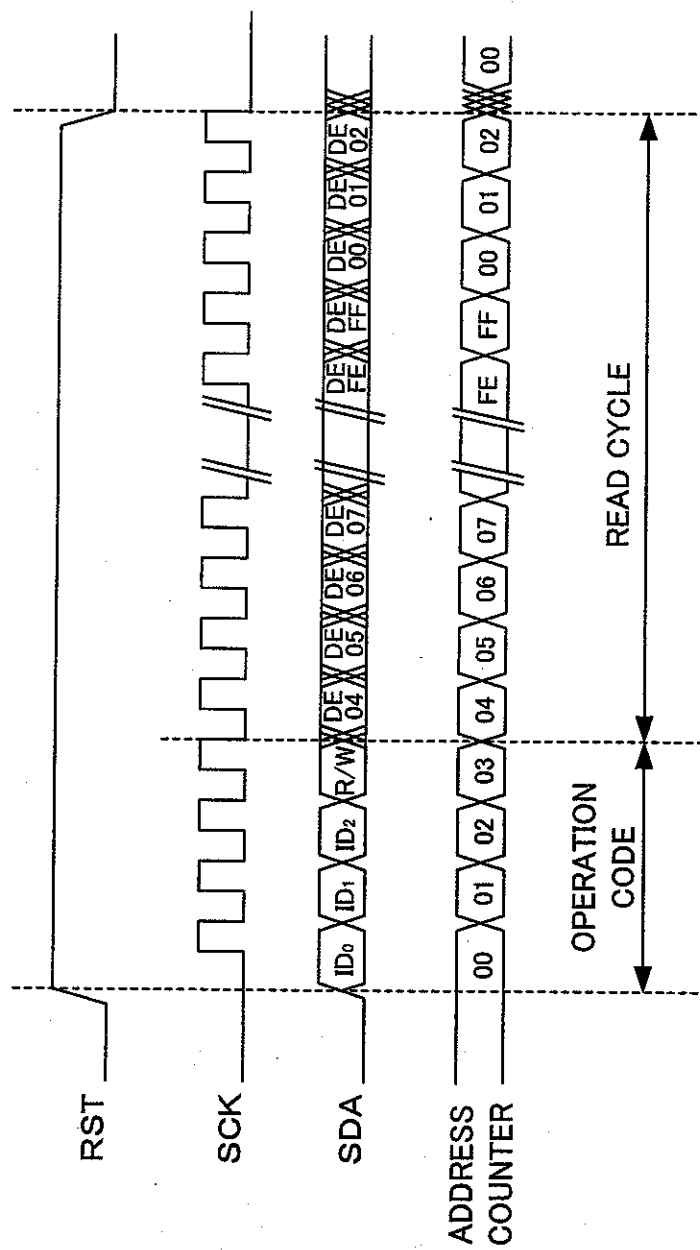


Fig.6

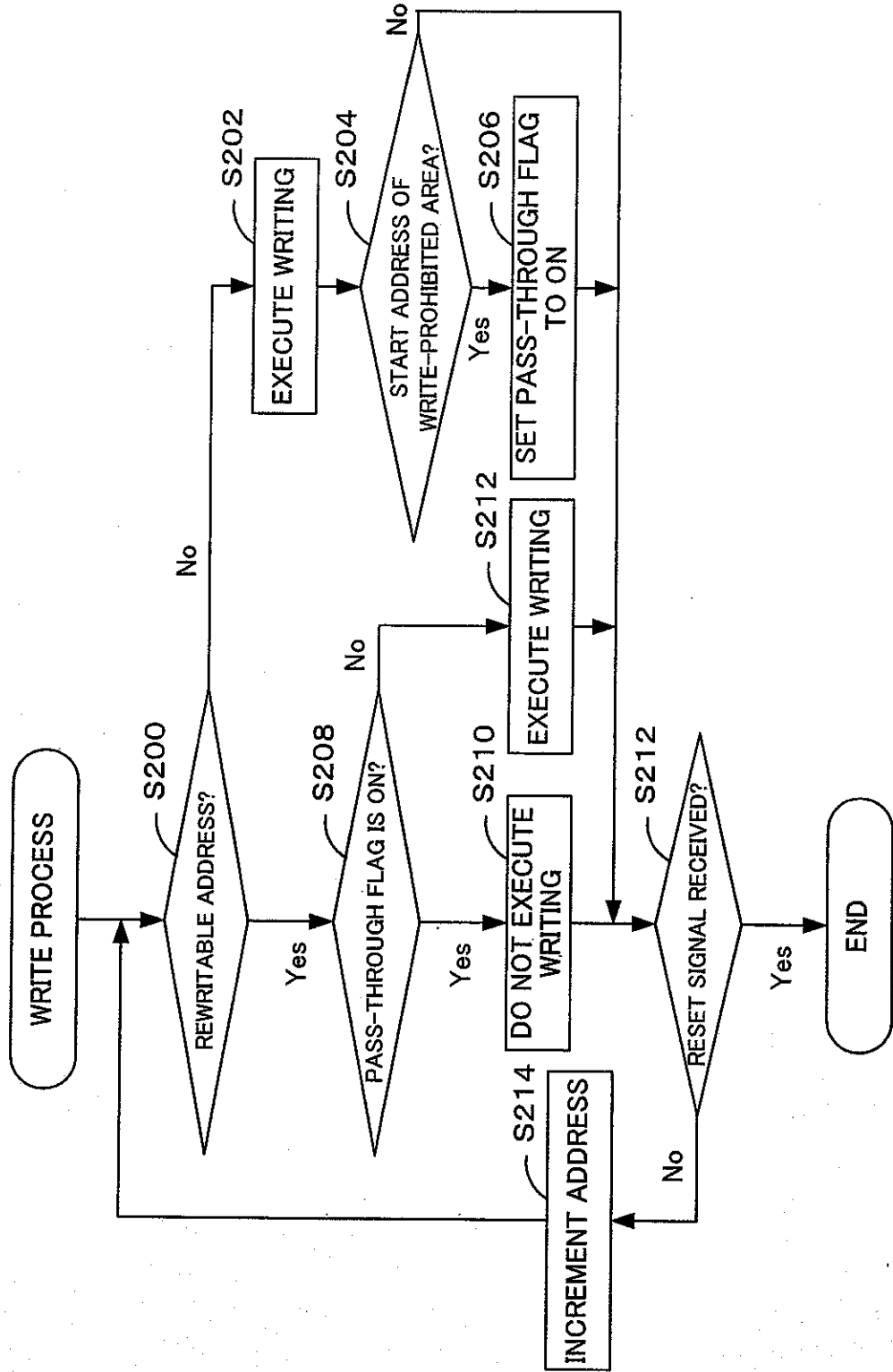


Fig.7

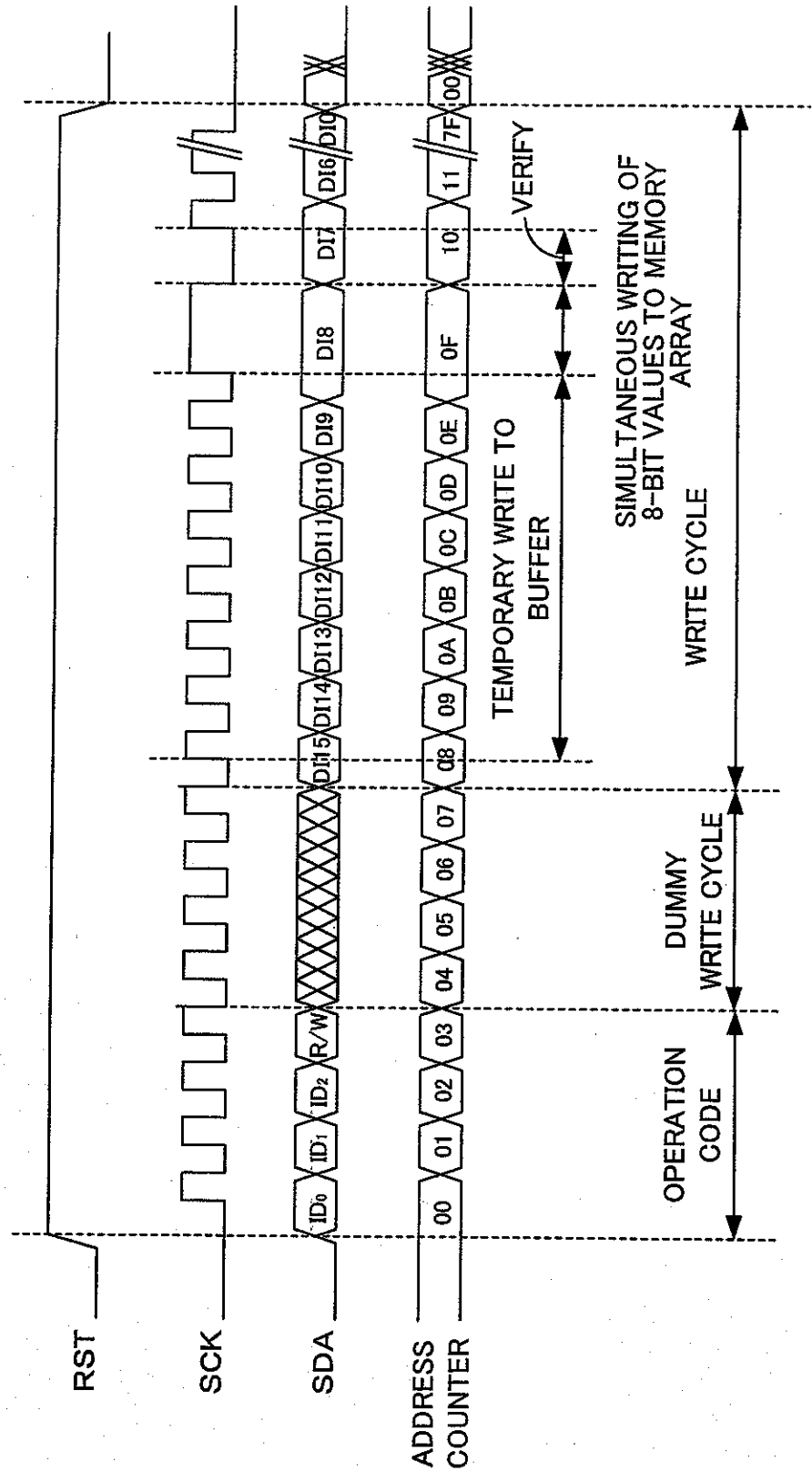


Fig.8

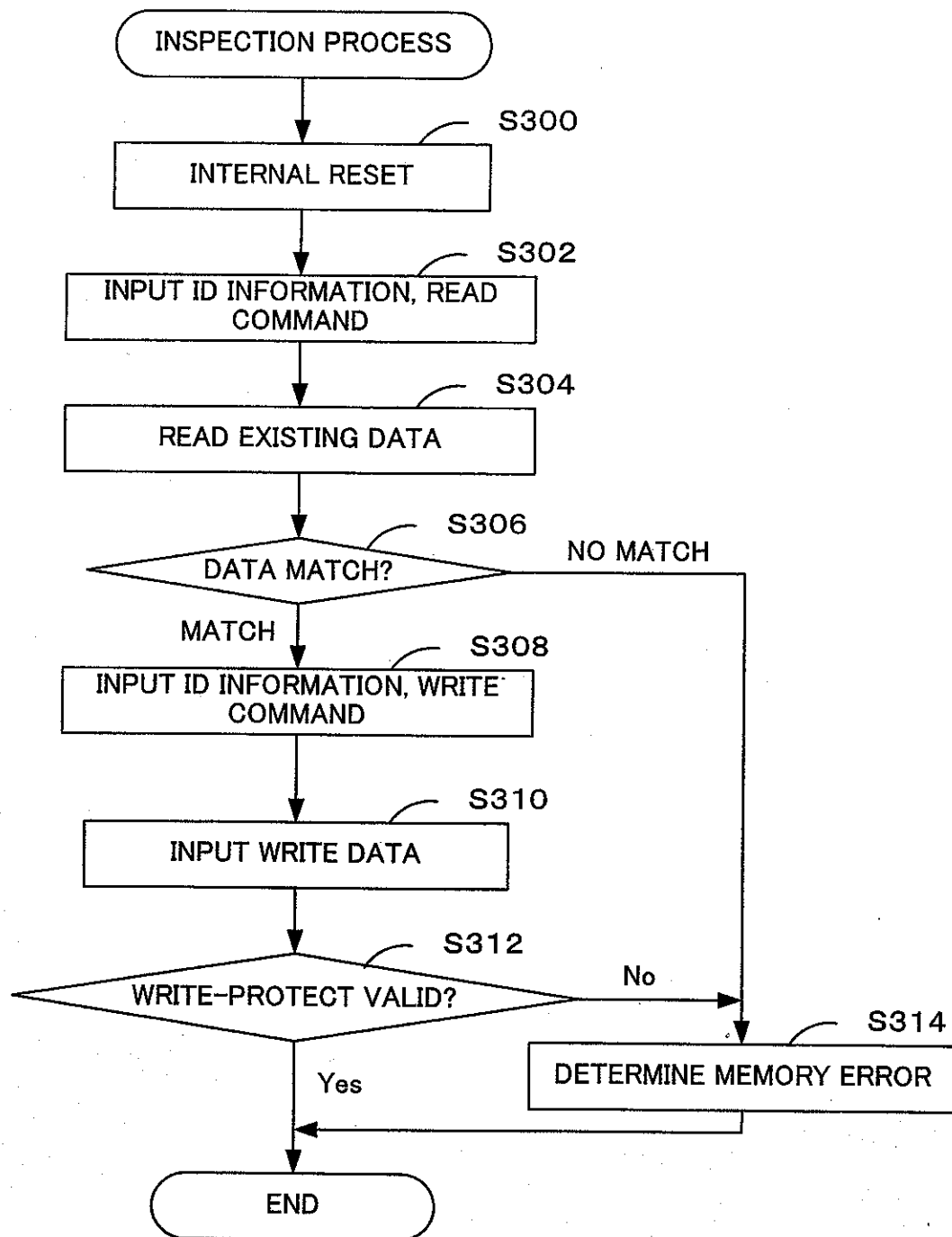


Fig.9

