

LOW VOLTAGE OPERATED OSCILLATORABSTRACT

This oscillator is realised in CMOS technology and comprises means (L1, L2) for biasing in the forward sense the source-tub junction of at least some of its transistors (M2, M4, M6), and means (R1, R3) for limiting the current flowing across this junction (S-B).

This assembly has as effect to lower the threshold of the transistors, in particular of those of the same type as the substrate.

The field of application concerns in particular electronic timepieces arranged to be powered by way of a single photovoltaic cell.

Figure 10