

ABSTRACT

A device outputs a first low voltage to a first terminal at a first timing in a period in which a voltage input to a second terminal is a high voltage. After outputting the first low voltage, the device outputs a second high voltage to the first terminal at a second timing in a period in which the voltage input to the second terminal is a low voltage. After outputting the second high voltage, the device outputs a second low voltage to the first terminal at a third timing in a period in which the voltage input to the second terminal is a high voltage.