

RECEIVING CIRCUIT AND MEMORY

ABSTRACT

The present disclosure provides a receiving circuit and a memory. The receiving circuit includes: an input buffer configured to receive a first input signal and a second input signal, compare the first input signal with the second input signal, and output a first output signal and a second output signal, where the first input signal and the second input signal are respectively a first signal and a second signal in a differential mode, the first input signal is one of the first signal and the second signal in a single-ended mode, the second input signal is a reference voltage signal, and the first signal and the second signal are complementary; and a conversion module configured to receive the first output signal and the second output signal and amplify a voltage difference between the first output signal and the second output signal to output a first internal signal and a second internal signal.

[Fig. 8]