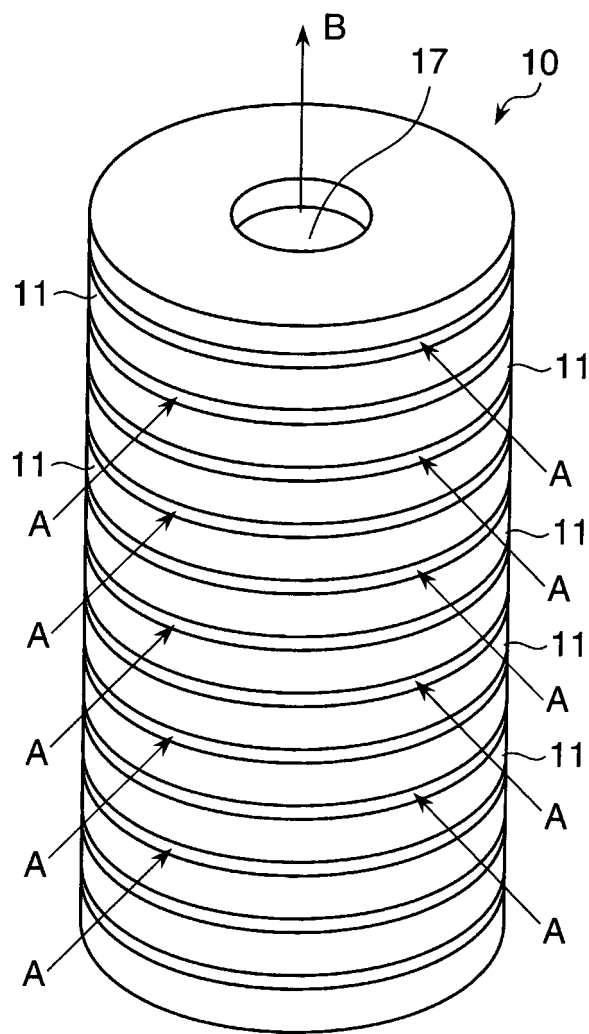


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図 1



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図 2 A

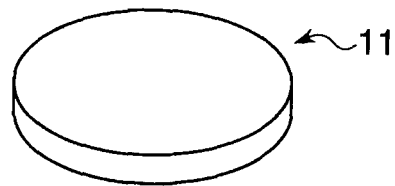


図 2 B

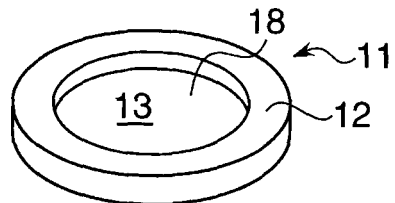


図 2 C

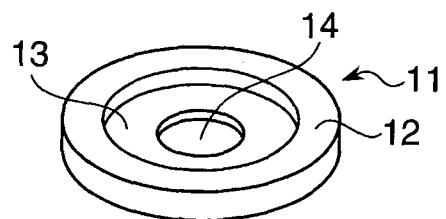


図 2 D

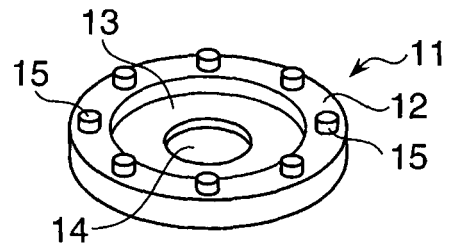


図 2 E

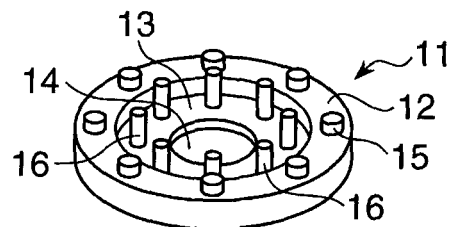
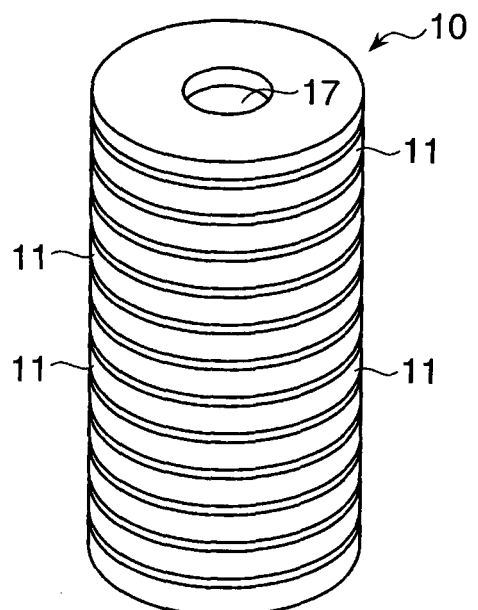


図 2 F



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图 3

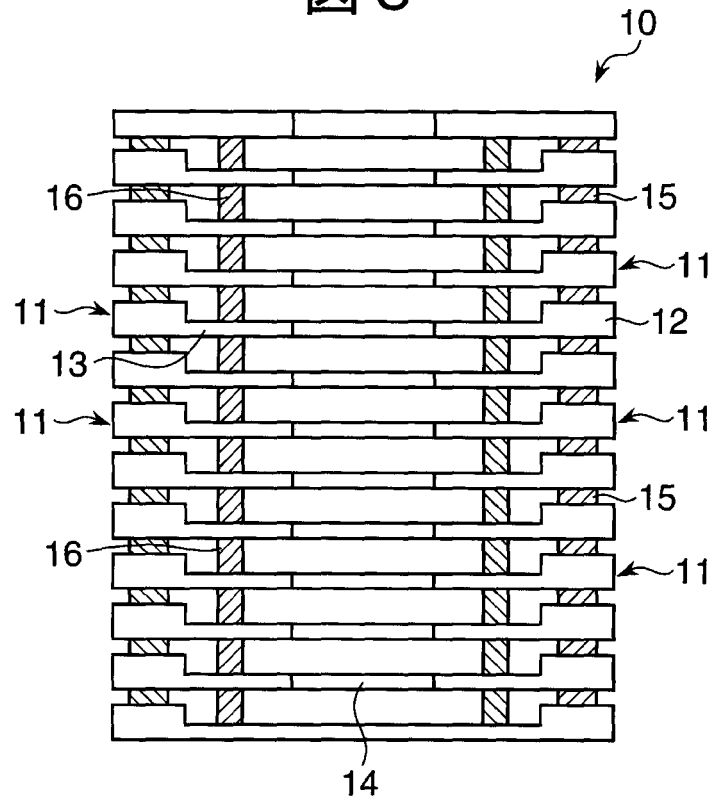
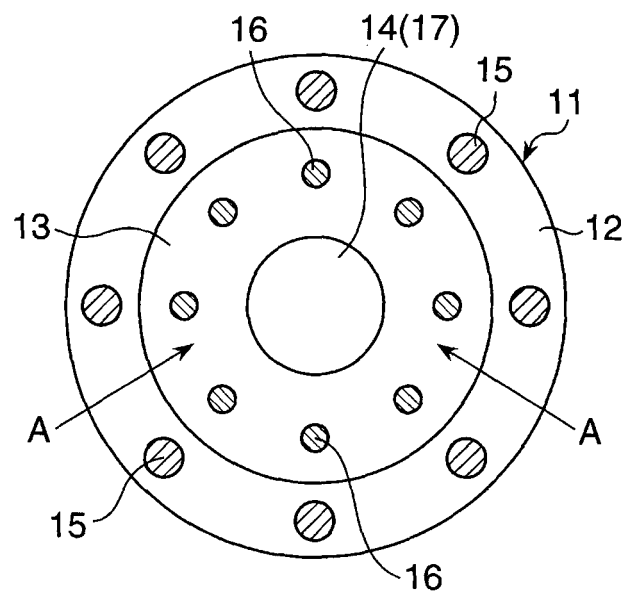


图 4



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図 5

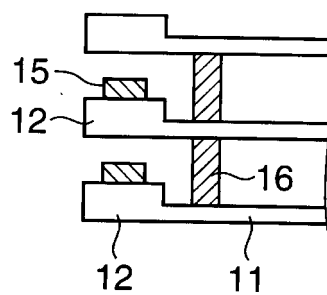
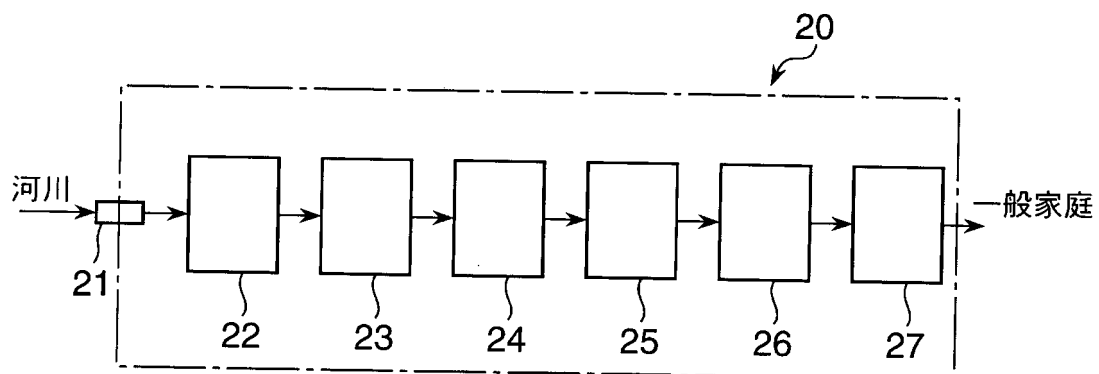
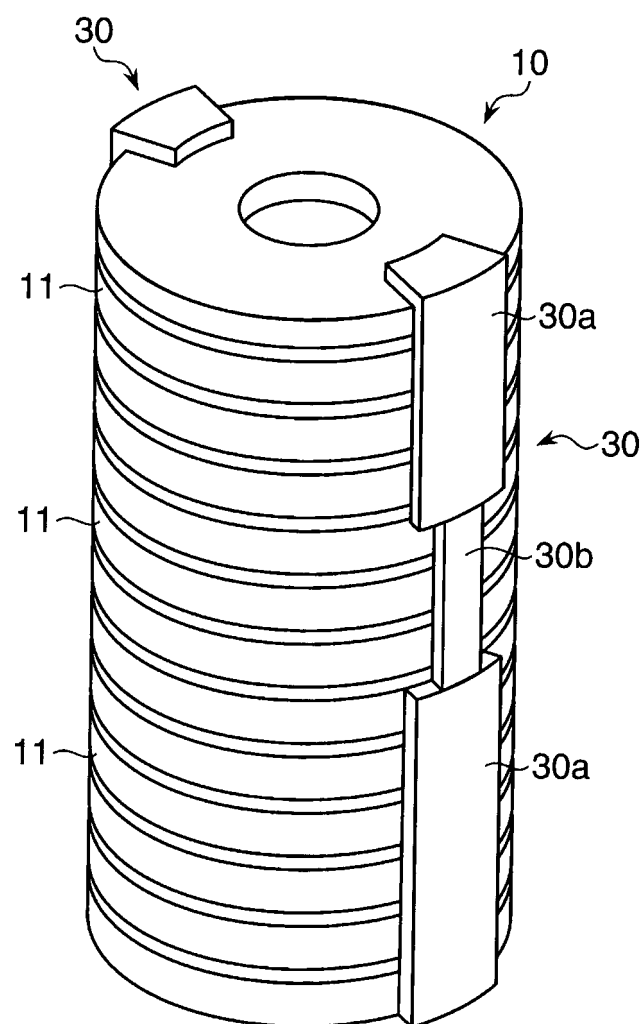


図 6



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図 7



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図 8

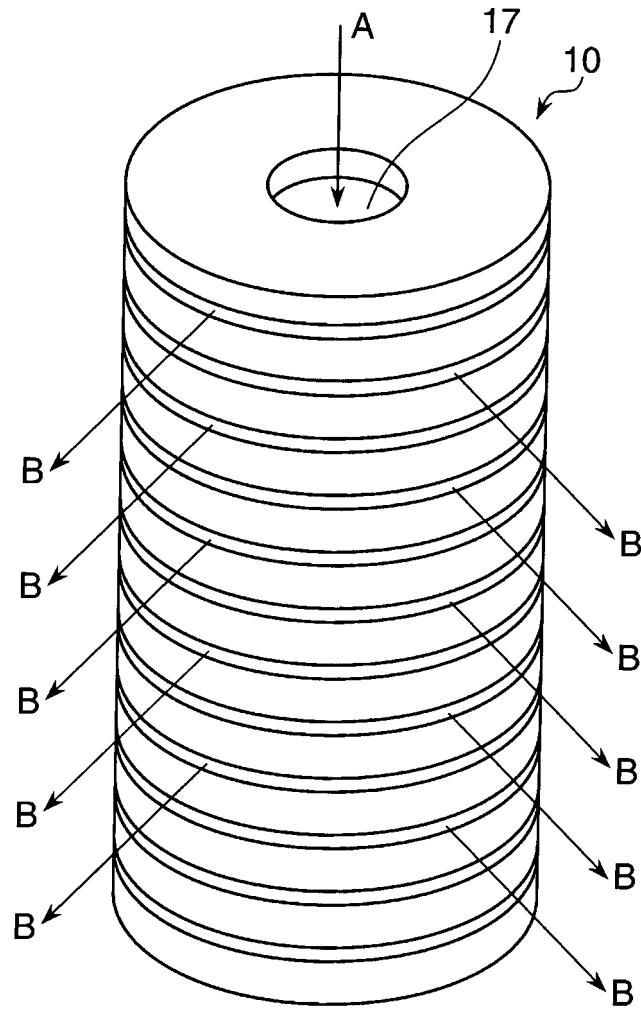
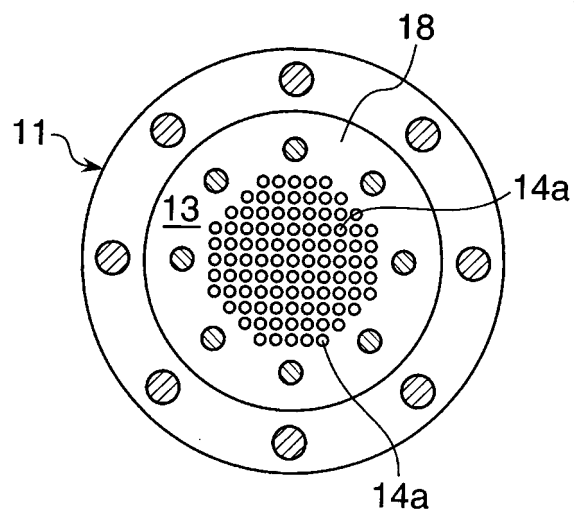


図 9



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図 10

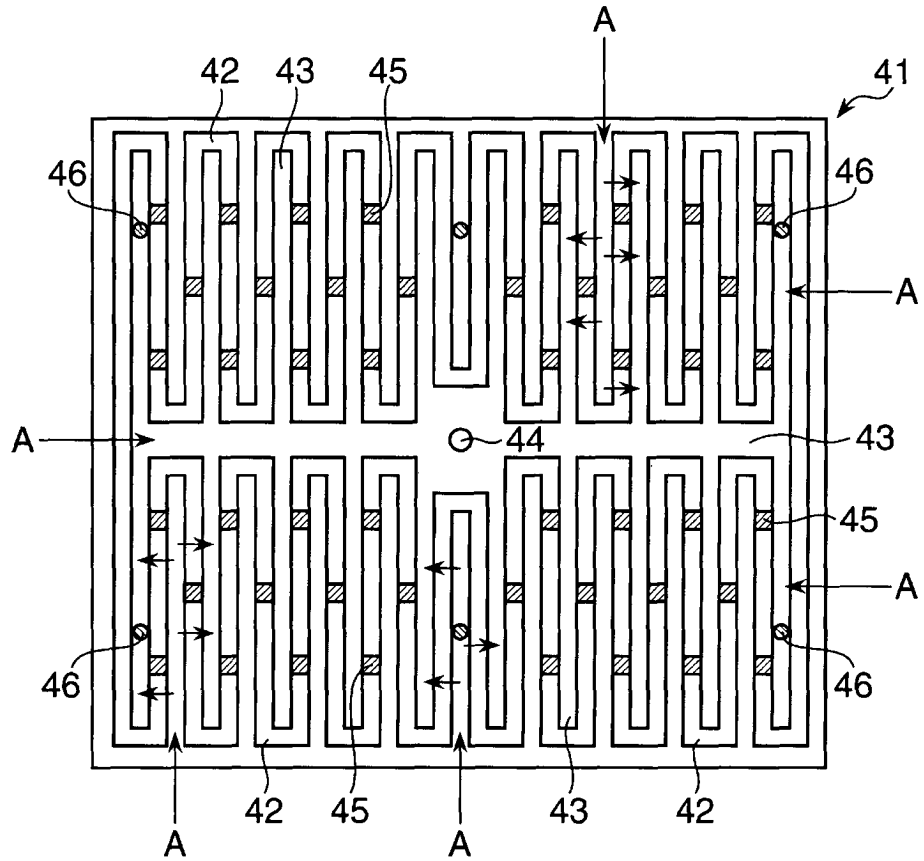


図 11

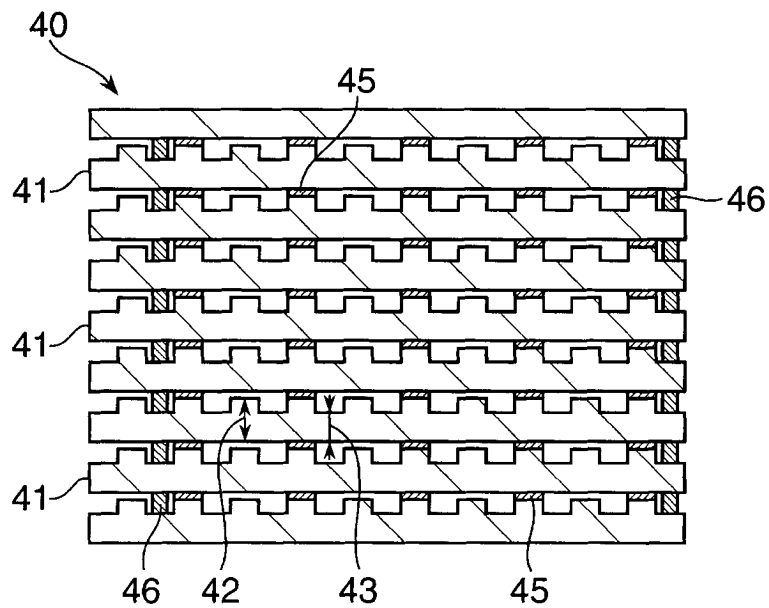


図 12

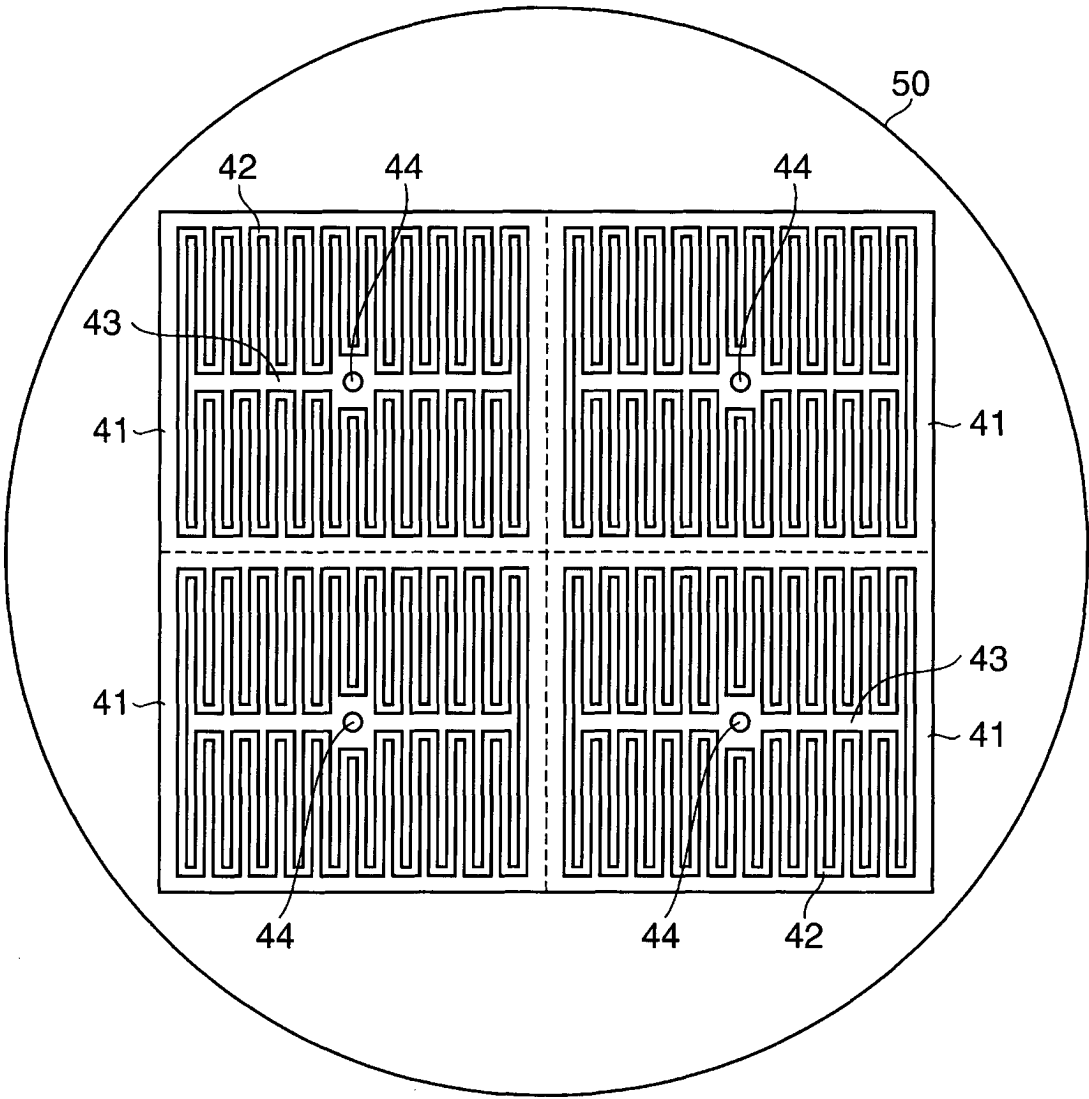


図 13

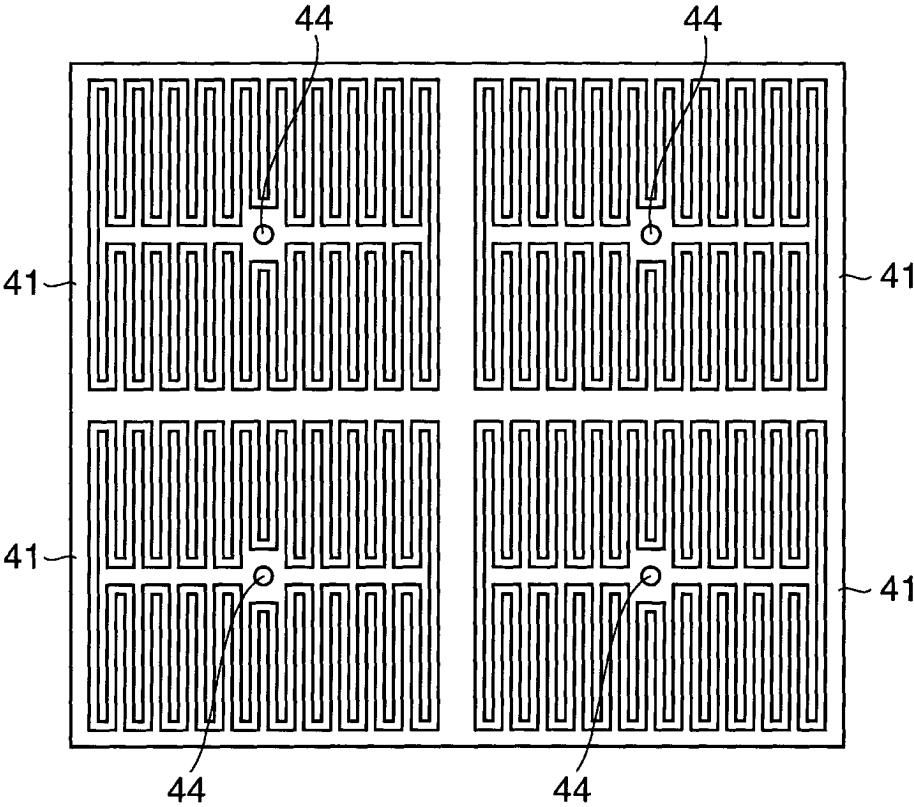
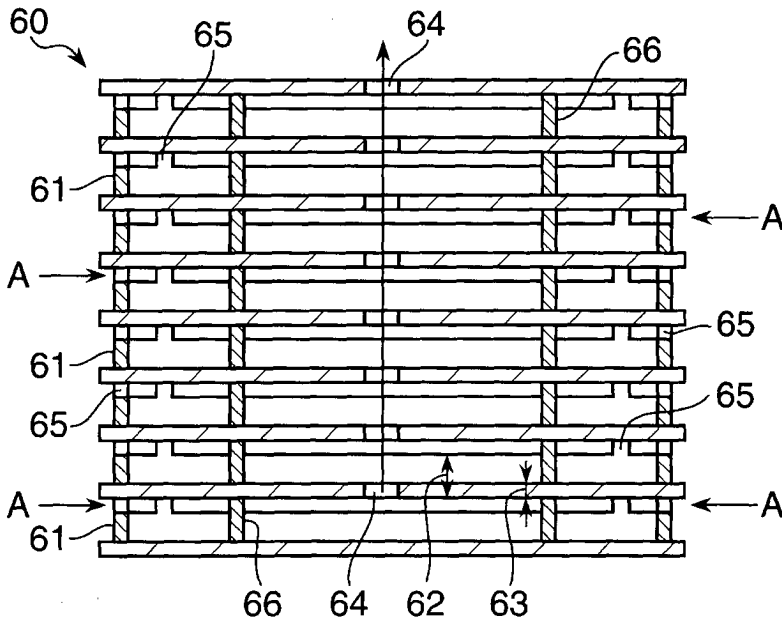


図 14



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図 15A

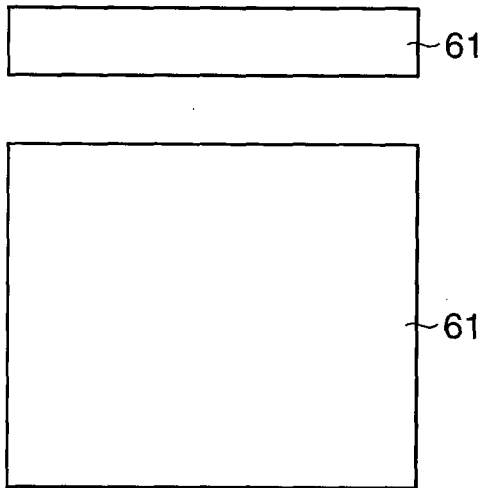


図 15B

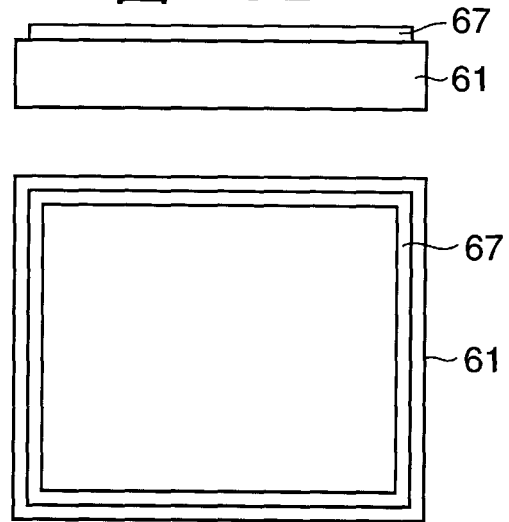


図 15C

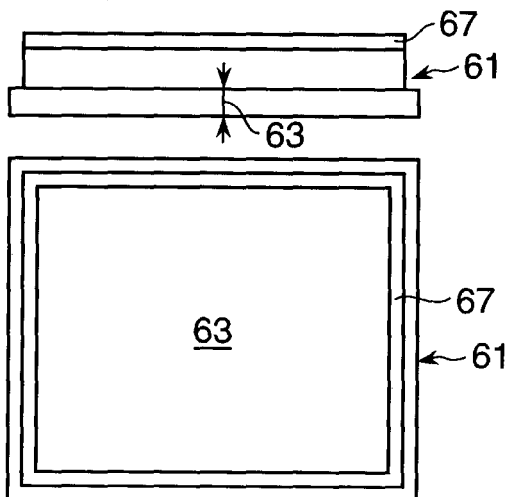


図 15D

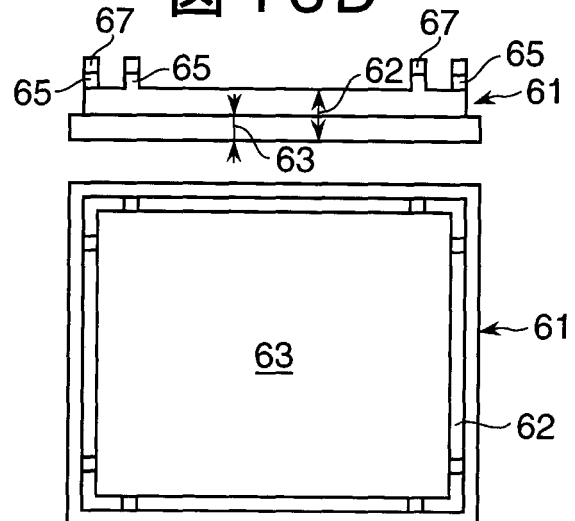


図 15E

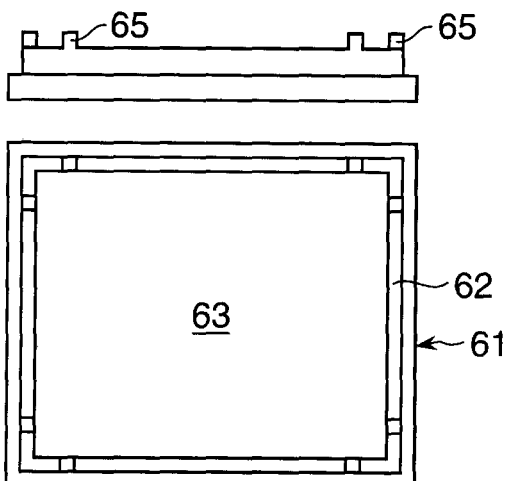


図 15F

