(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau

(43) International Publication Date
21 December 2007 (21.12.2007)

PCT

(10) International Publication Number
WO 2007/146853 A2

(51) International Patent Classification:
G09G 3/36 (2006.01)

(21) International Application Number:
PCT/US2007/070827

(22) International Filing Date: 11 June 2007 (11.06.2007)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/804,494 12 June 2006 (12.06.2006) US

(71) Applicant *(for all designated States except US)*: **JOHNSON & JOHNSON VISION CARE, INC.** [US/US]; 7500 Centurion Parkway, Suite 100, Jacksonville, Florida 32256 (US).

(72) Inventor; and
(75) Inventor/Applicant *(for US only)*: **MEREDITH, Gerald** [US/US]; 2995 W. Montage Vista Drive, Tucson, Arizona 85745 (US).

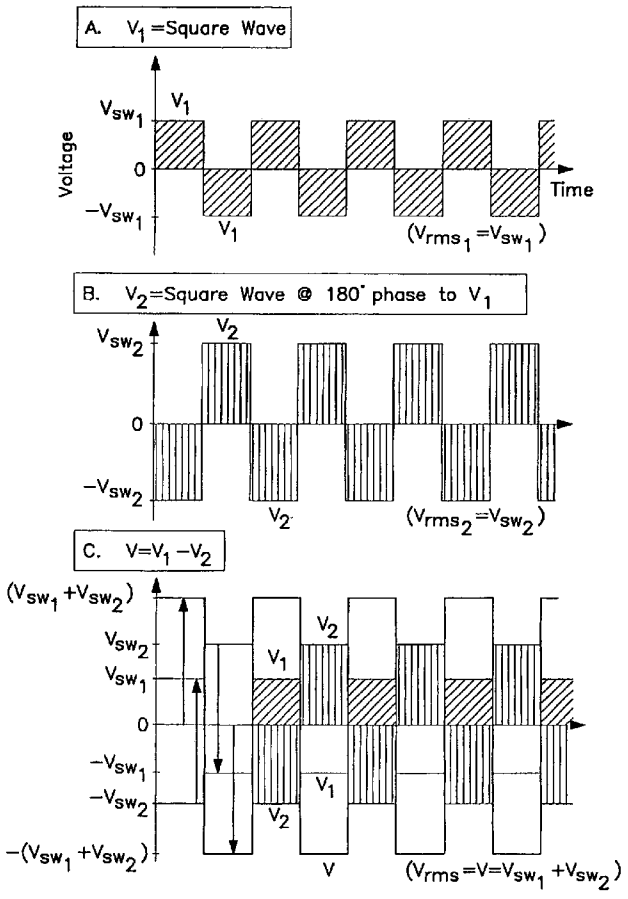
(74) Agents: **PEARL, Zeev** et al.; Pearl Cohen Zedek Latzer, LLP, 1500 Broadway, 12th Floor, New York, NY 10036 (US).

(81) Designated States *(unless otherwise indicated, for every kind of national protection available)*: AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States *(unless otherwise indicated, for every kind of regional protection available)*: ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,

[Continued on next page]

(54) Title: METHOD TO REDUCE POWER CONSUMPTION WITH ELECTRO-OPTIC LENSES



(57) Abstract: Provided is an electro-optic device having reduced power consumption. More specifically, an electro-optic device is provided comprising: a liquid crystal layer between a pair of opposing transparent substrates; a patterned electrode set positioned between the liquid crystal layer and the inward-facing surface of the first transparent substrate; a conductive layer between the liquid crystal layer and the inward-facing surface of the second transparent substrate; and means for applying voltage to the patterned electrode set and the conductive layer, wherein the voltage applied to the conductive layer is below the threshold voltage (the RMS voltage difference above which the optical transmission of the liquid crystal layer changes).

WO 2007/146853 A2

ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— *without international search report and to be republished upon receipt of that report*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.