

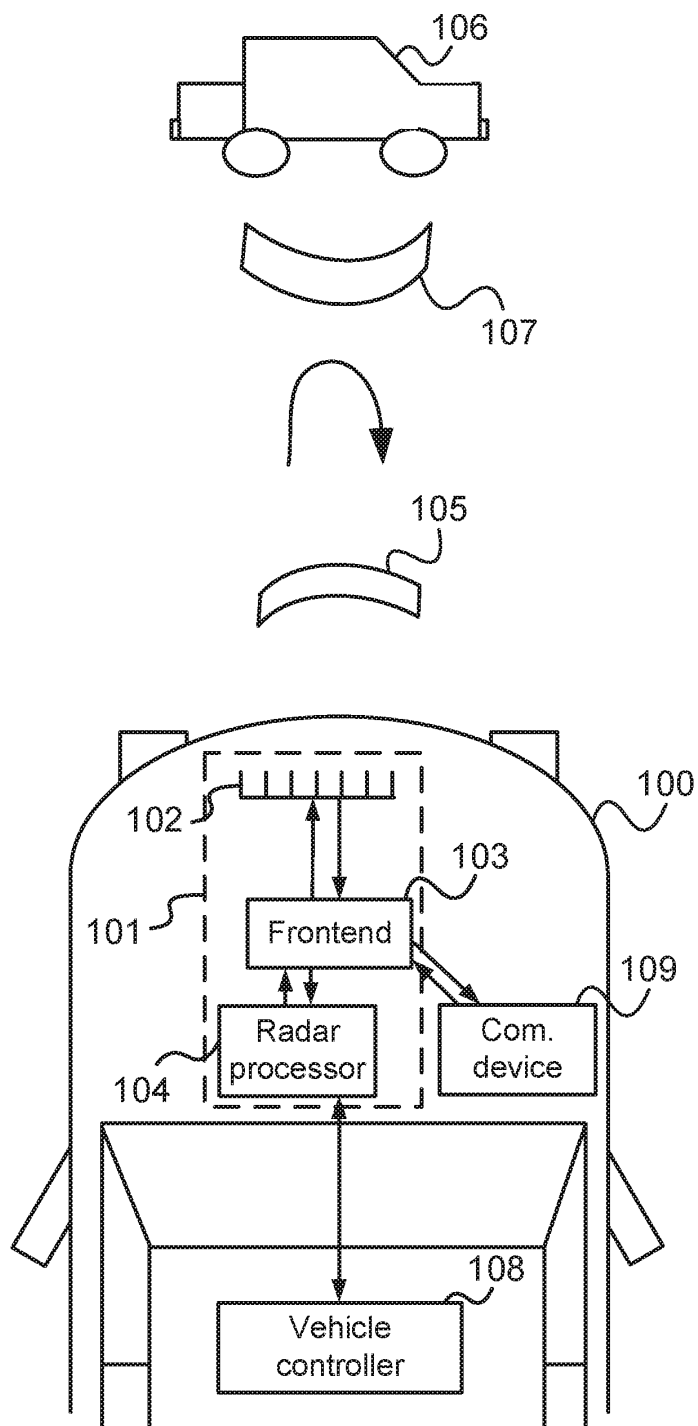
FIG 1

FIG 2

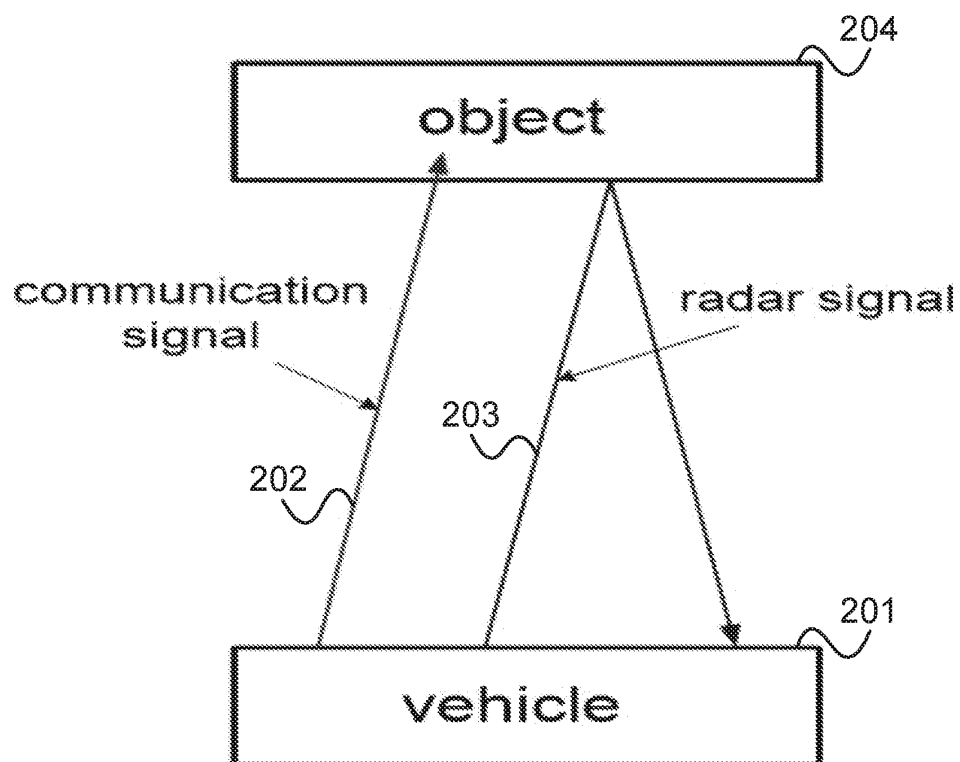


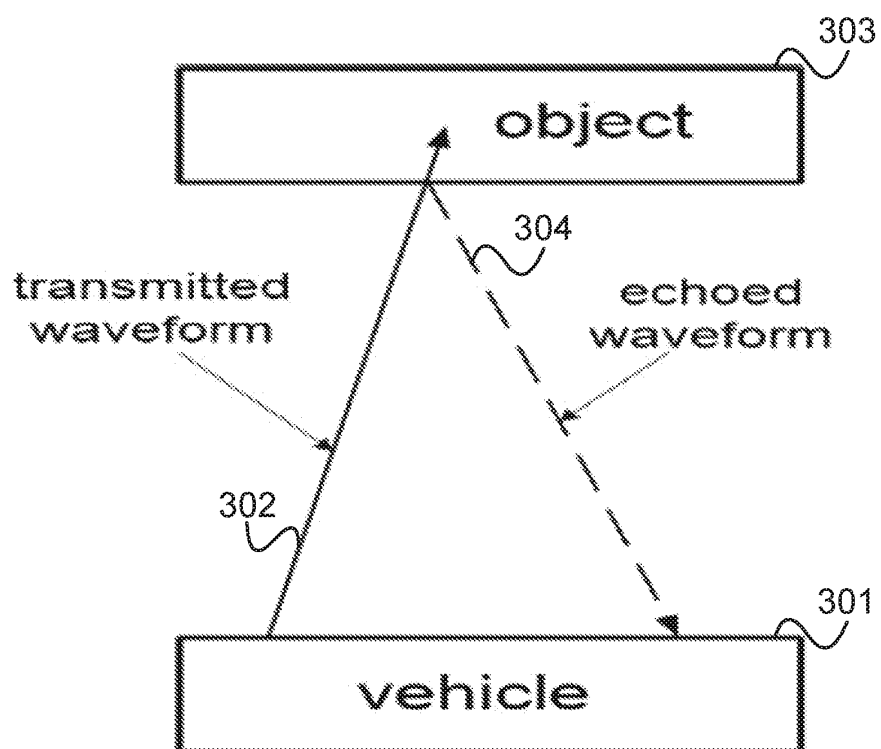
FIG 3

FIG 4

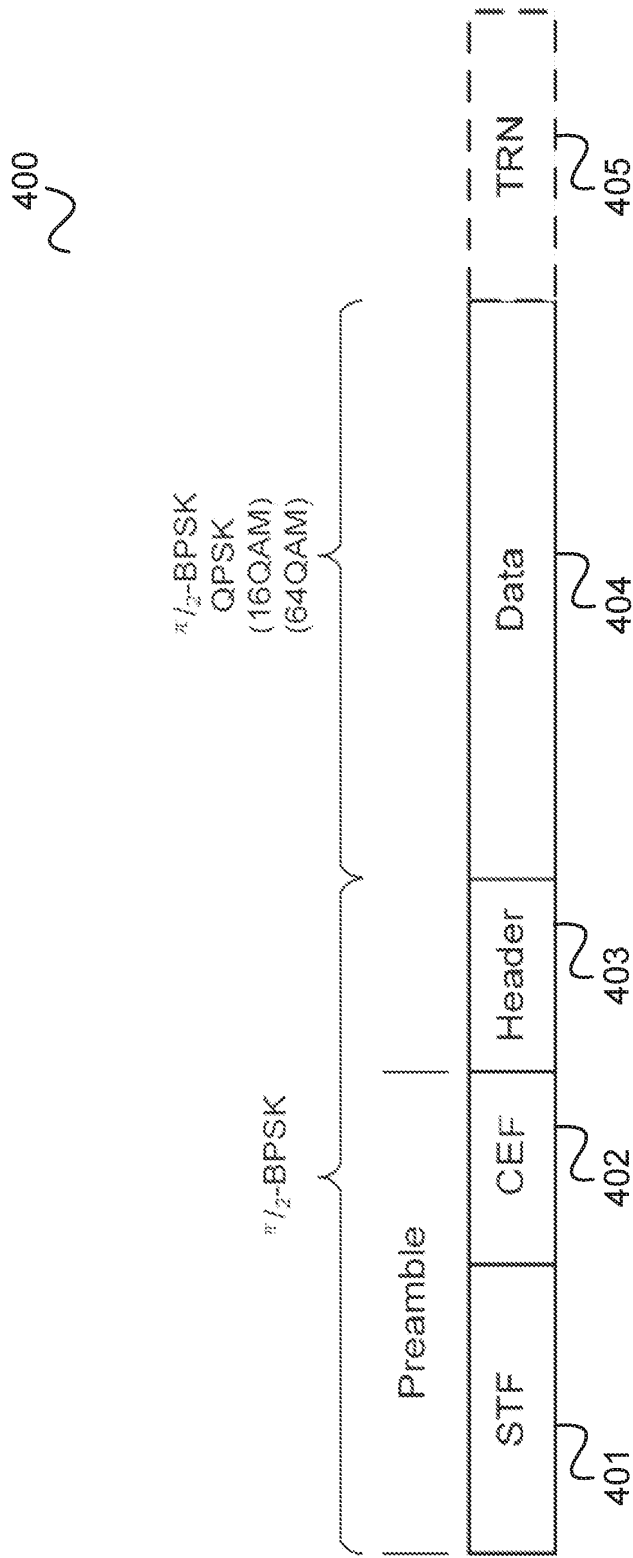
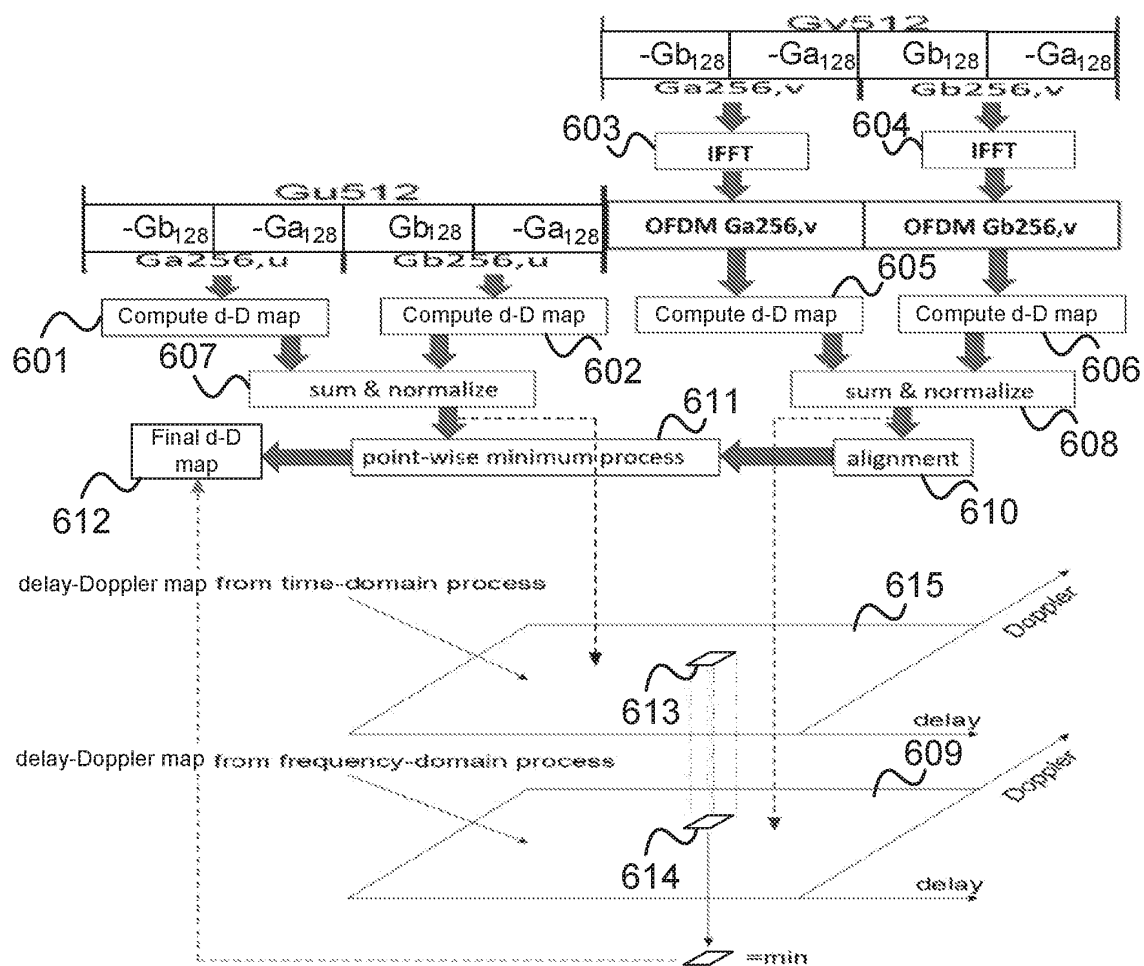


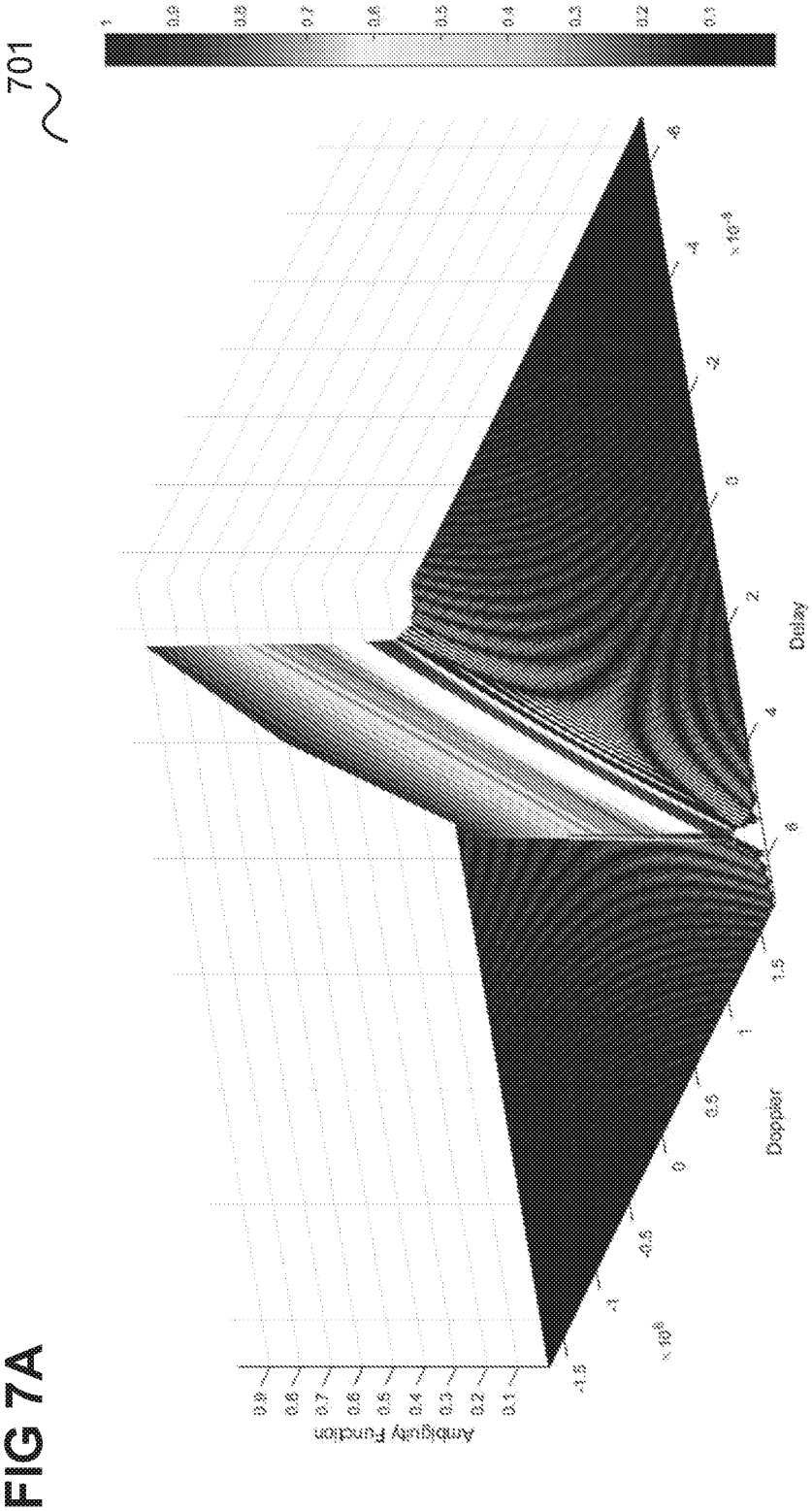
FIG 5

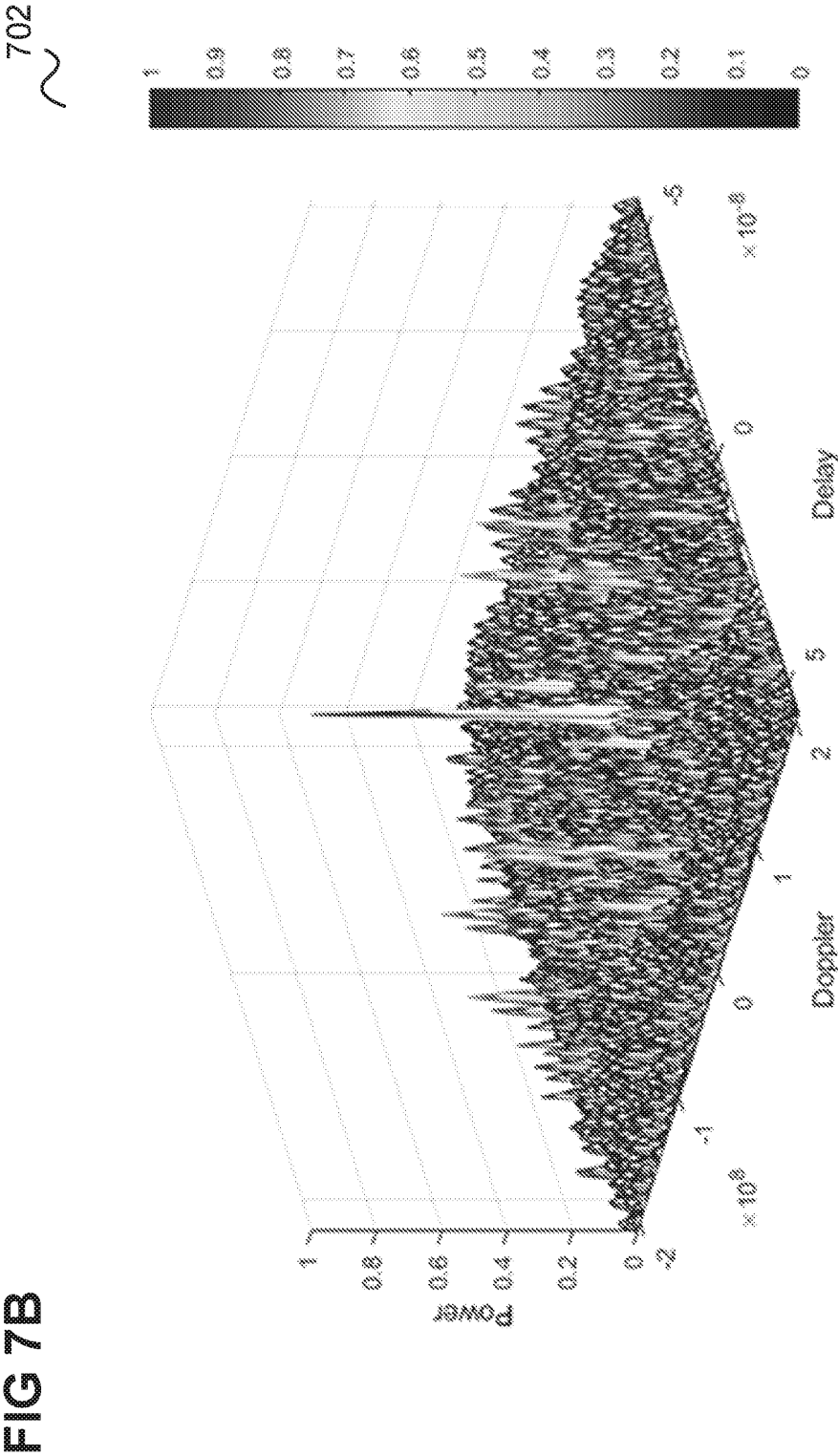
500

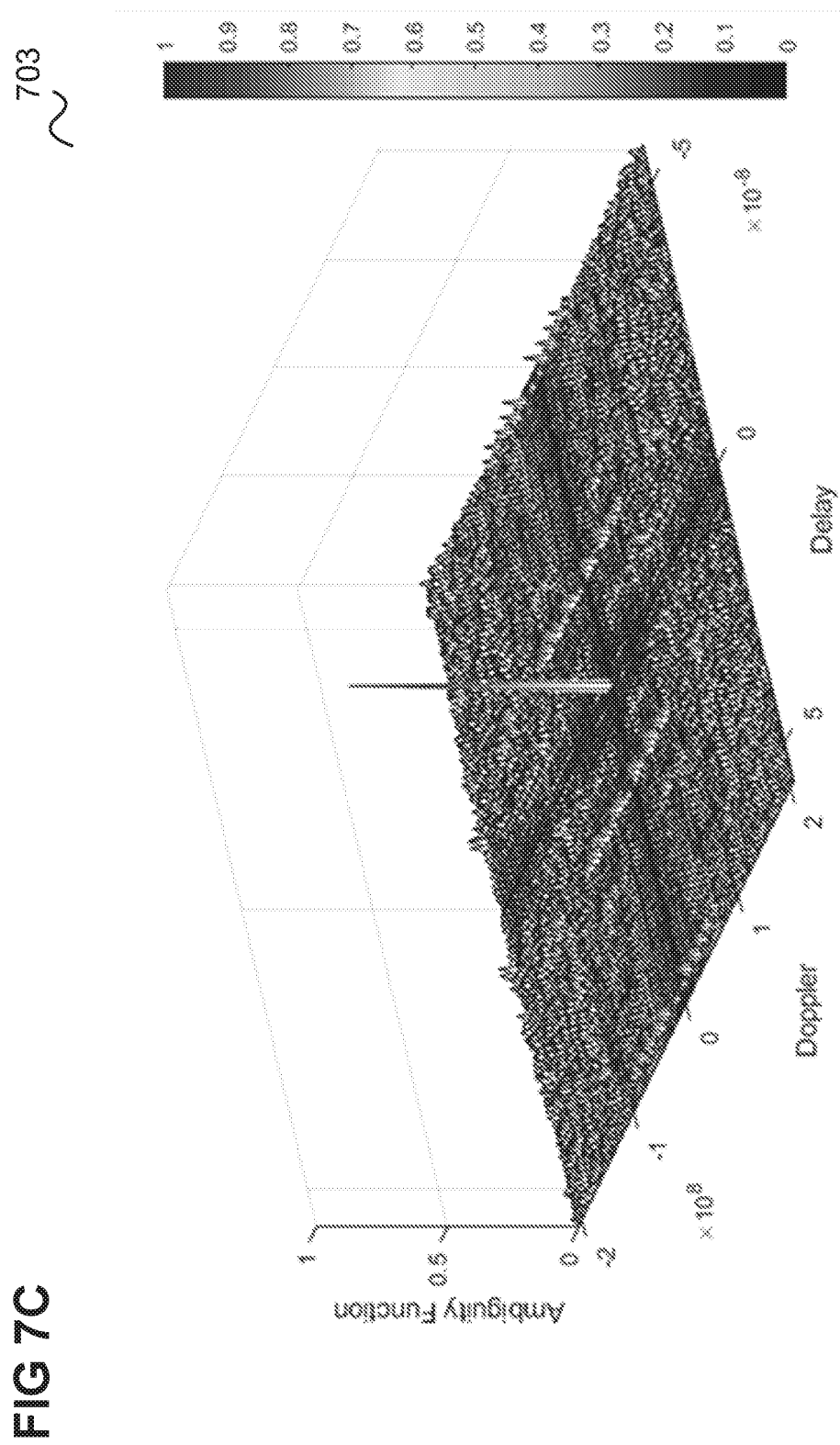
Gu512				Gv512				Gv128	
-Gb ₁₂₈	-Ga ₁₂₈	Gb ₁₂₈	-Ga ₁₂₈	-Gb ₁₂₈	Ga ₁₂₈	-Gb ₁₂₈	-Ga ₁₂₈	-Gb ₁₂₈	
Ga256,u				Ga256,v				Gb256,v	

FIG 6









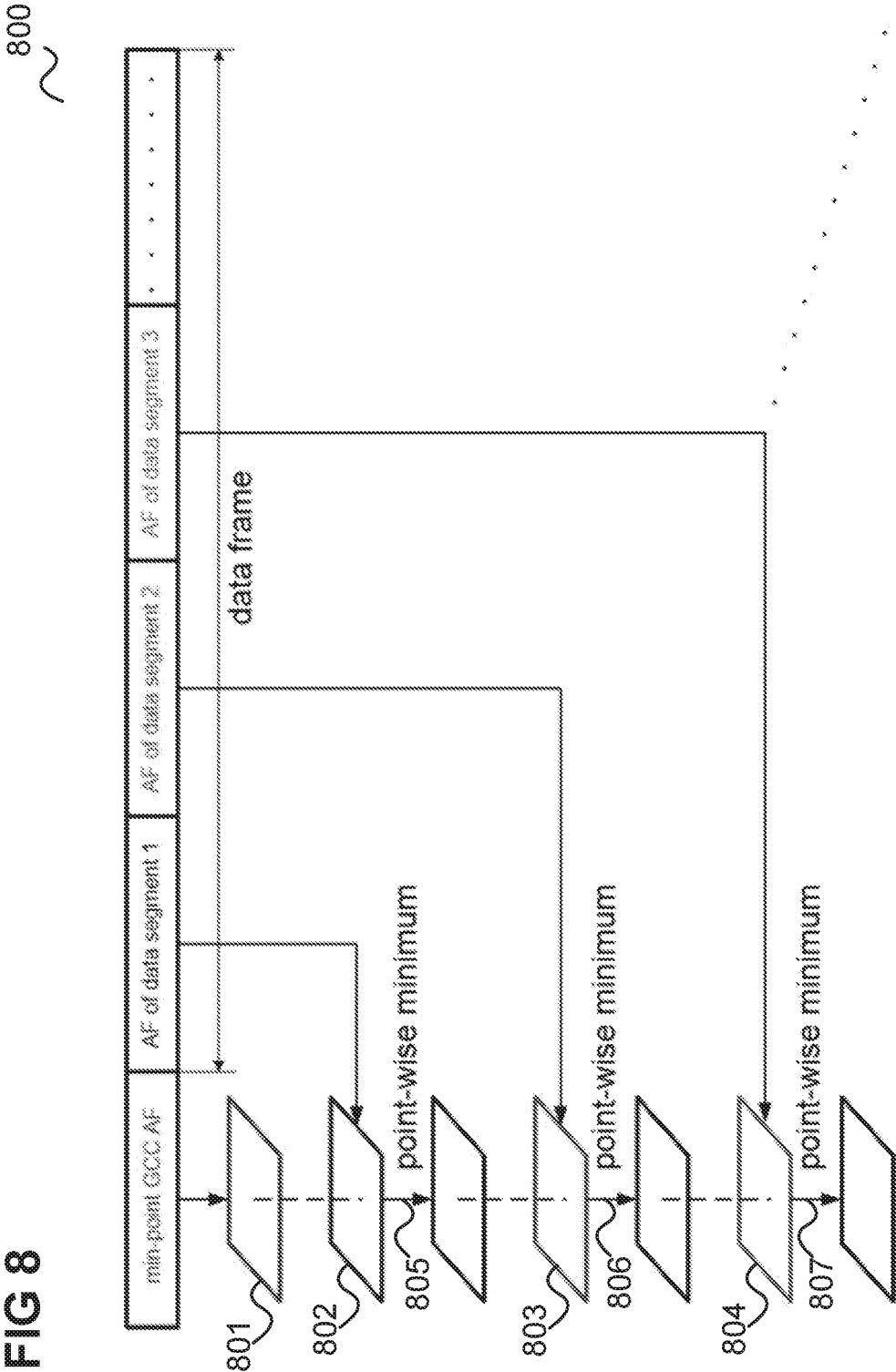


FIG 9

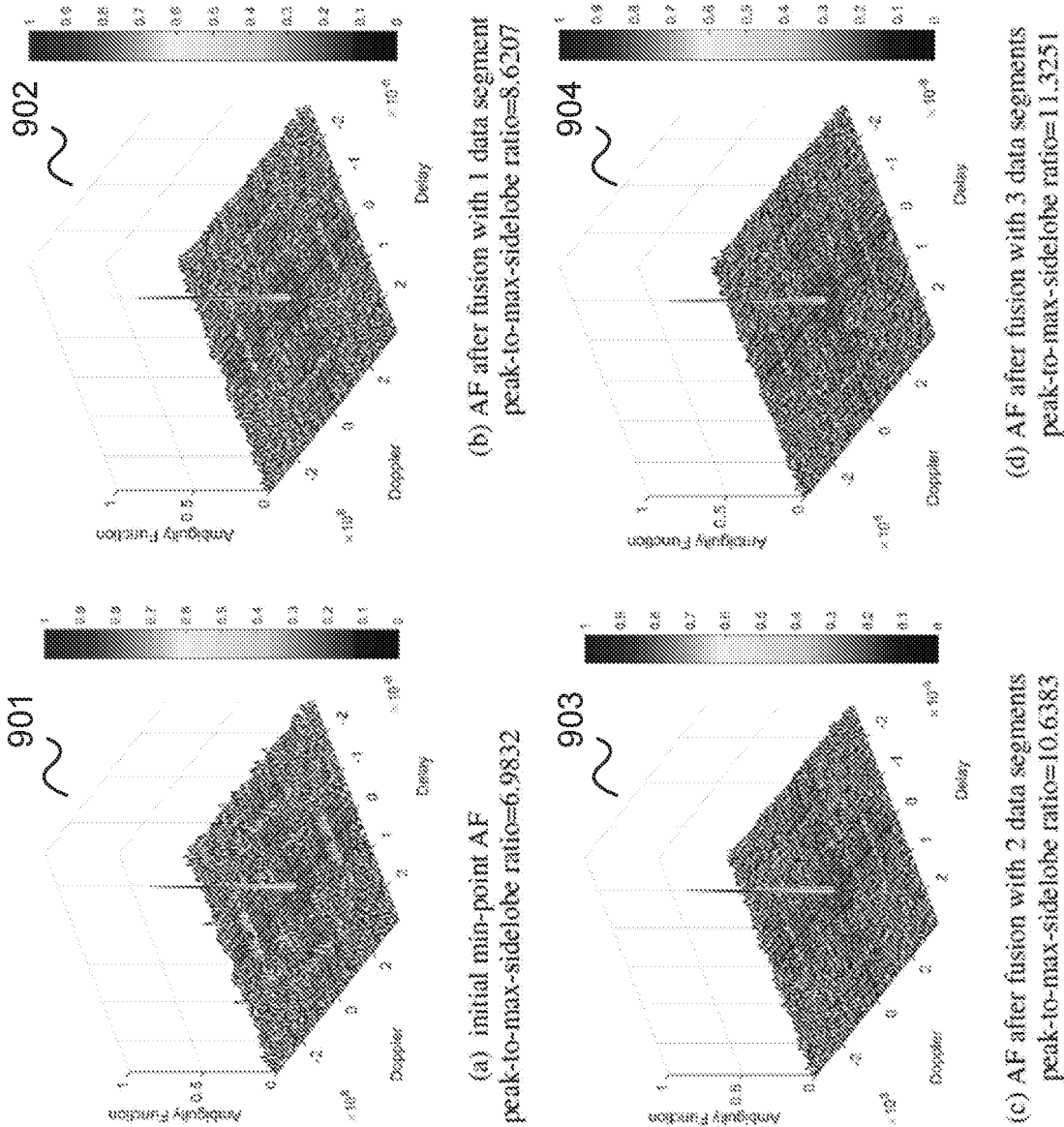


FIG 10

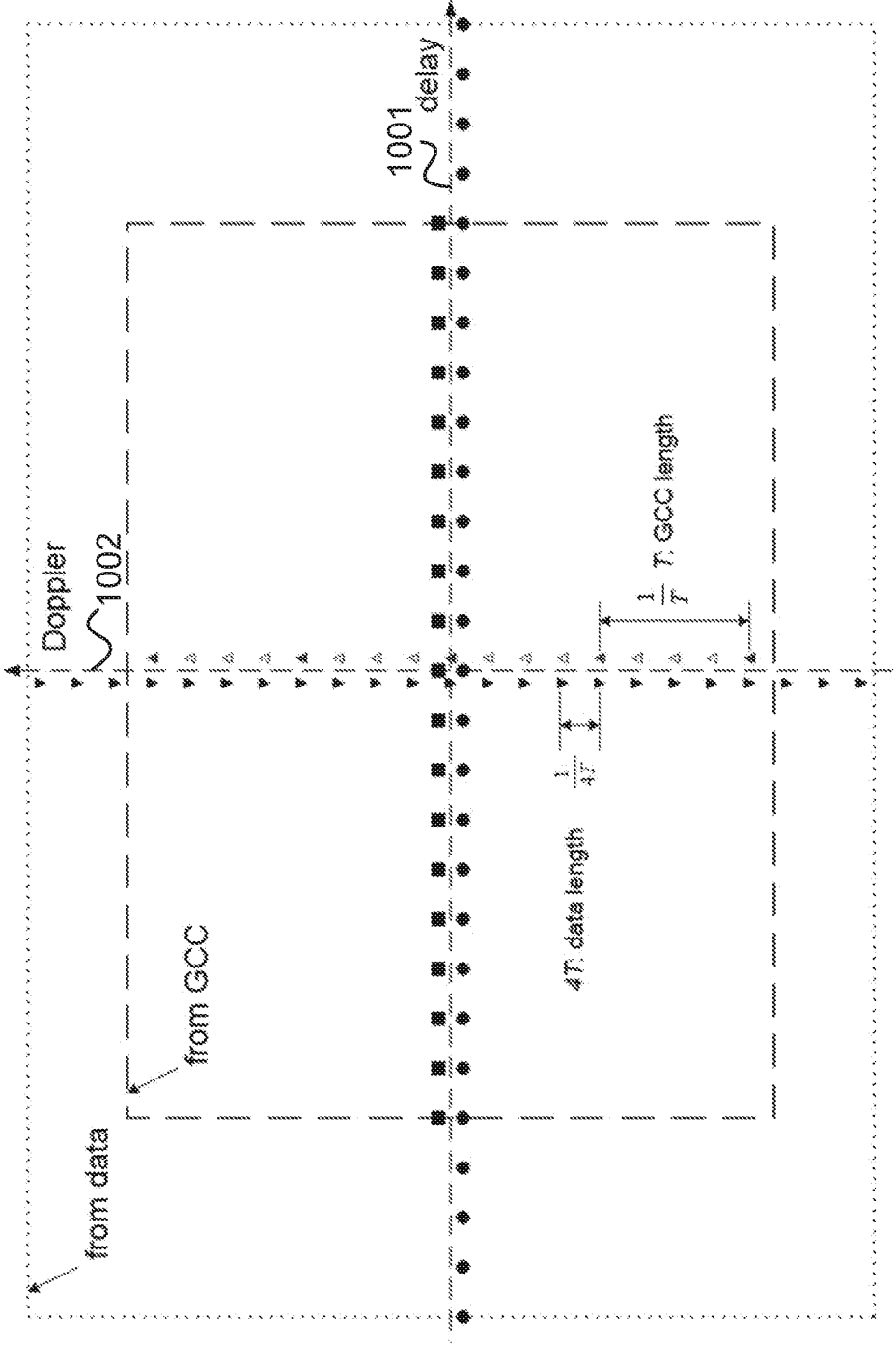


FIG 11

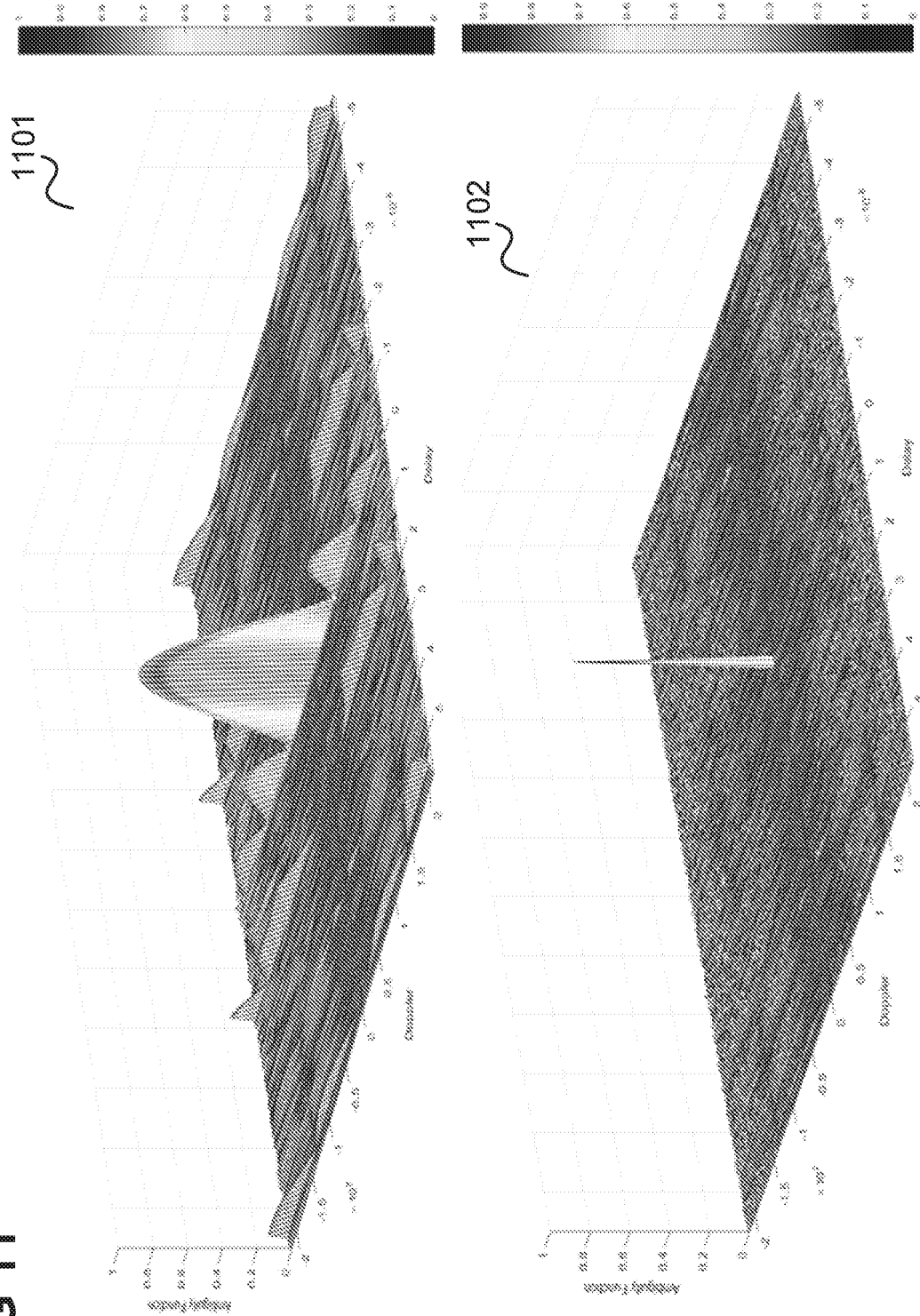


FIG 12

1200

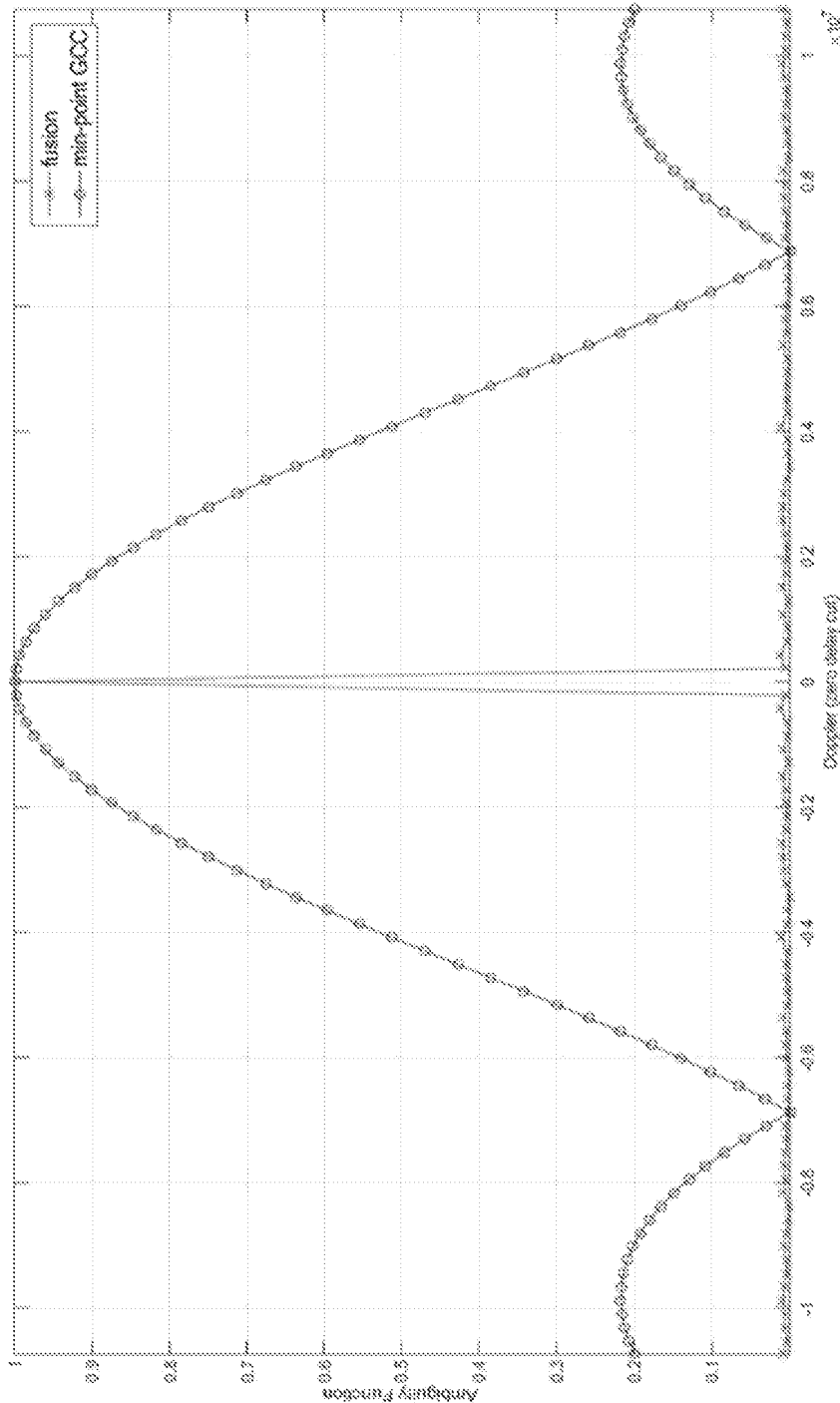
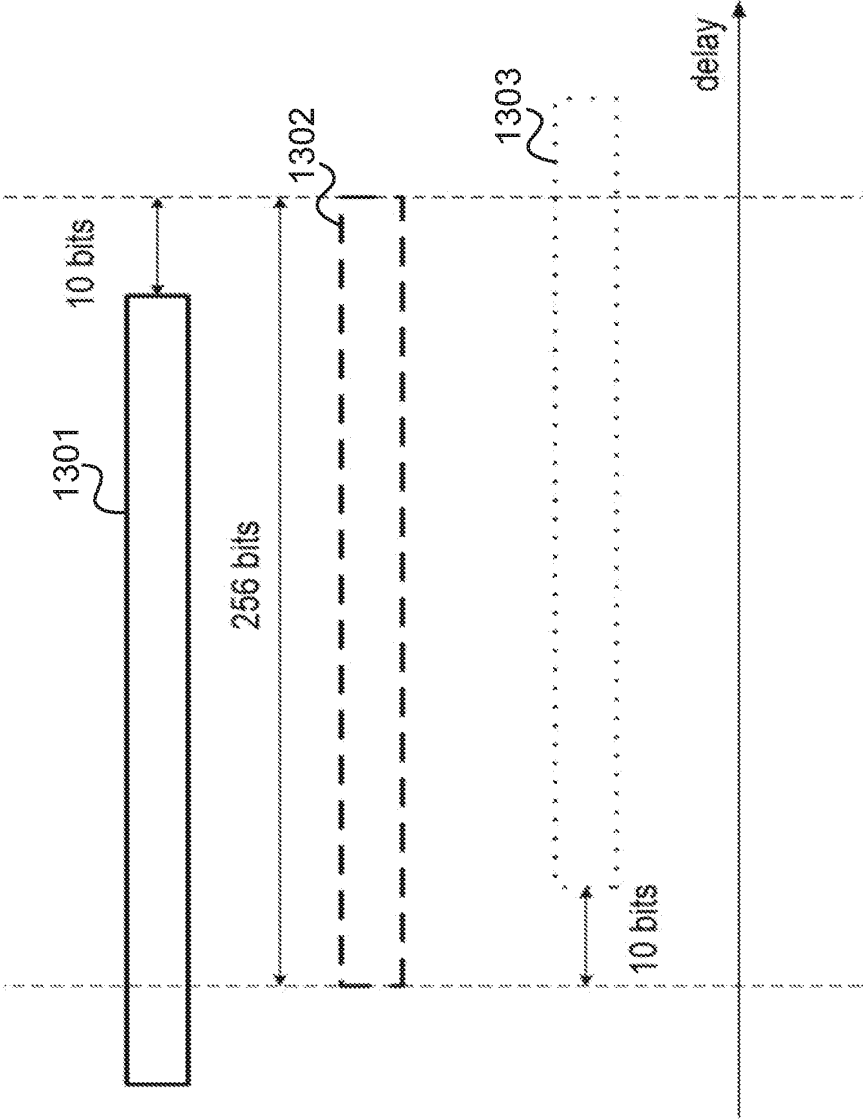


FIG 13



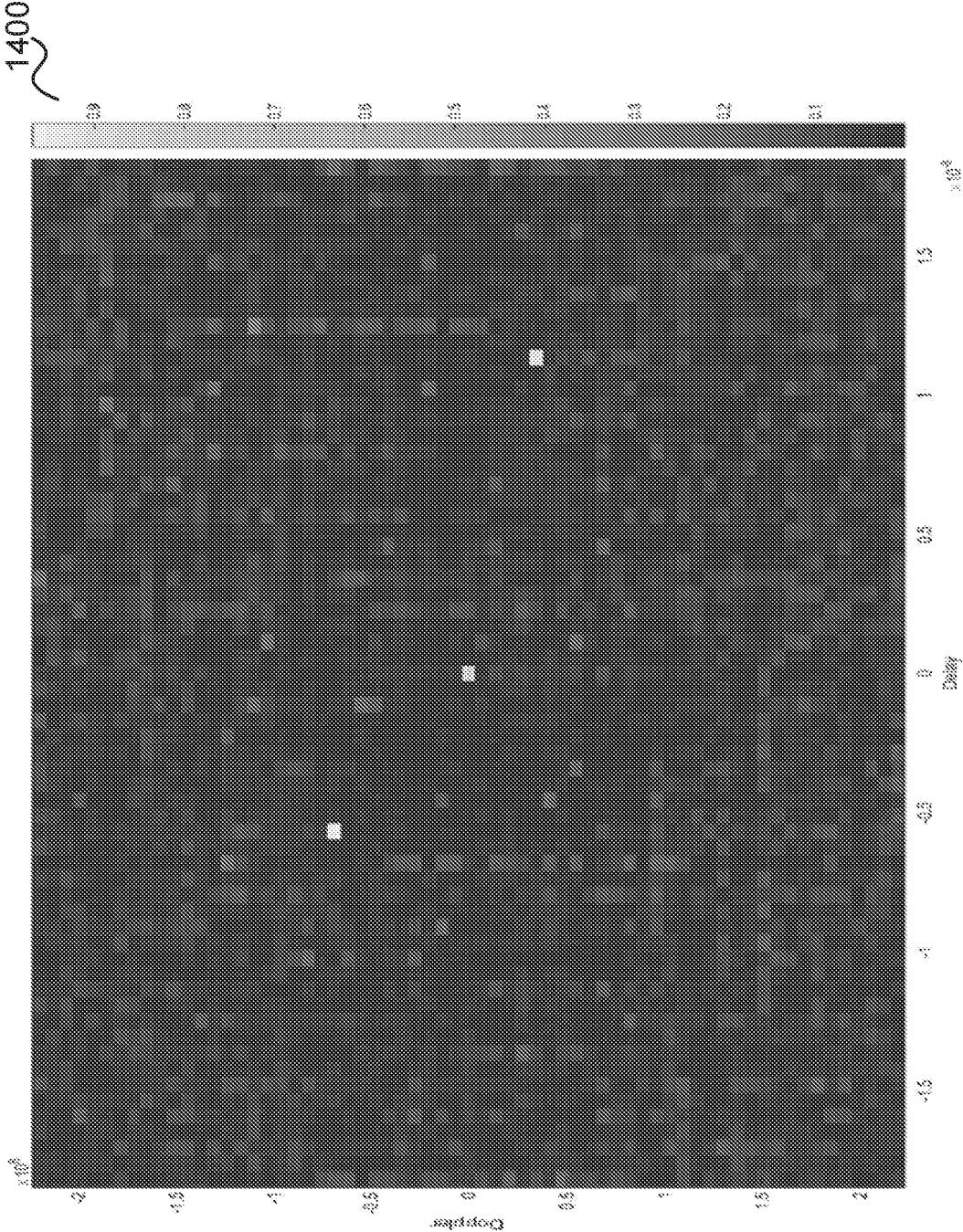


FIG 14

FIG 15

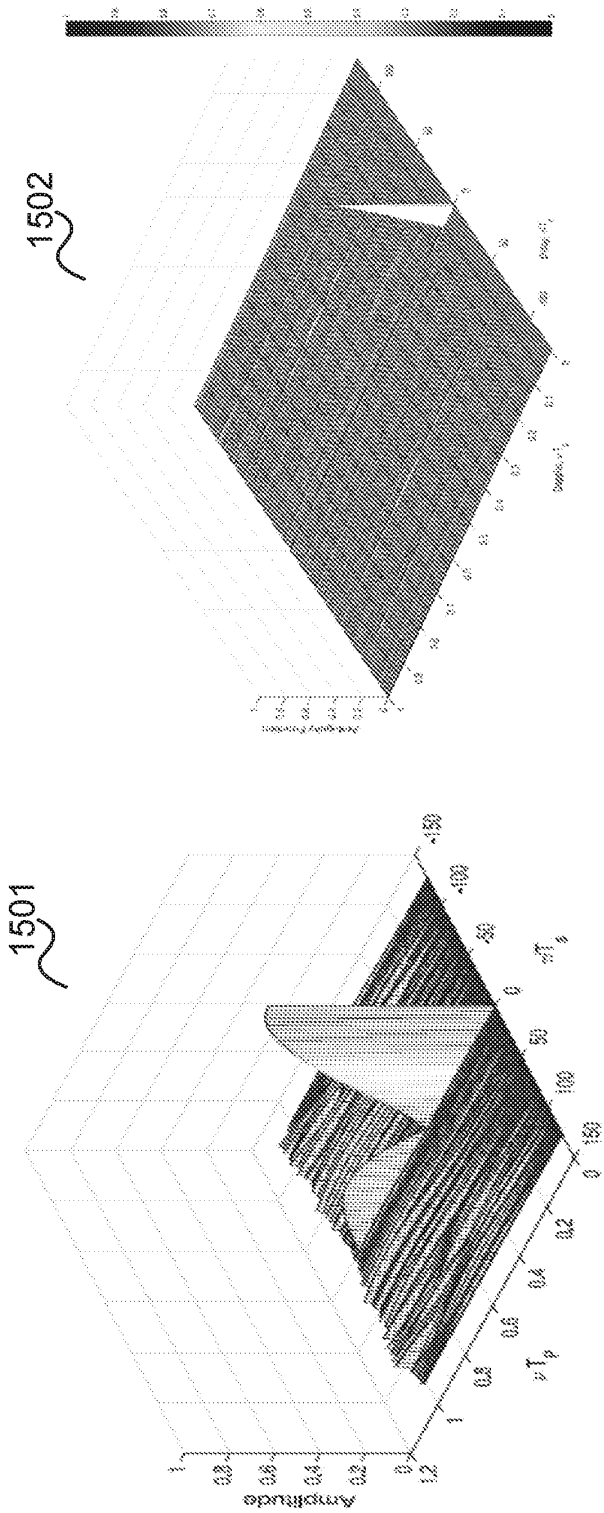


FIG 16

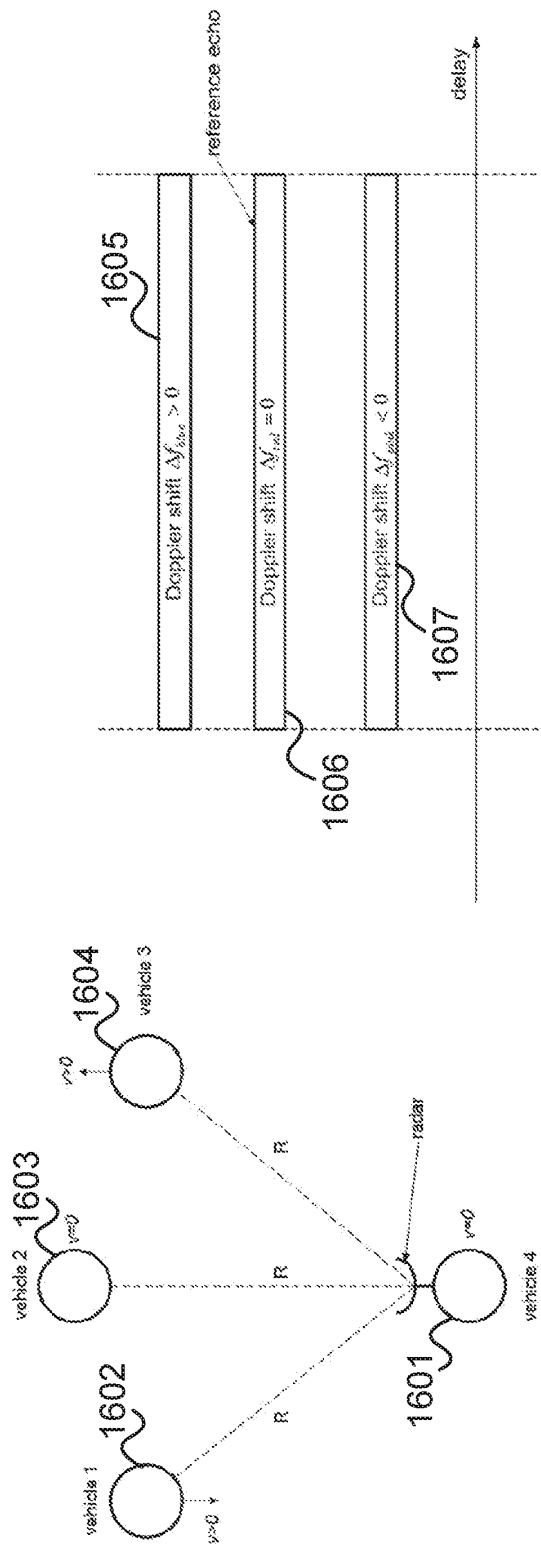


FIG 17

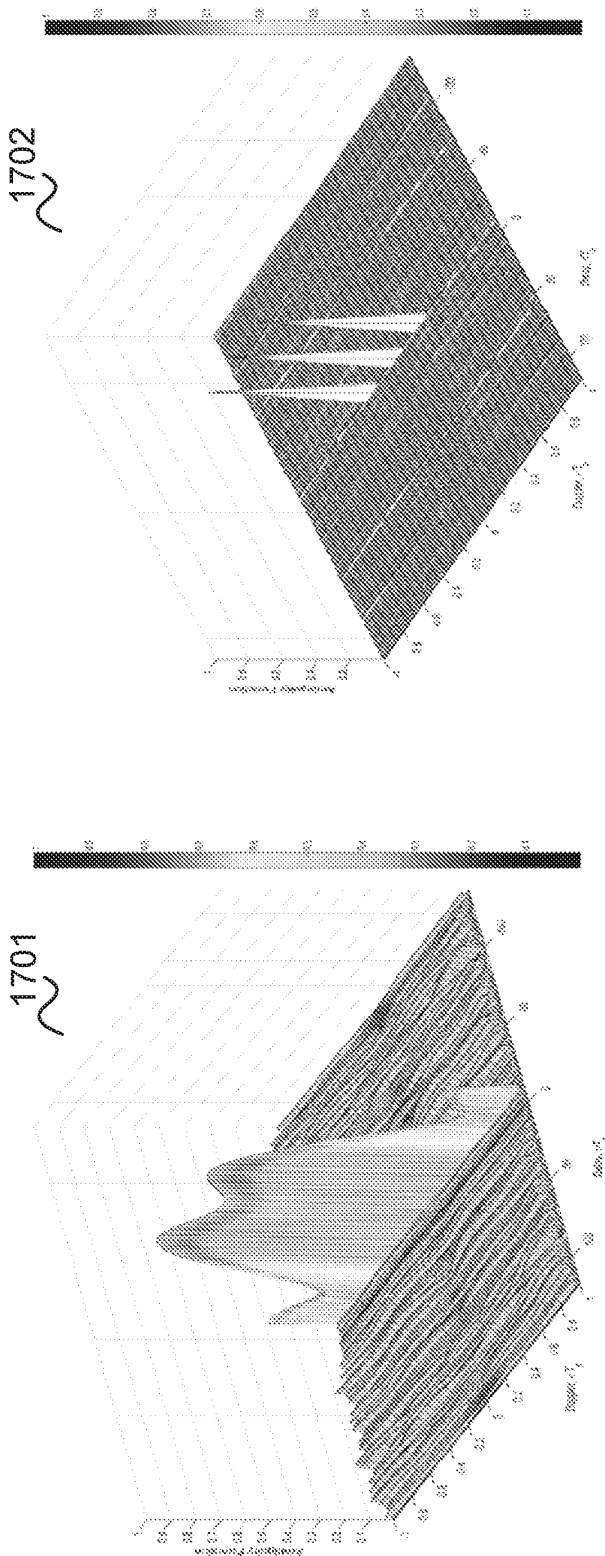


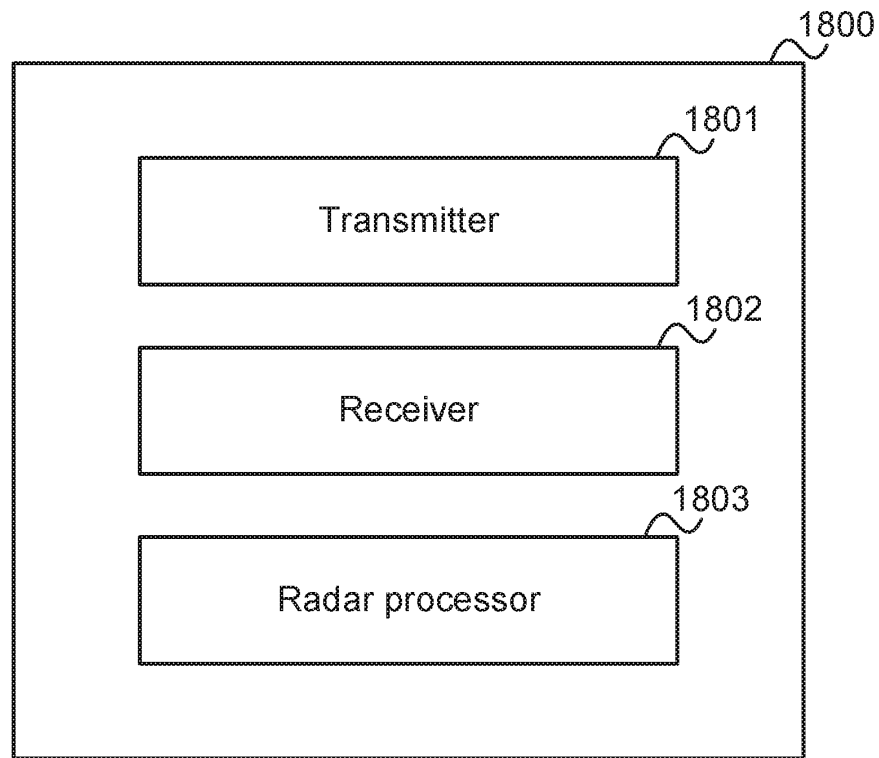
FIG 18

FIG 19