

SYNCHRONIZING/DRIVING CIRCUIT FOR A
FORWARD SYNCHRONOUS RECTIFIER

ABSTRACT OF THE INVENTION

5 An isolated forward switching power converter
includes a primary side circuit and a secondary side
circuit having an output inductor, a first MOS gated
transistor coupled in series with the output inductor, a
second MOS gated transistor coupled in shunt relationship
10 with the output inductor, and a synchronous rectifier
control circuit which senses the voltage across the
output inductor and alternately biases the first and
second transistors on and off in response thereto.

Fig. 1