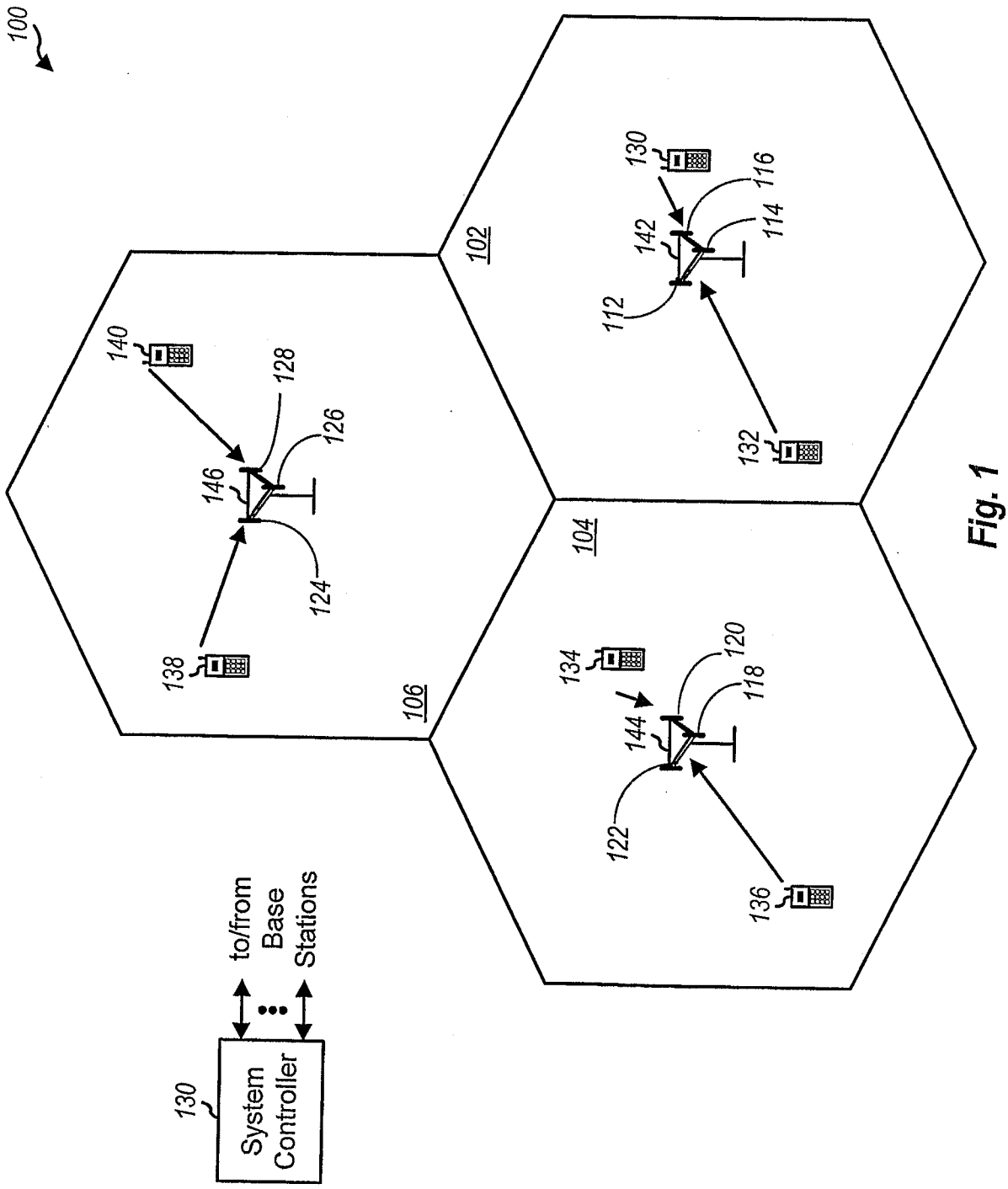

100
102
104
106
112
114
116
130
132
134
136
138
140
142
144
146
118
120
122
124
126
128
System Controller
130
to/from Base Stations

Fig. 1

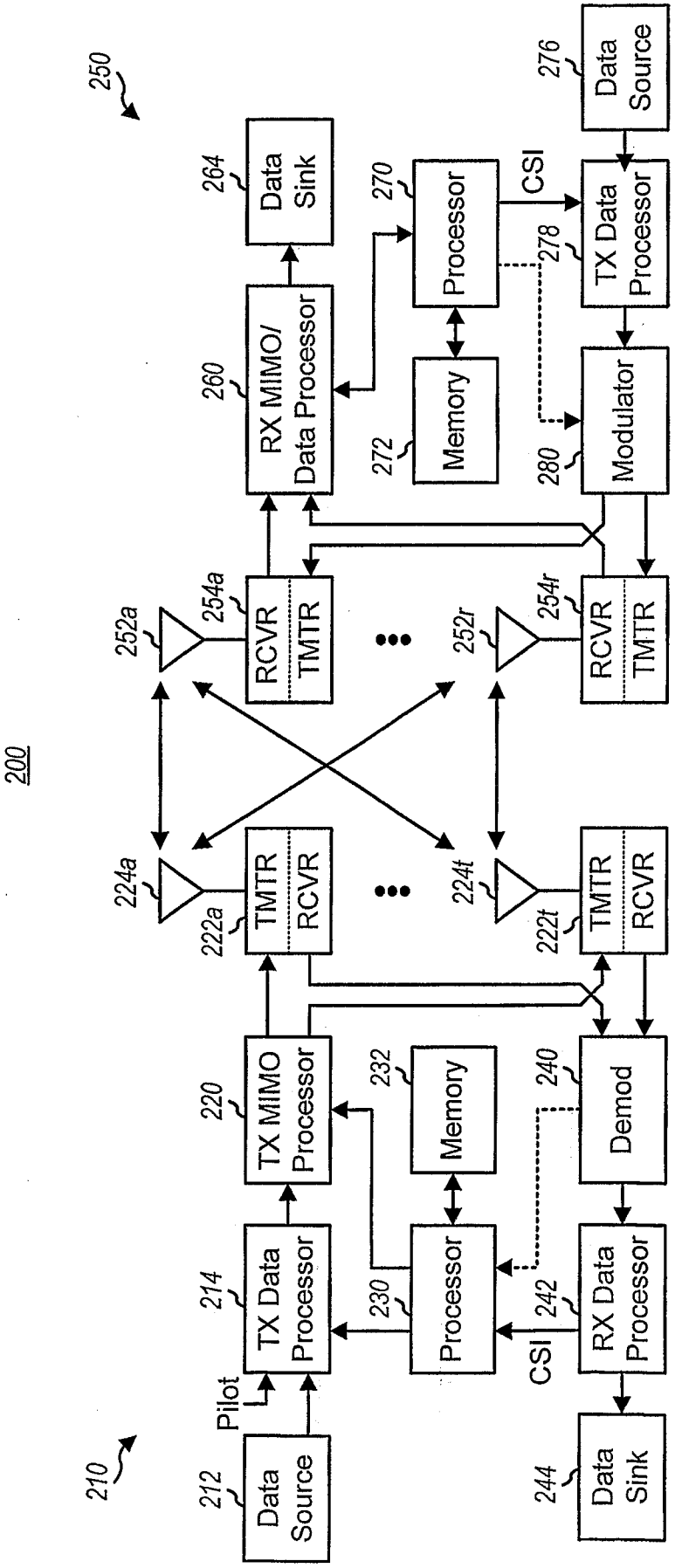

200
210
212
Data Source
Pilot
214
TX Data Processor
220
TX MIMO Processor
222a
TMTR
RCVR
224a
224t
222t
230
Processor
232
Memory
240
Demod
242
RX Data Processor
CSI
244
Data Sink
250
252a
254a
RCVR
TMTR
252r
254r
260
RX MIMO/ Data Processor
264
Data Sink
270
Processor
272
Memory
276
Data Source
278
TX Data Processor
CSI
280
Modulator

Fig. 2

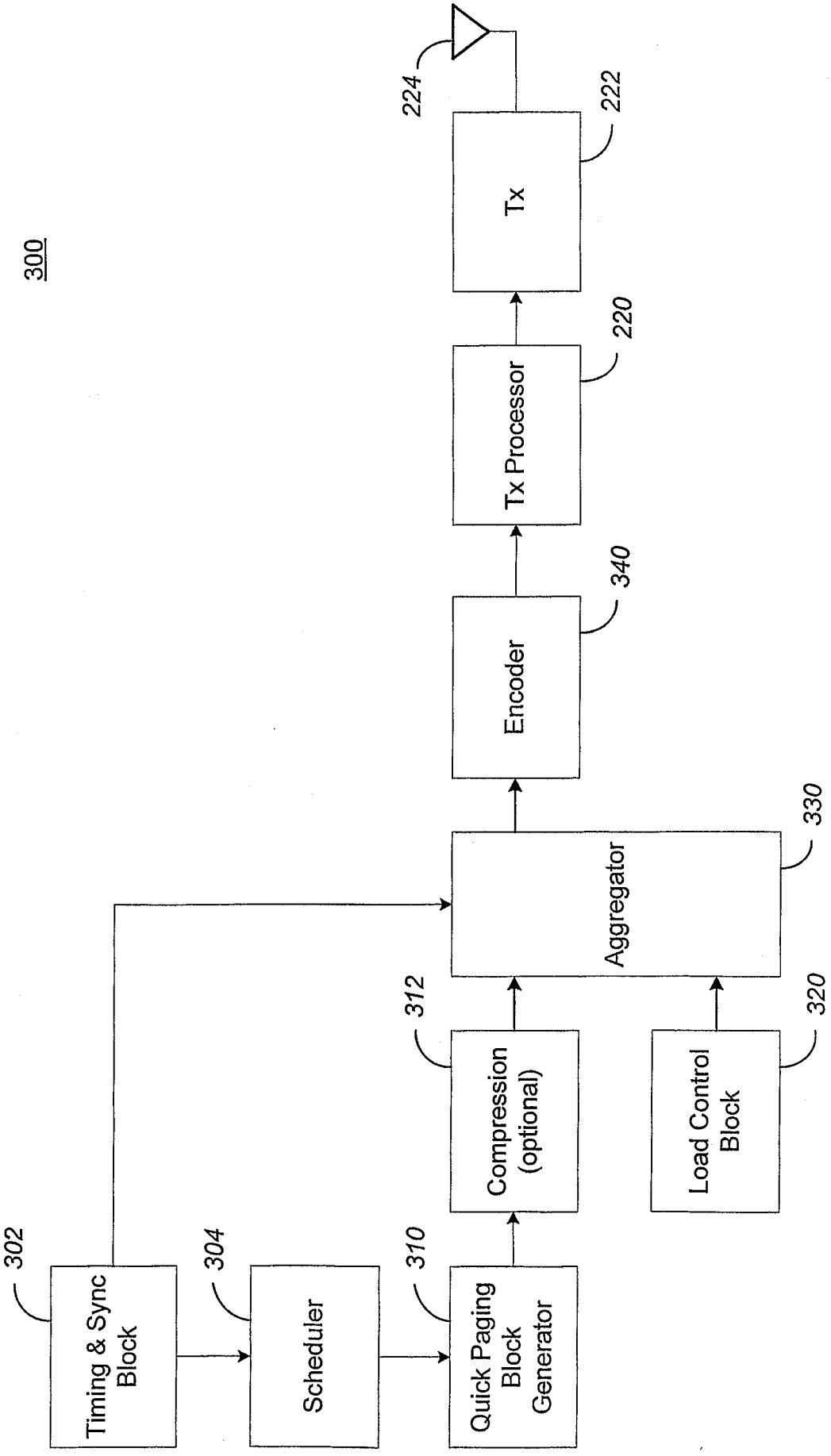

300
Timing & Sync Block
302
Scheduler
304
Quick Paging Block Generator
310
Compression (optional)
312
Load Control Block
320
Aggregator
330
Encoder
340
Tx Processor
220
Tx
222
224

Fig. 3

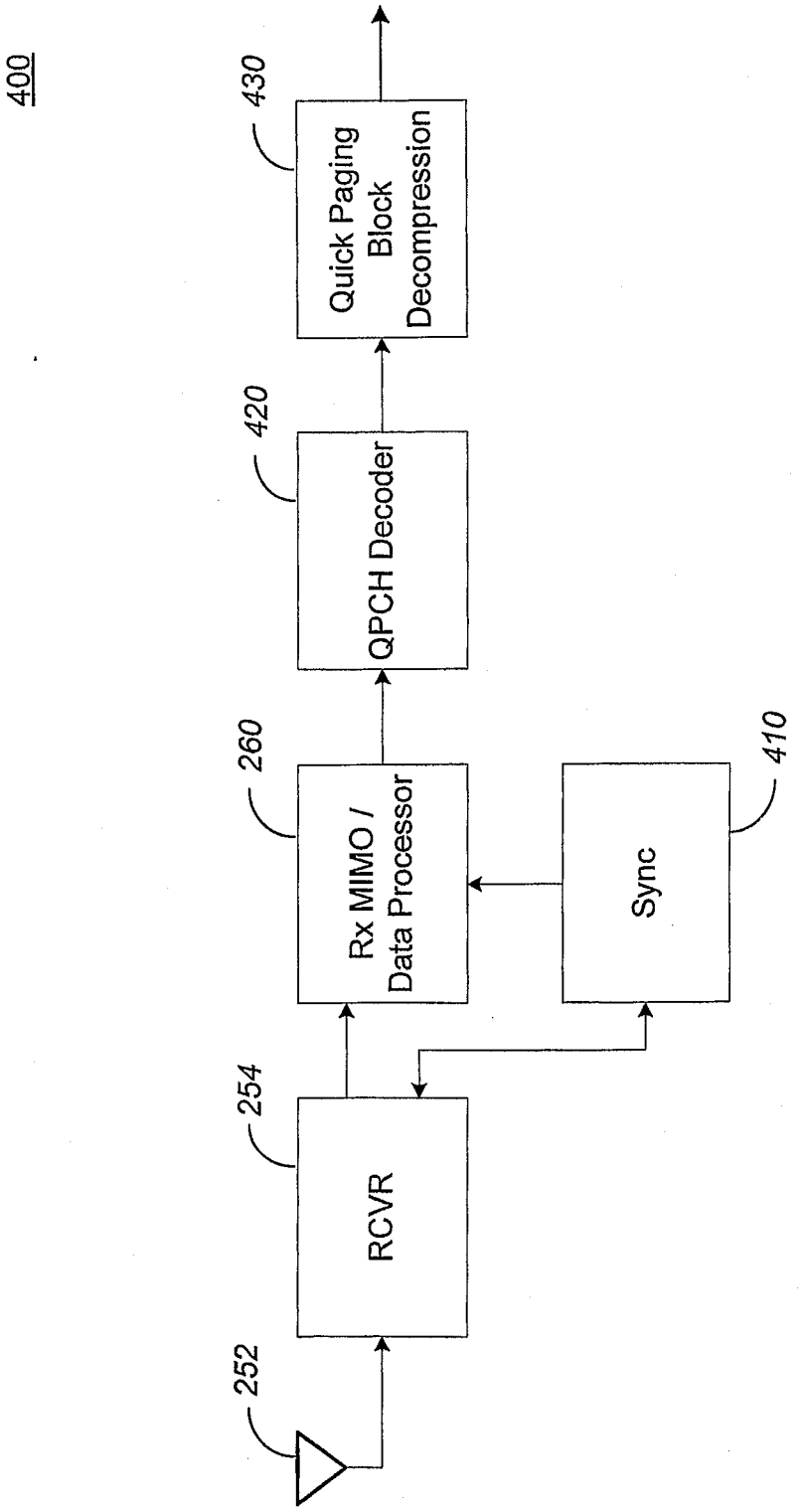

400
430
Quick Paging Block Decompression
420
QPCH Decoder
260
Rx MIMO / Data Processor
410
Sync
254
RCVR
252

Fig. 4

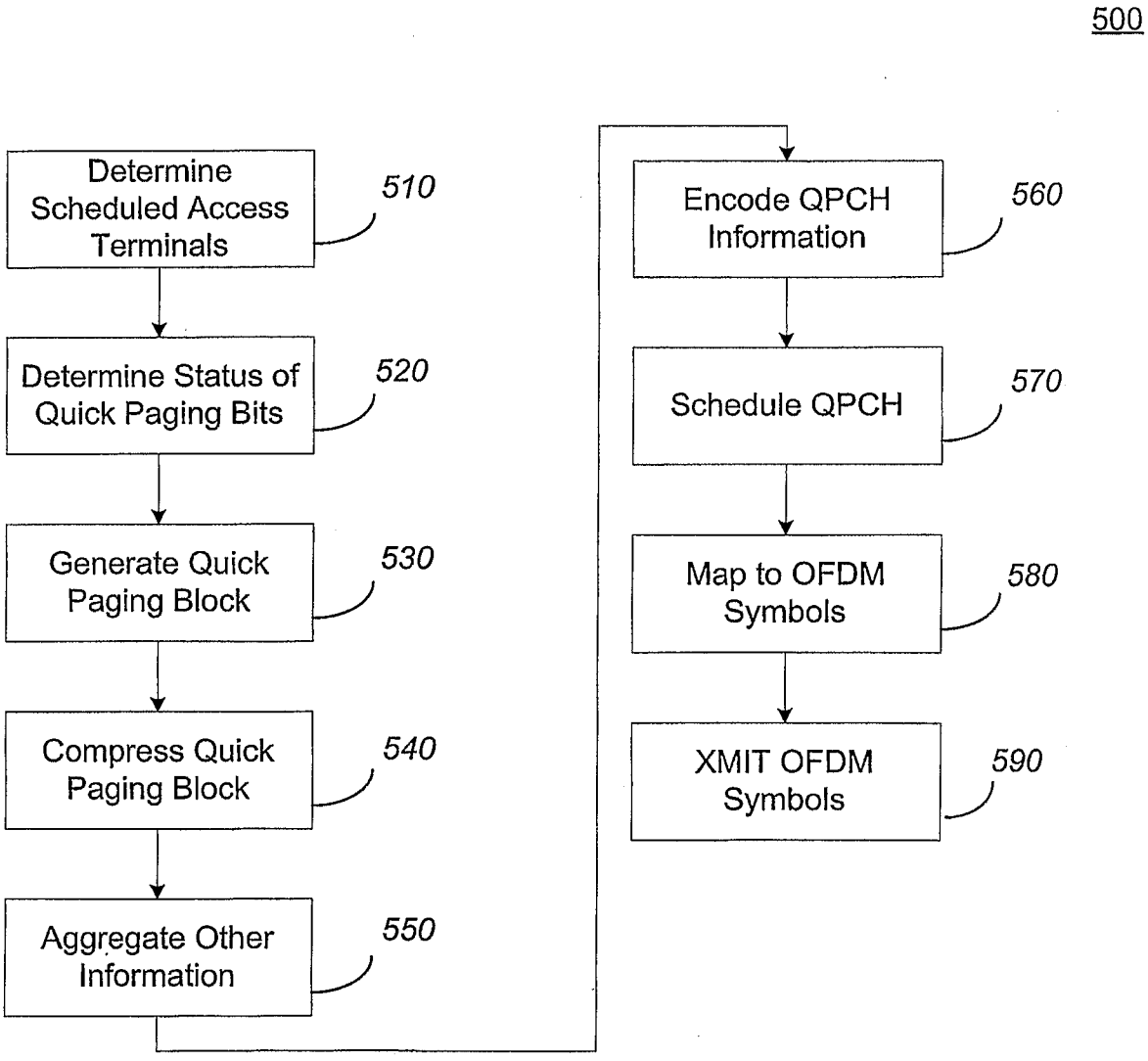

500
Determine Scheduled Access Terminals
510
Determine Status of Quick Paging Bits
520
Generate Quick Paging Block
530
Compress Quick Paging Block
540
Aggregate Other Information
550
Encode QPCH Information
560
Schedule QPCH
570
Map to OFDM Symbols
580
XMIT OFDM Symbols
590

Fig. 5

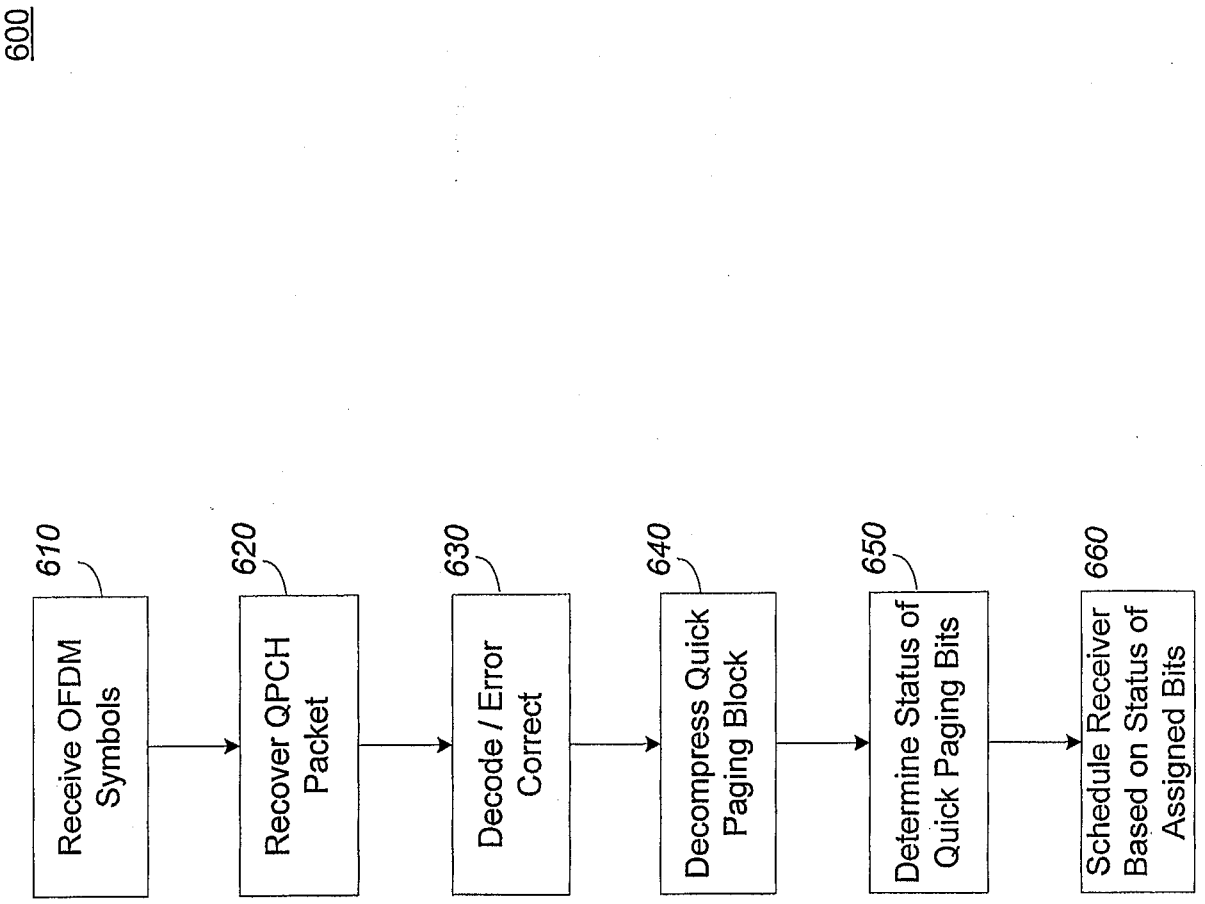

600
610
Receive OFDM Symbols
620
Recover QPCH Packet
630
Decode / Error Correct
640
Decompress Quick Paging Block
650
Determine Status of Quick Paging Bits
660
Schedule Receiver Based on Status of Assigned Bits

Fig. 6

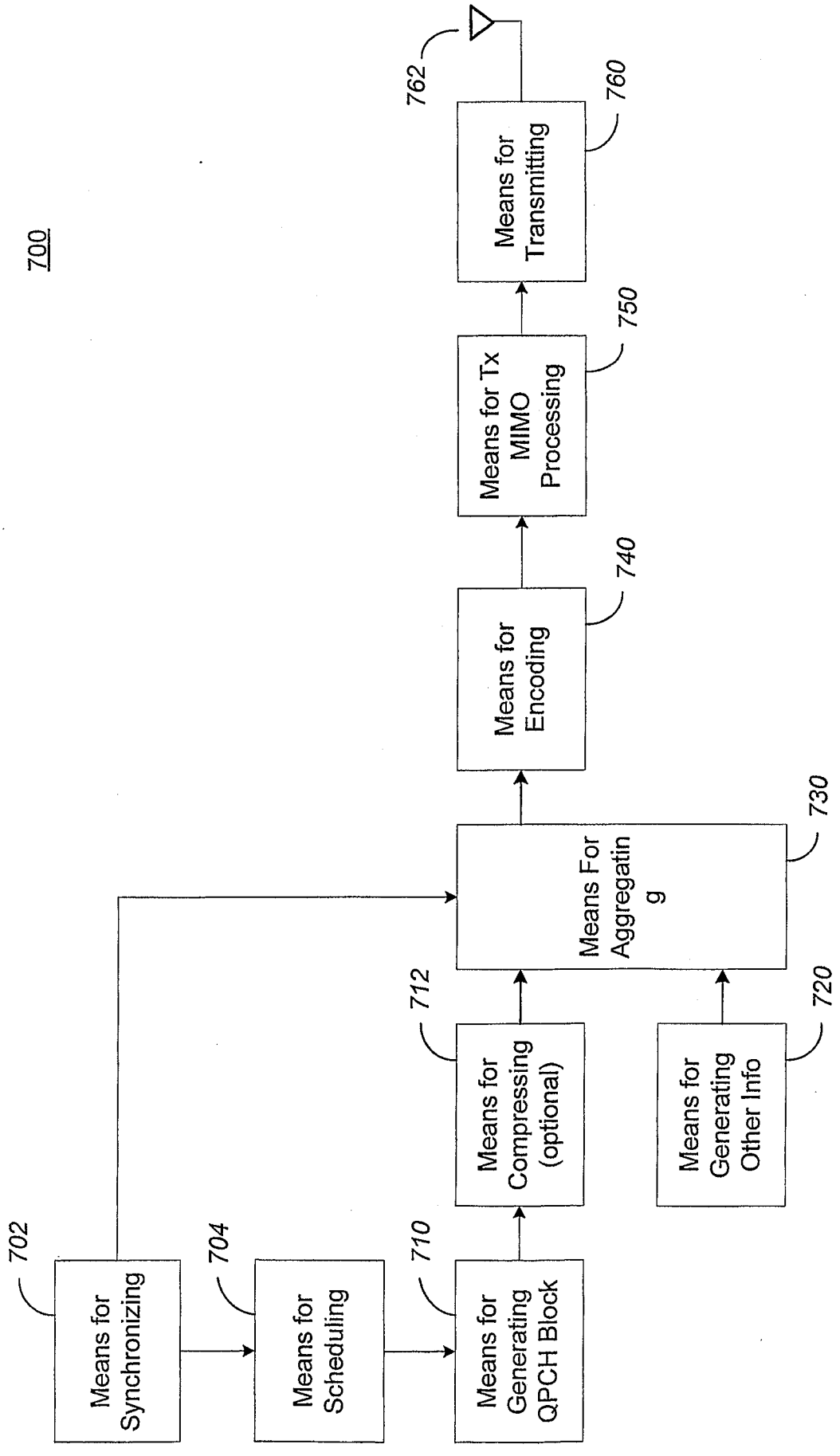

700
762
760
Means for Transmitting
750
Means for Tx MIMO Processing
740
Means for Encoding
730
Means For Aggregatin g
712
Means for Compressing (optional)
720
Means for Generating Other Info
702
Means for Synchronizing
704
Means for Scheduling
710
Means for Generating QPCH Block

Fig. 7

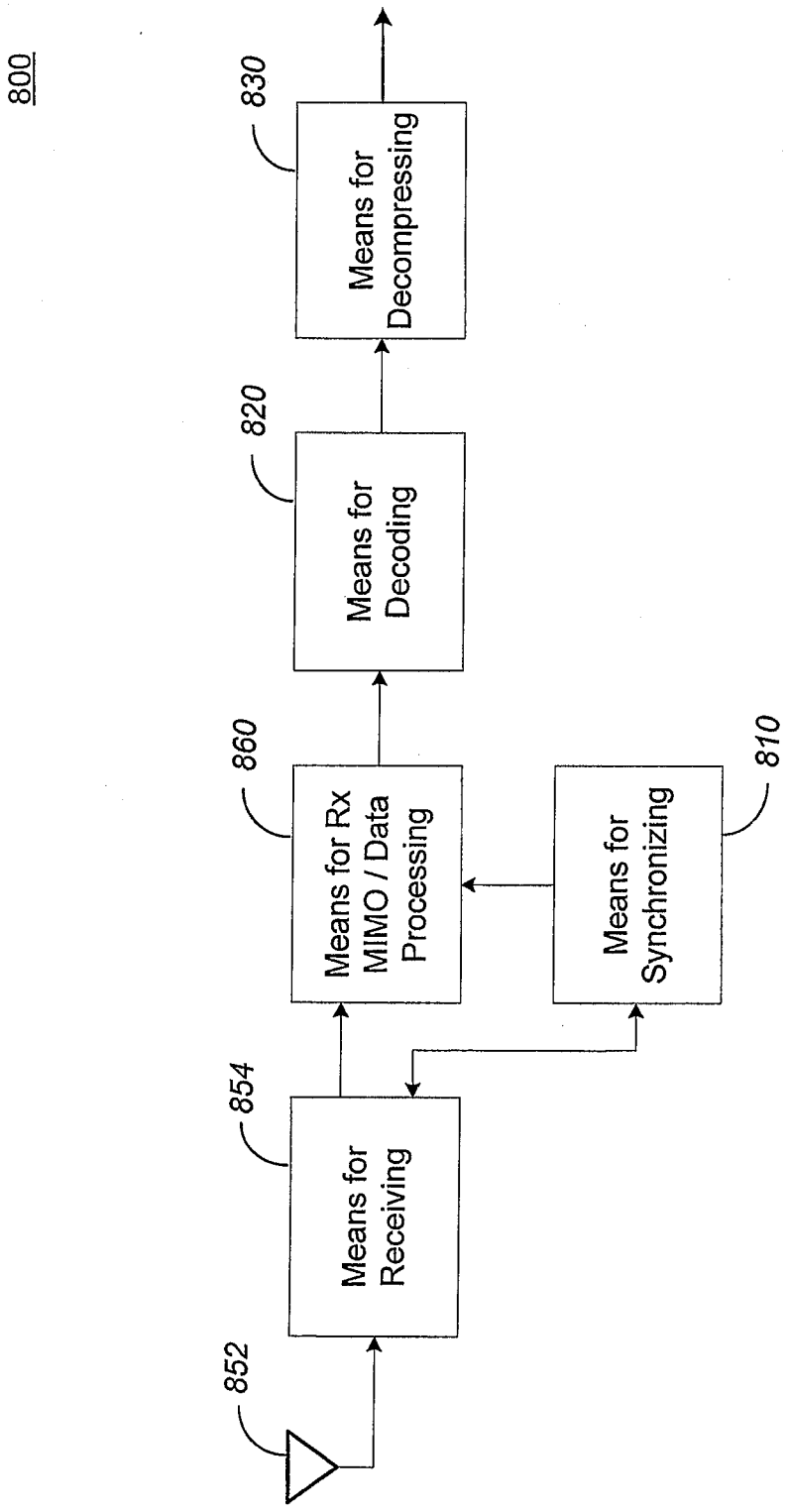

800
830
Means for Decompressing
820
Means for Decoding
860
Means for Rx MIMO / Data Processing
810
Means for Synchronizing
854
Means for Receiving
852

Fig. 8