

FIG. 1

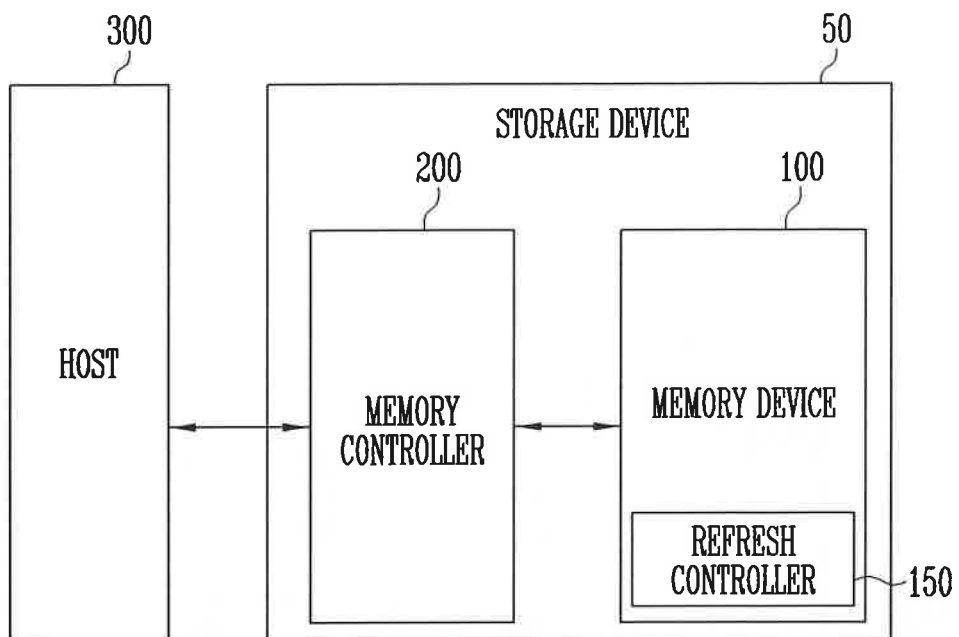


FIG. 2

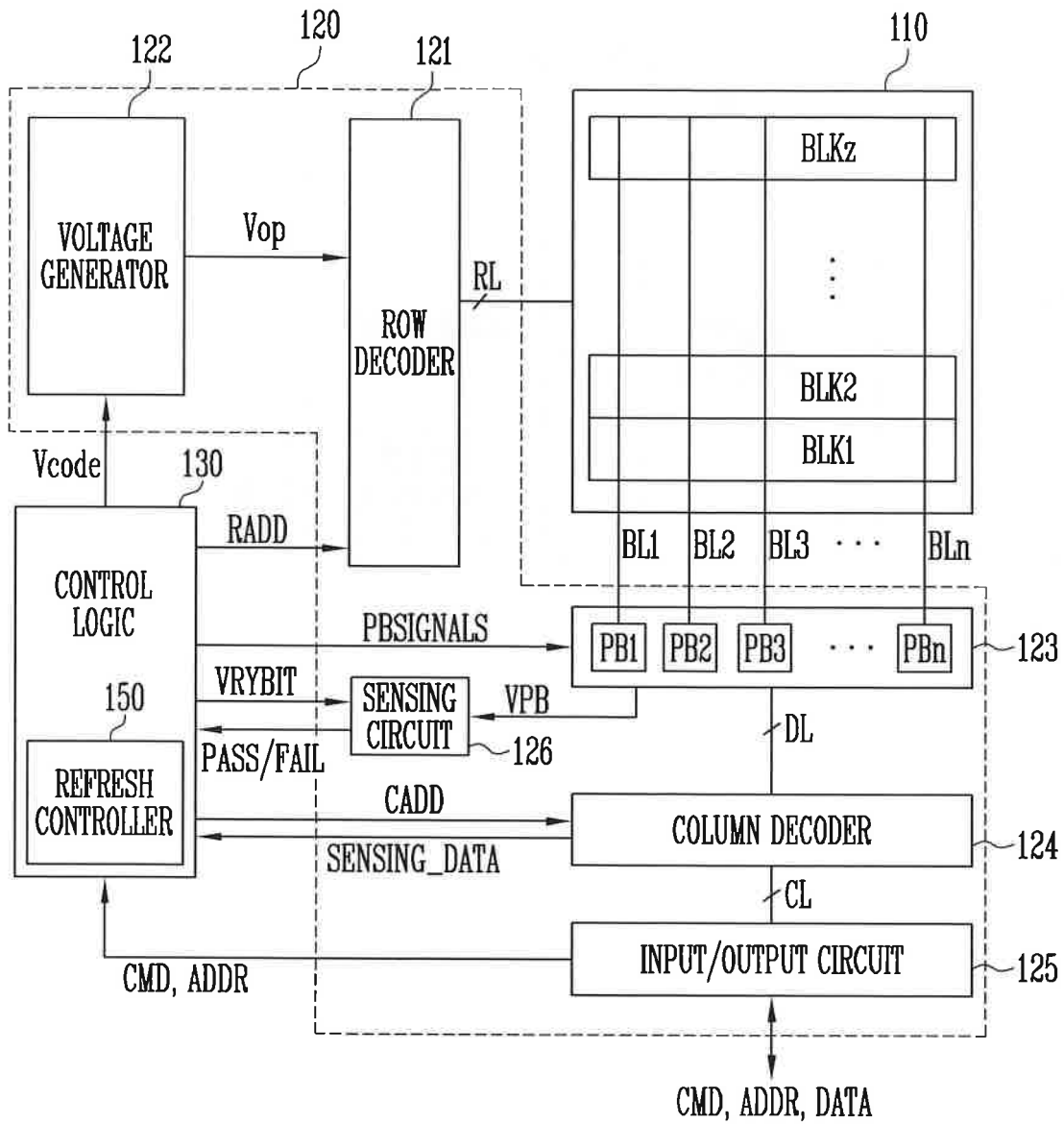
100

FIG. 3

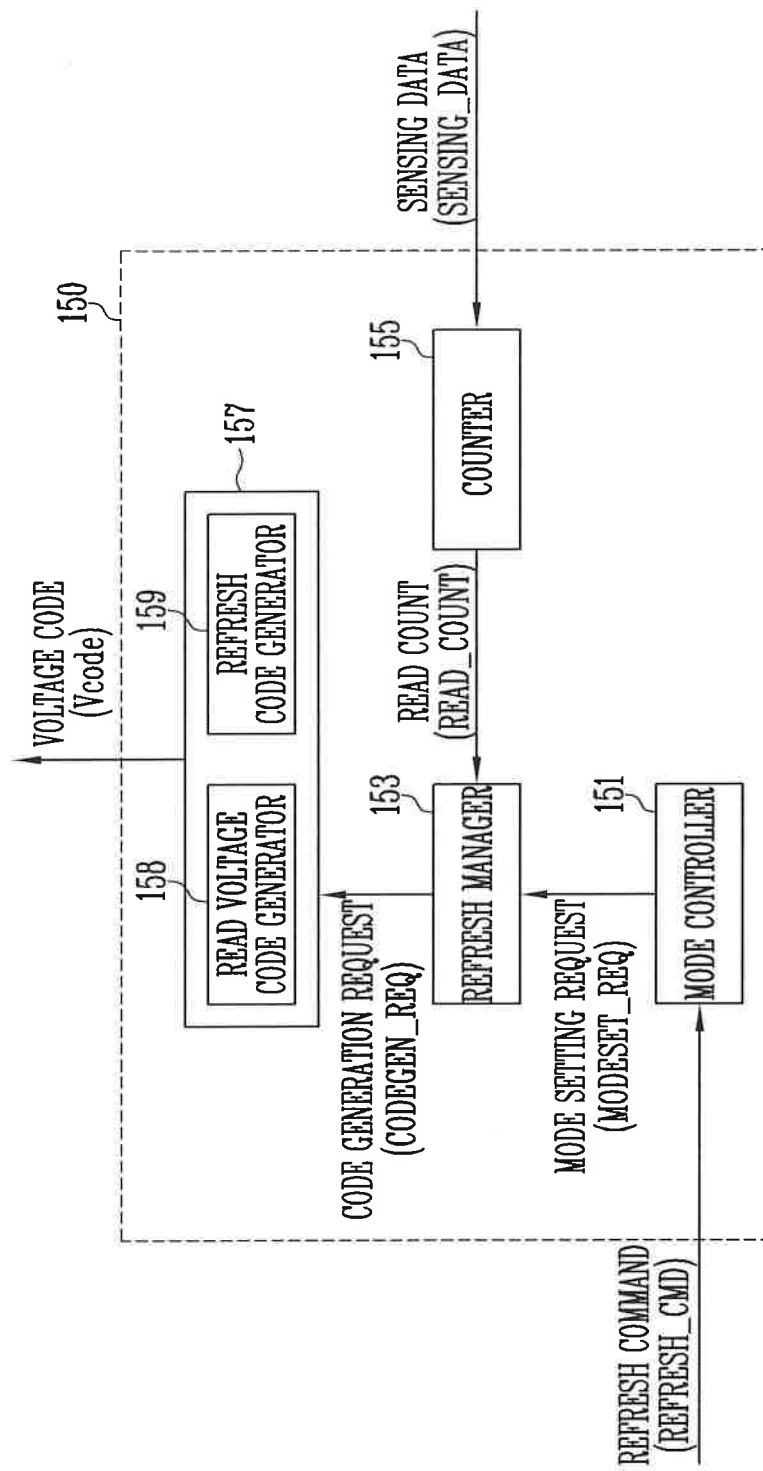


FIG. 4

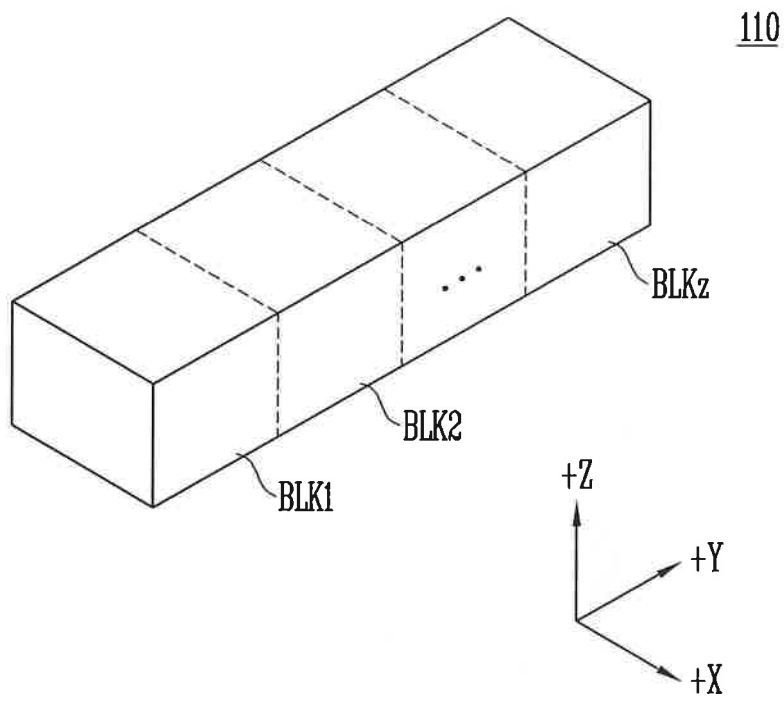


FIG. 5

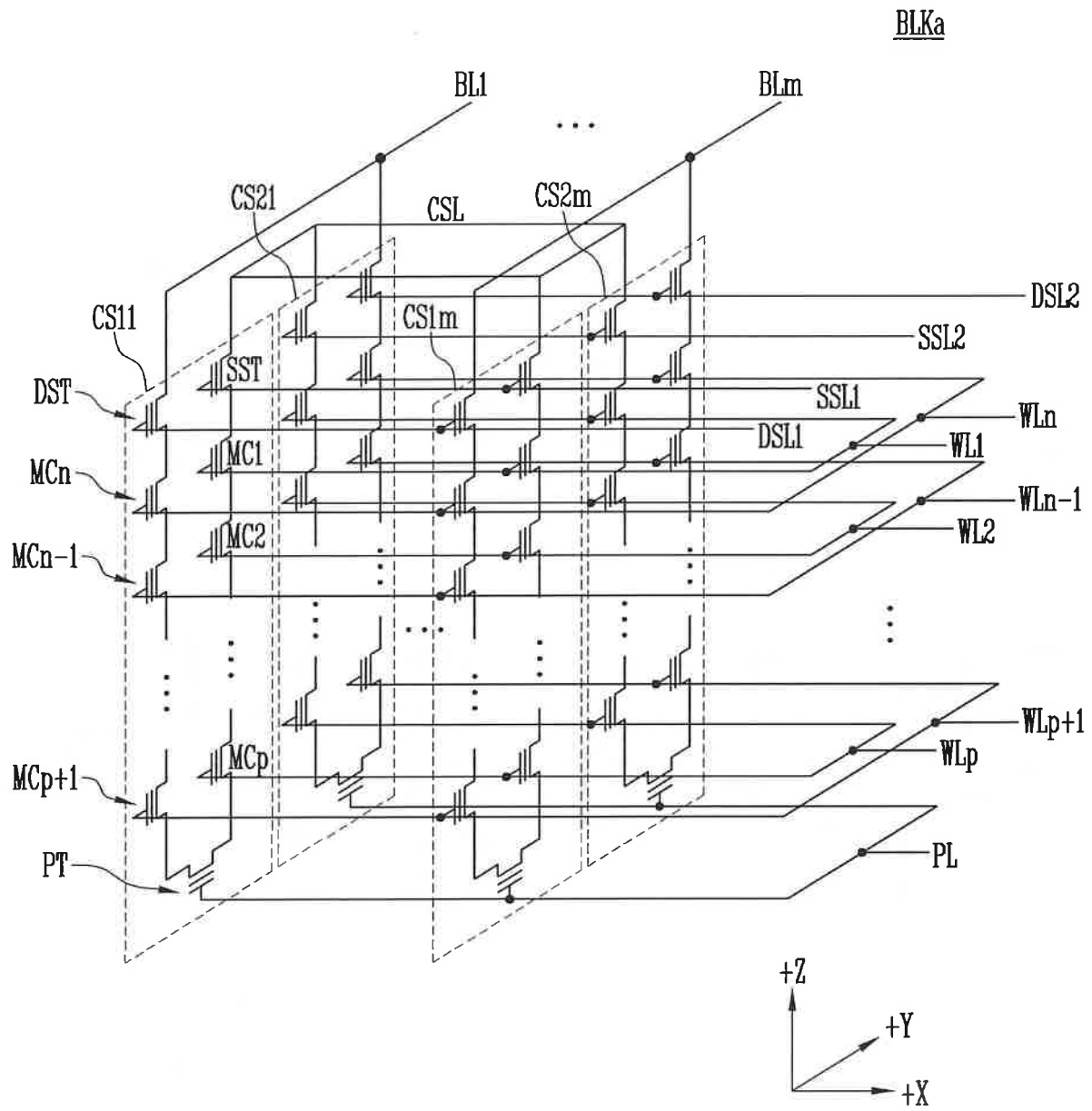


FIG. 6

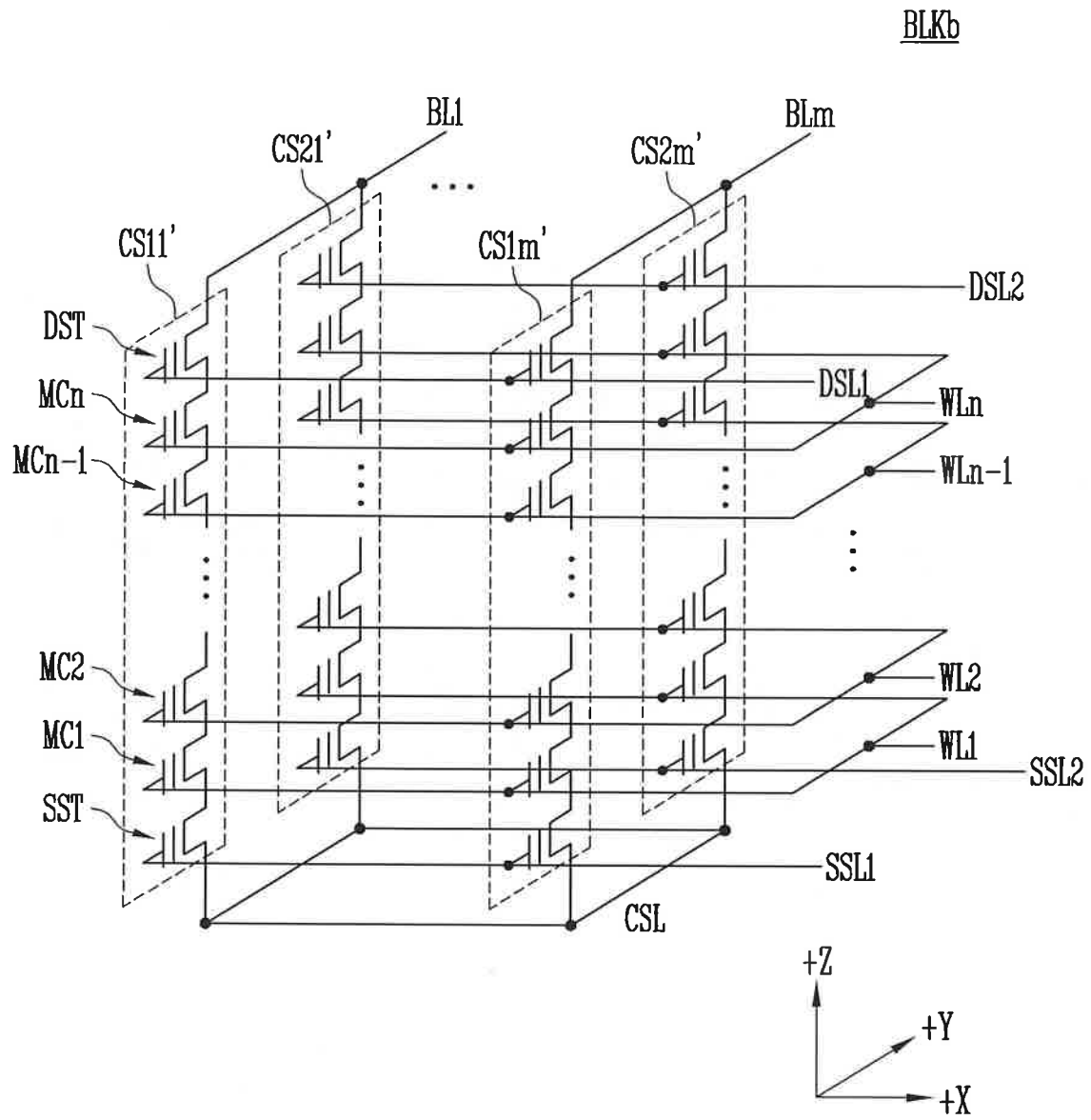


FIG. 7

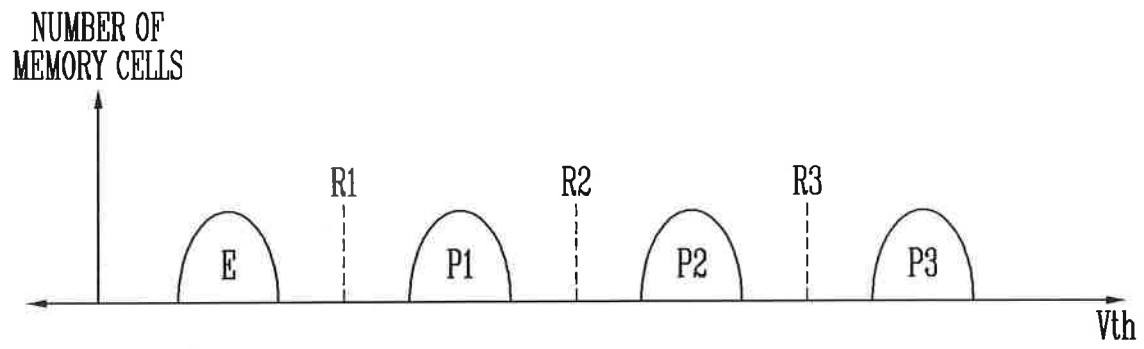


FIG. 8

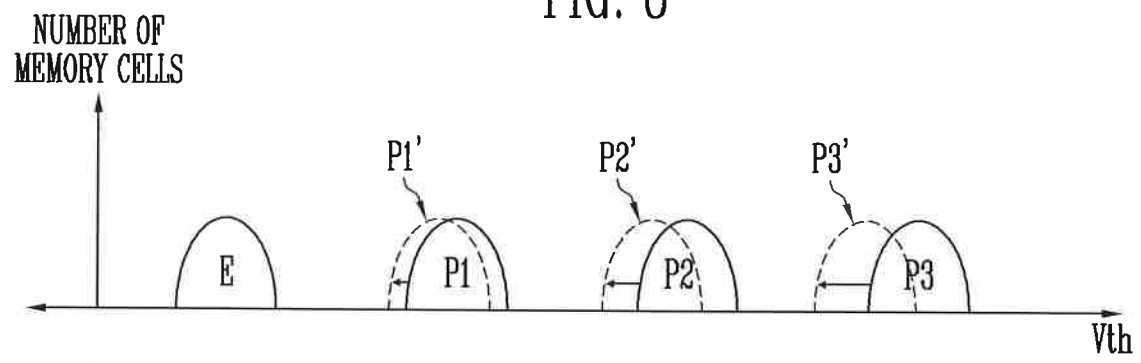


FIG. 9

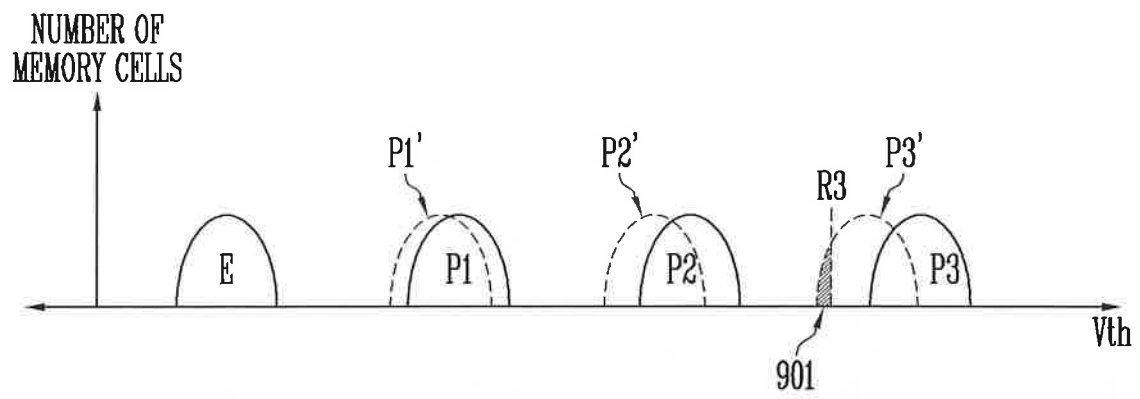


FIG. 10

REFERENCE COUNT (REF_COUNT)	READ COUNT (READ_COUNT)	REFERENCE COUNT - READ COUNT
REF_COUNT3	READ_COUNT1	REF_COUNT3 - READ_COUNT1
REF_COUNT3	READ_COUNT2	REF_COUNT3 - READ_COUNT2
REF_COUNT3	READ_COUNT3	REF_COUNT3 - READ_COUNT3
REF_COUNT3	READ_COUNT4	REF_COUNT3 - READ_COUNT4

FIG. 11

PROGRAMMED STATE (P)	REFERENCE READ VOLTAGE (R)	SHIFTED READ VOLTAGE (SHIFT_RV)		
P1	R1	R1'	R1''	R1'''
P2	R2	R2'	R2''	R2'''
P3	R3	R3'	R3''	R3'''

FIG. 12

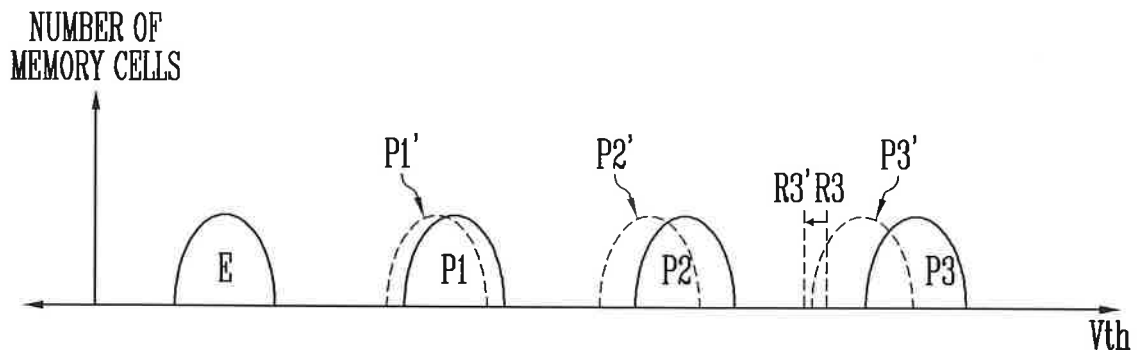


FIG. 13

SHIFTING COUNT (SHIFTING_COUNT)	STEP VOLTAGE (Vstep)
1	Vstep1
2	Vstep2
3	Vstep3
4	Vstep4

FIG. 14

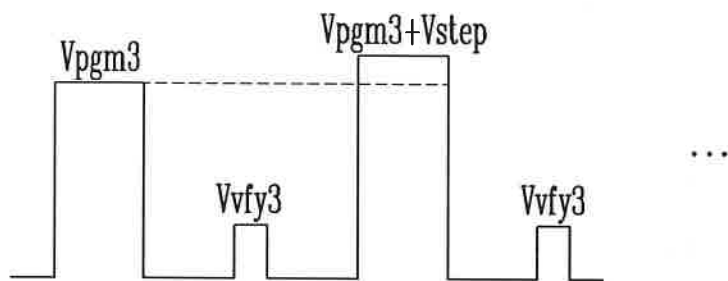


FIG. 15

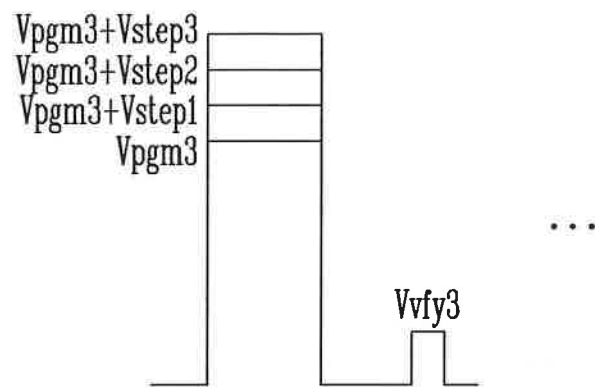


FIG. 16

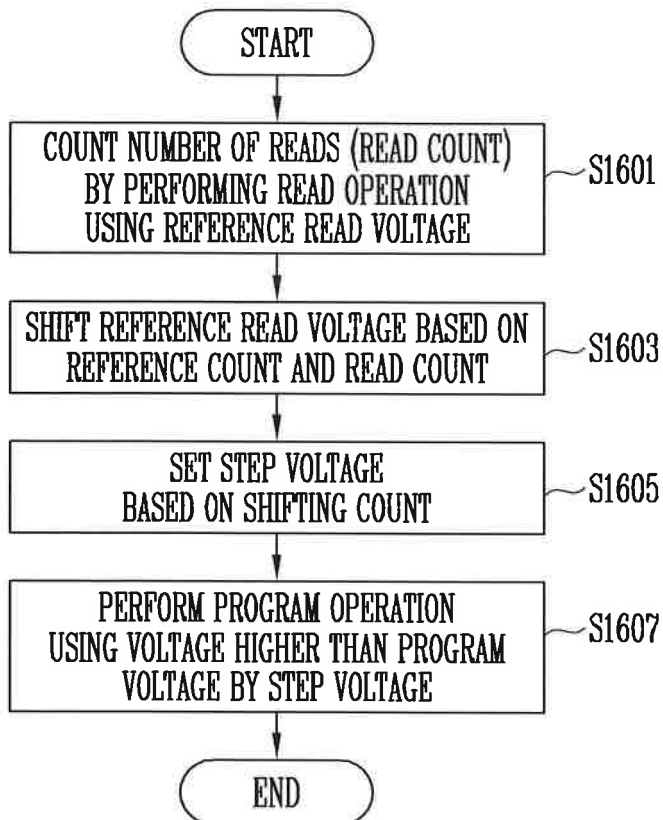


FIG. 17

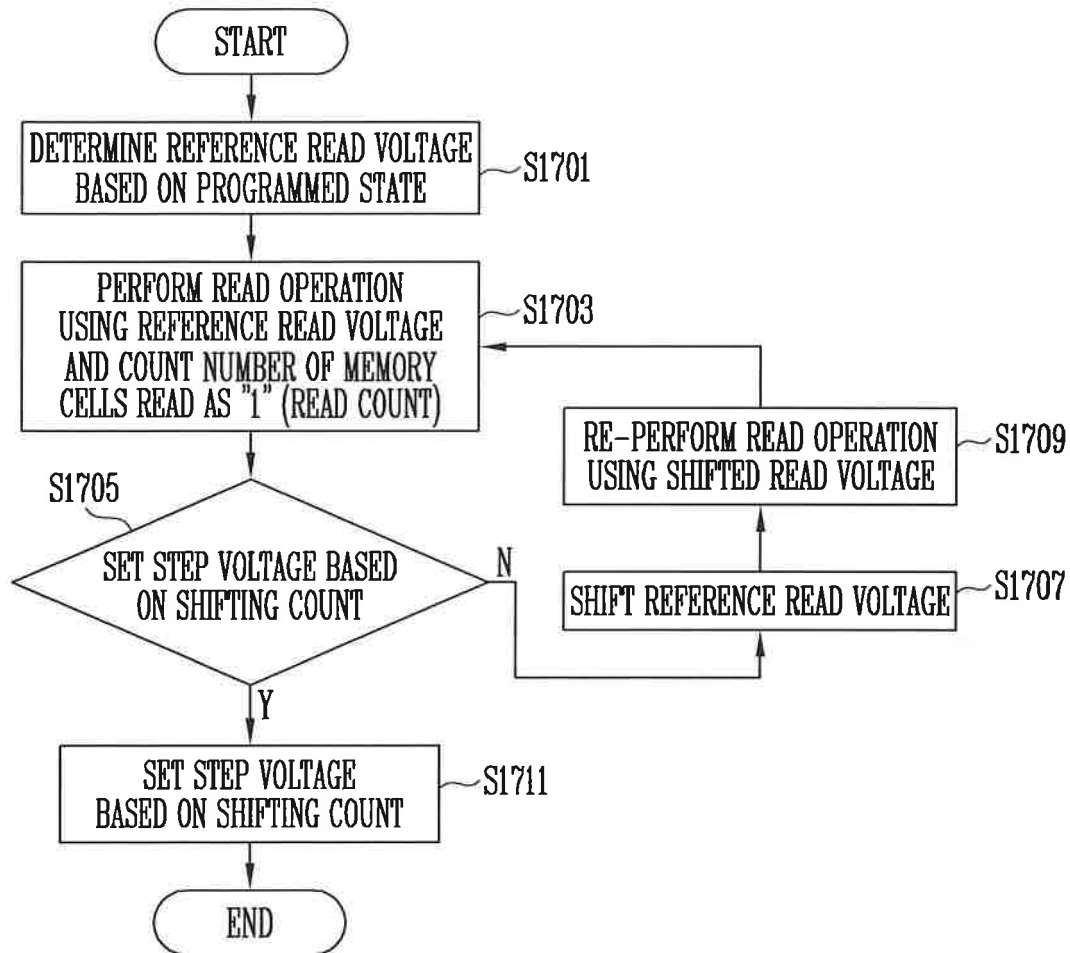


FIG. 18

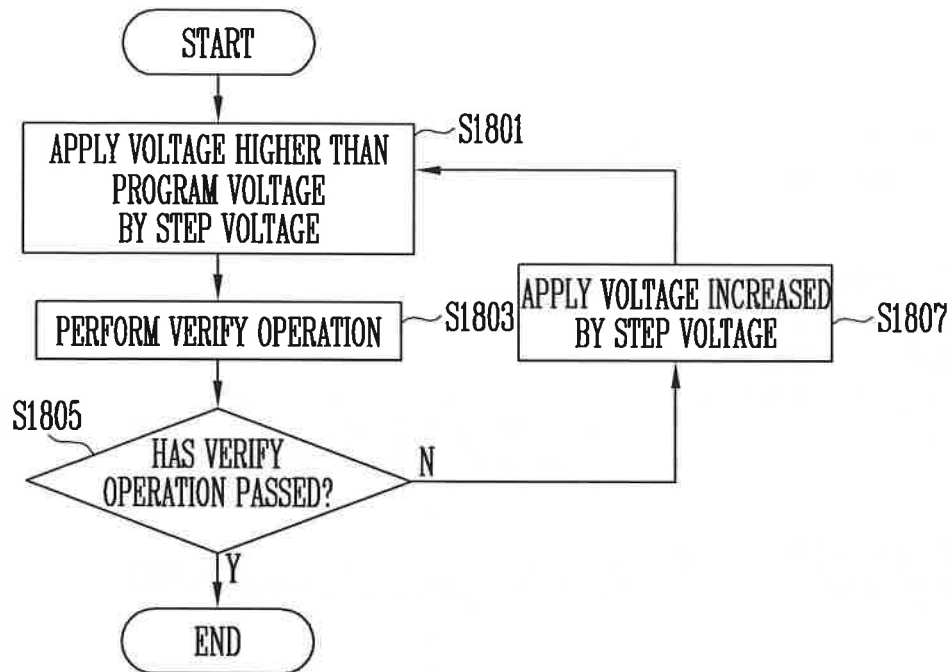


FIG. 19

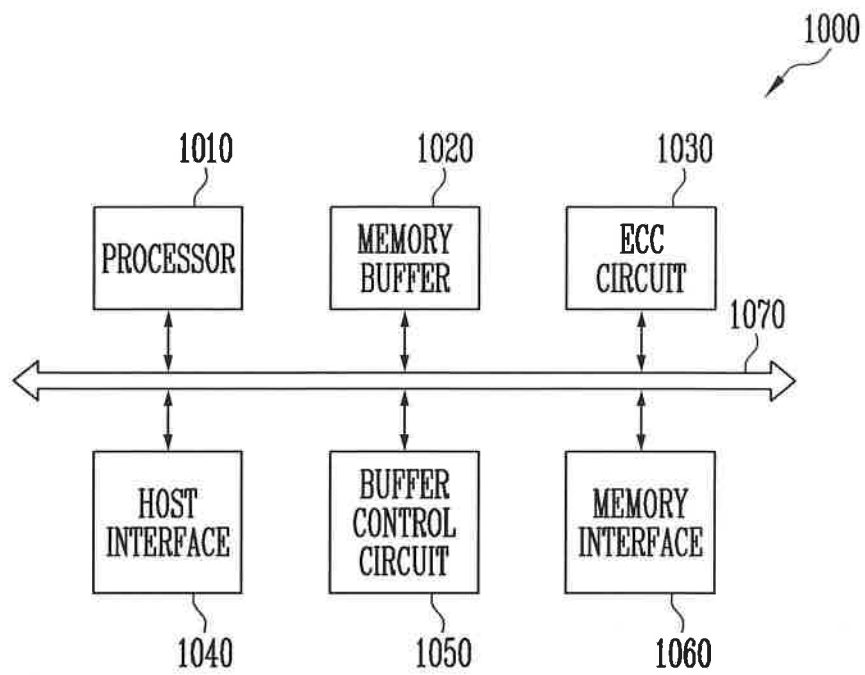


FIG. 20

2000

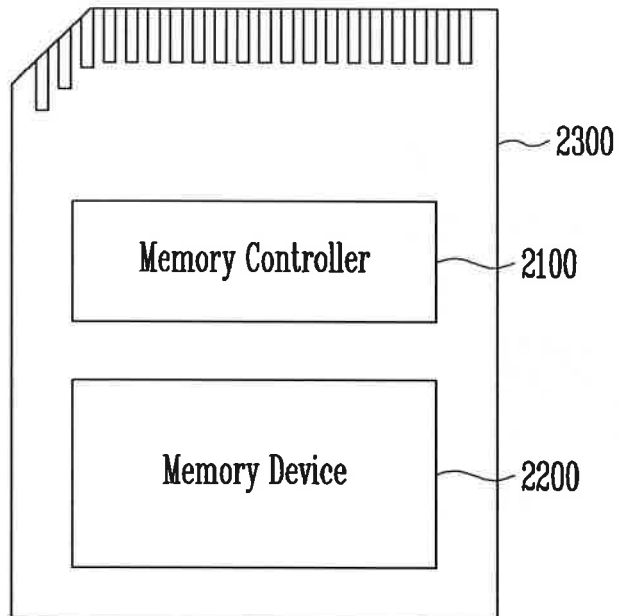


FIG. 21

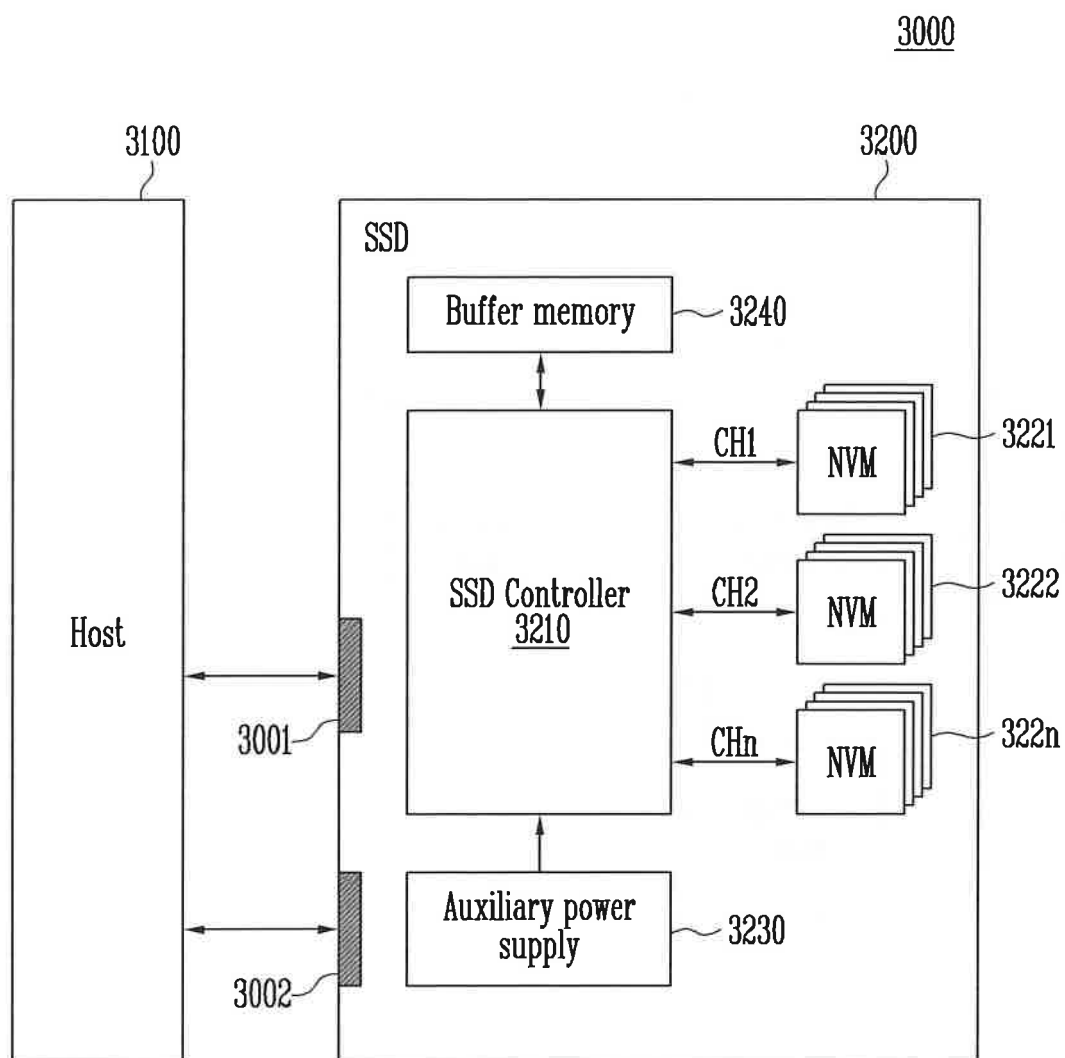


FIG. 22

