FIG. 1

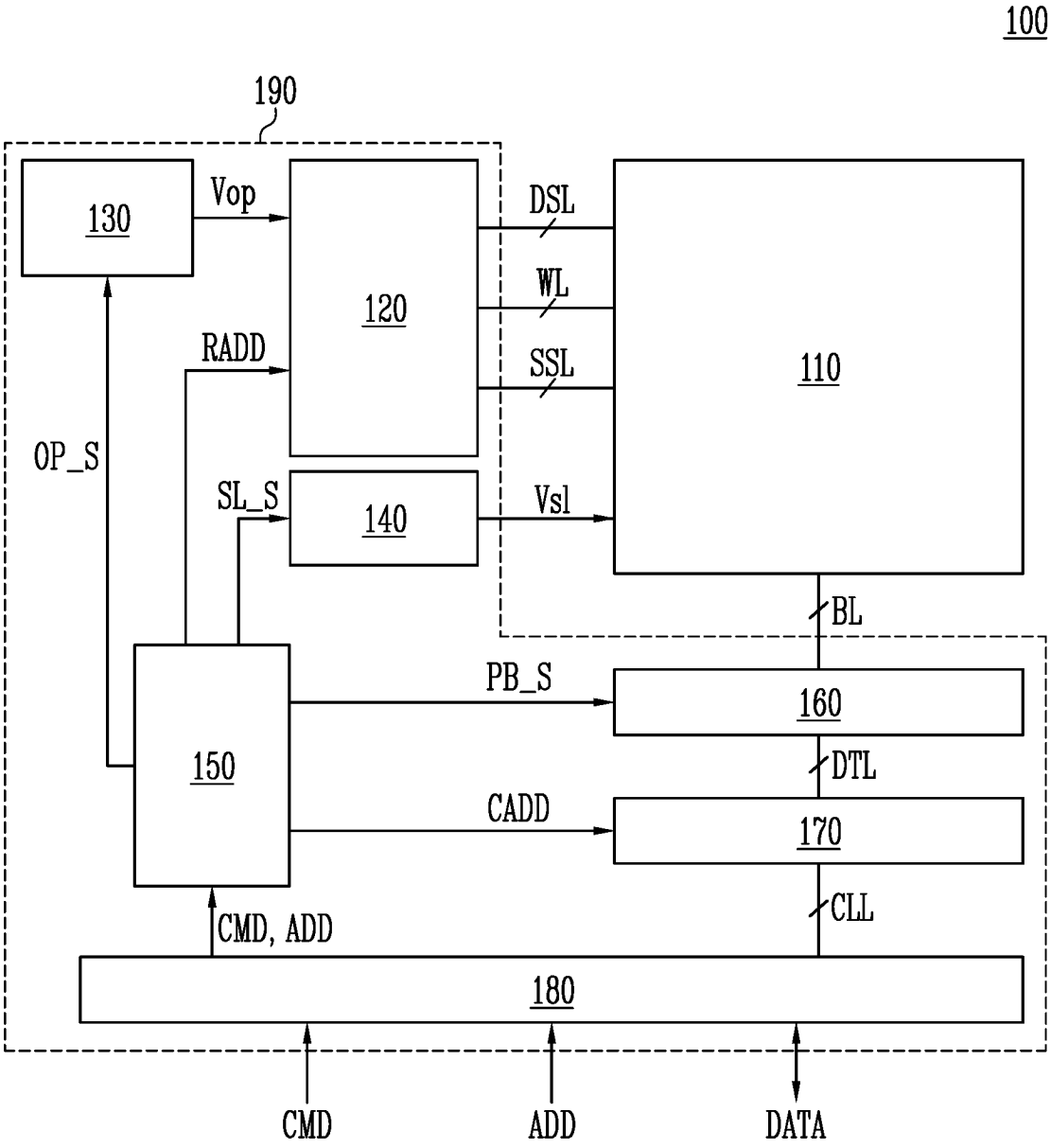

100
190
130
Vop
120
DSL
WL
SSL
110
RADD
OP_S
SL_S
140
Vsl
BL
PB_S
160
150
DTL
CADD
170
CLL
CMD, ADD
180
CMD
ADD
DATA

FIG. 2

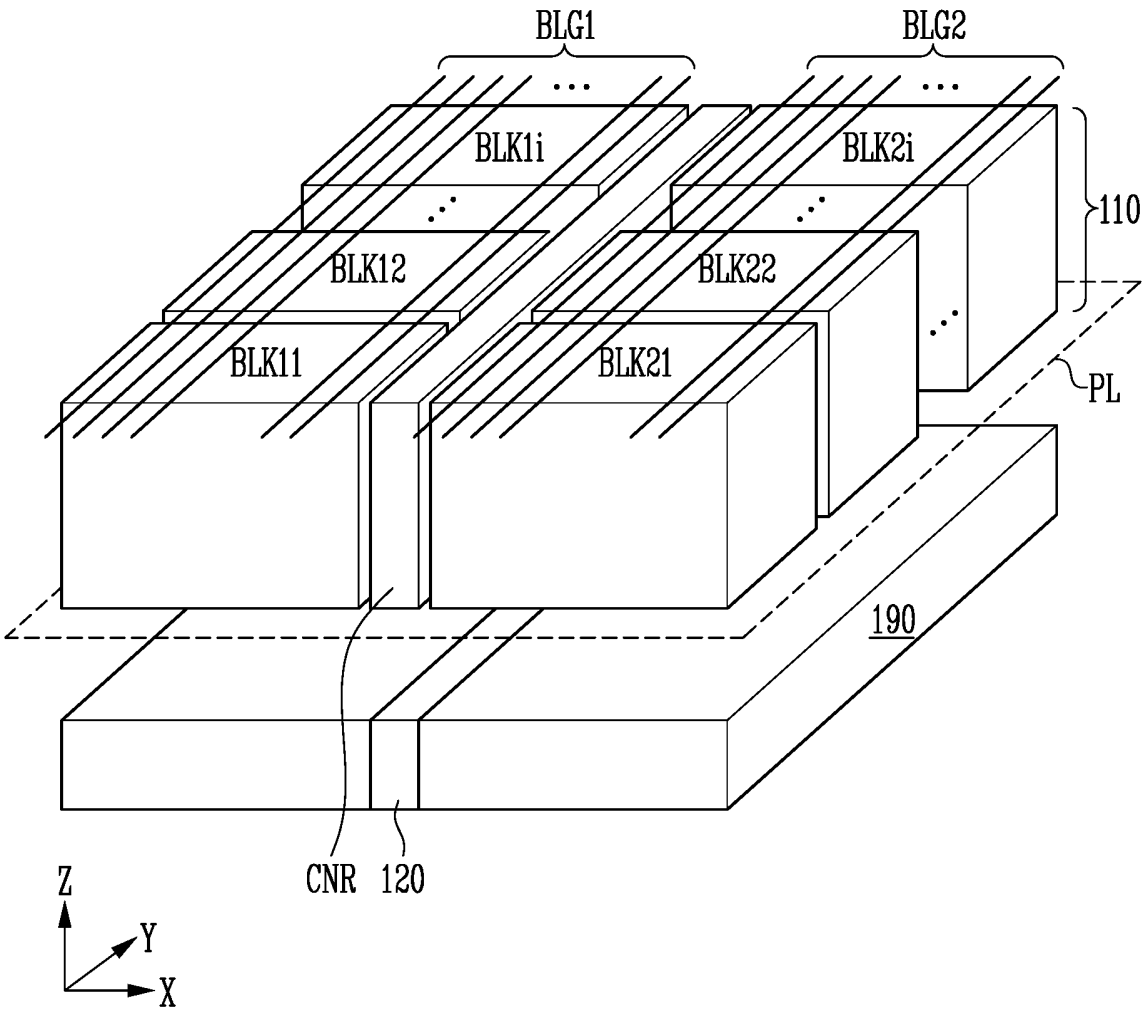

BLG1
BLG2
BLK1i
BLK2i
BLK12
BLK22
BLK11
BLK21
110
PL
190
CNR
120
Z
Y
X

FIG. 3

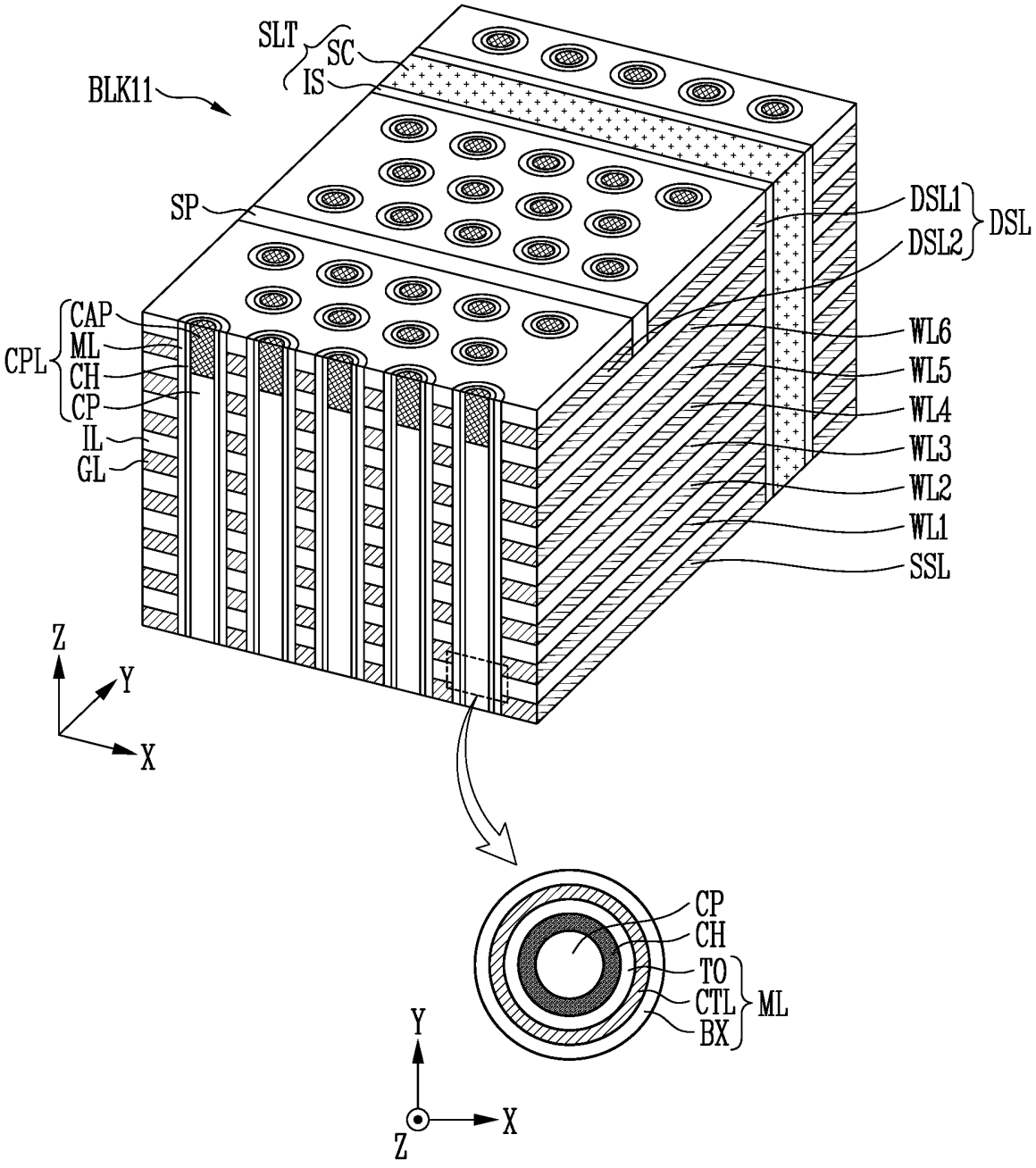

SLT
SC
IS
BLK11
SP
DSL1
DSL2
DSL
CAP
ML
CH
CP
CPL
IL
GL
WL6
WL5
WL4
WL3
WL2
WL1
SSL
Z
Y
X
CP
CH
TO
CTL
BX
ML
Y
X
Z

FIG. 4

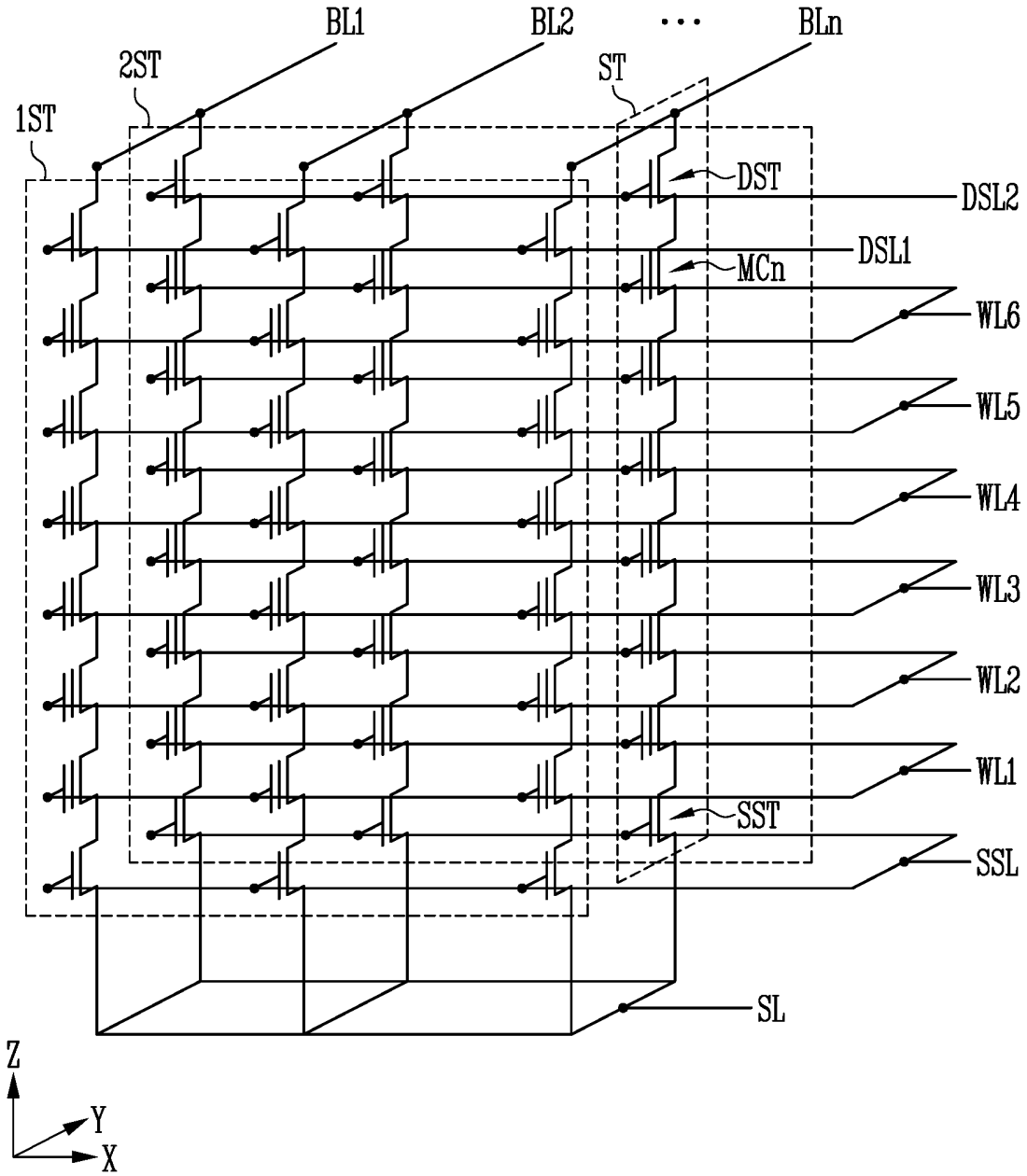

BL1
BL2
BLn
2ST
ST
1ST
DST
DSL2
DSL1
MCn
WL6
WL5
WL4
WL3
WL2
WL1
SST
SSL
SL
Z
Y
X

FIG. 5

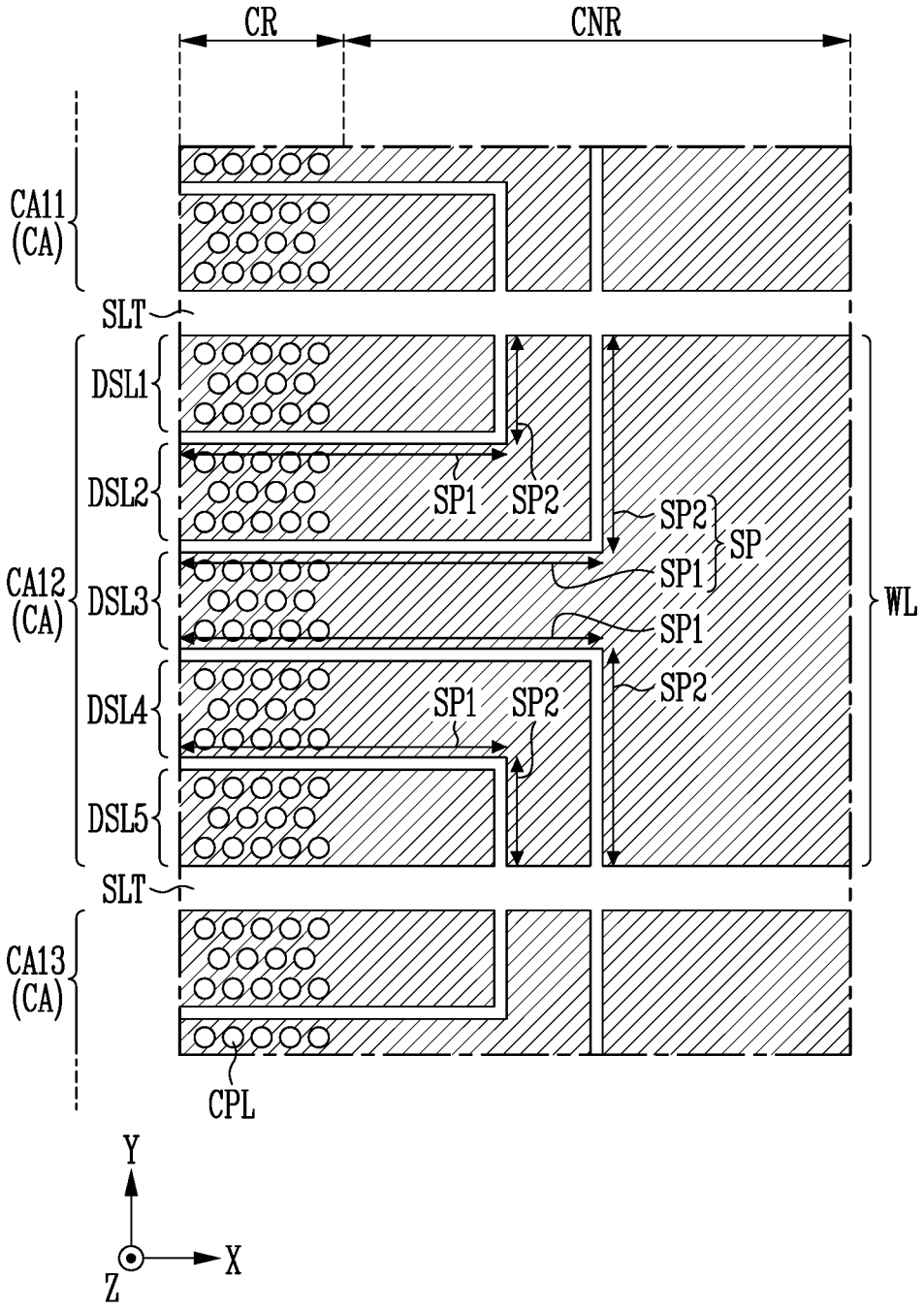

CR
CNR
CA11
(CA)
SLT
DSL1
DSL2
DSL3
DSL4
DSL5
CA12
(CA)
SLT
CA13
(CA)
CPL
SP1
SP2
SP2
SP
SP1
SP1
SP1
SP2
SP2
WL
Y
X
Z

FIG. 6

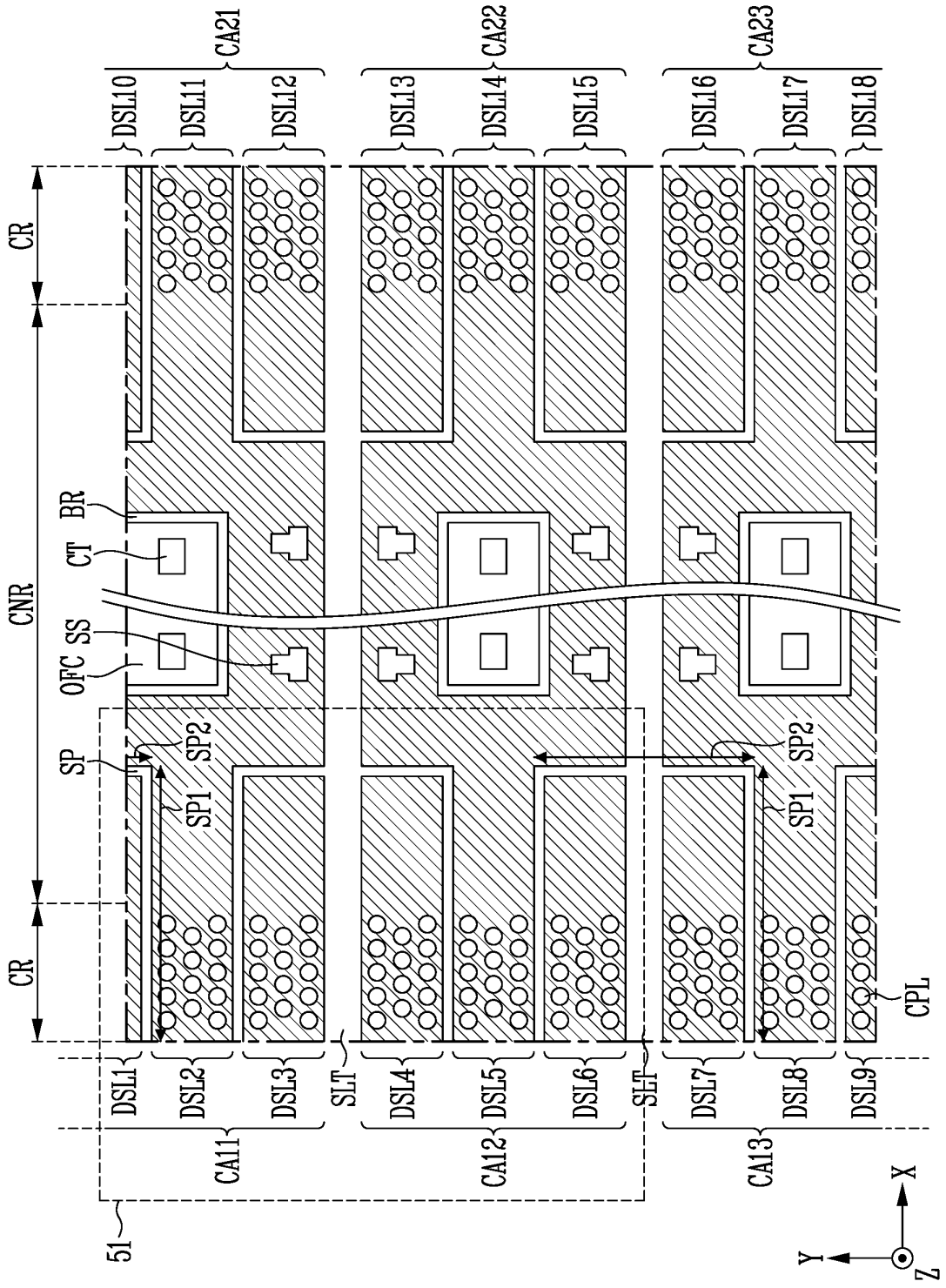

CA21
CA22
CA23
DSL10
DSL11
DSL12
DSL13
DSL14
DSL15
DSL16
DSL17
DSL18
CR
CNR
CR
BR
CT
SS
OFC
SP
SP2
SP1
SP2
SP1
CPL
DSL1
DSL2
DSL3
SLT
DSL4
DSL5
DSL6
SLT
DSL7
DSL8
DSL9
CA11
CA12
CA13
51
X
Y
Z

FIG. 7

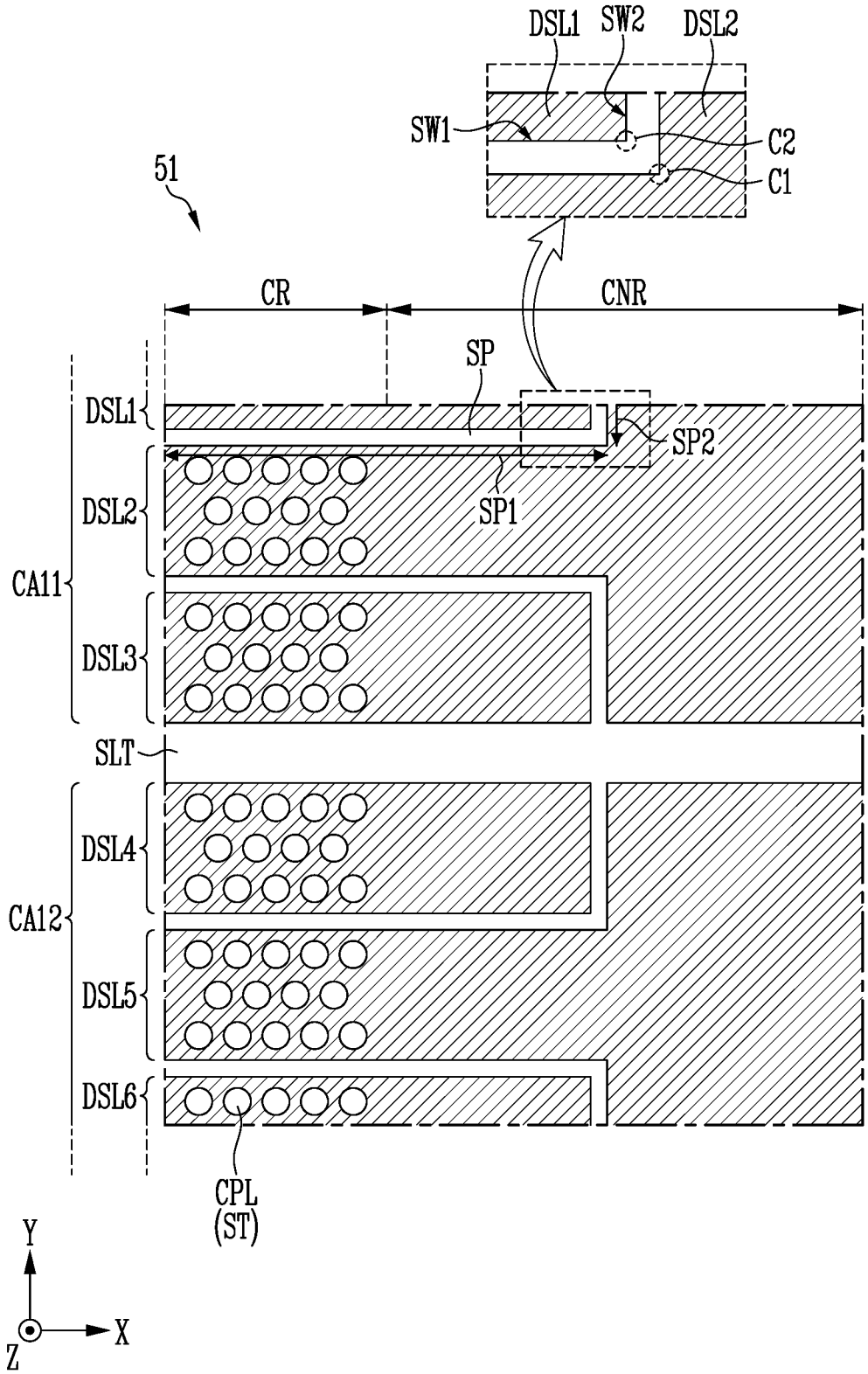

DSL1
SW2
DSL2
SW1
C2
C1
51
CR
CNR
SP
DSL1
SP2
DSL2
SP1
CA11
DSL3
SLT
DSL4
CA12
DSL5
DSL6
CPL
(ST)
Y
X
Z

FIG. 8A

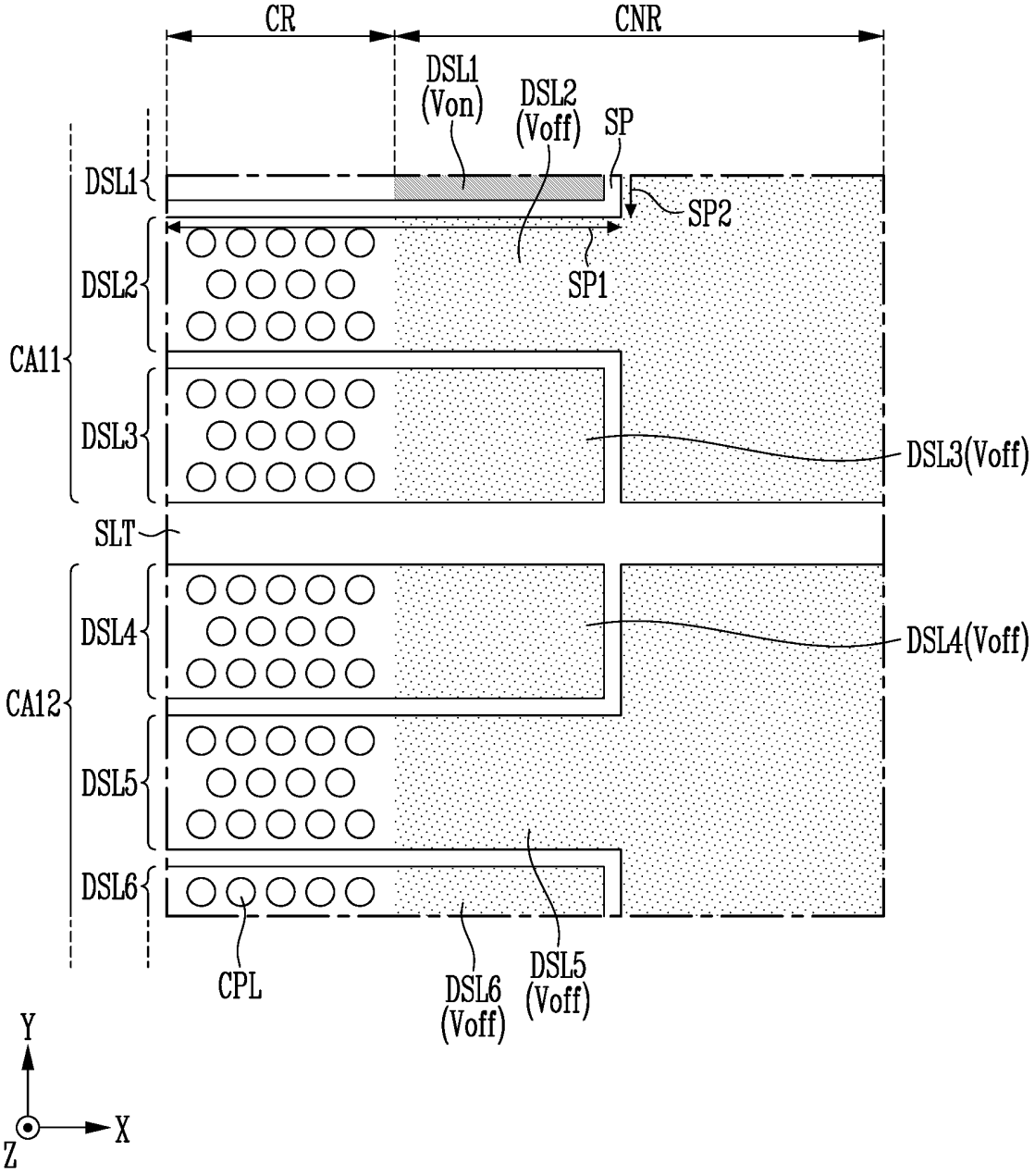

CR
CNR
DSL1 (Von)
DSL2 (Voff)
SP
SP2
SP1
DSL1
DSL2
CA11
DSL3
DSL3(Voff)
SLT
DSL4
DSL4(Voff)
CA12
DSL5
DSL6
CPL
DSL6 (Voff)
DSL5 (Voff)
Y
X
Z

FIG. 8B

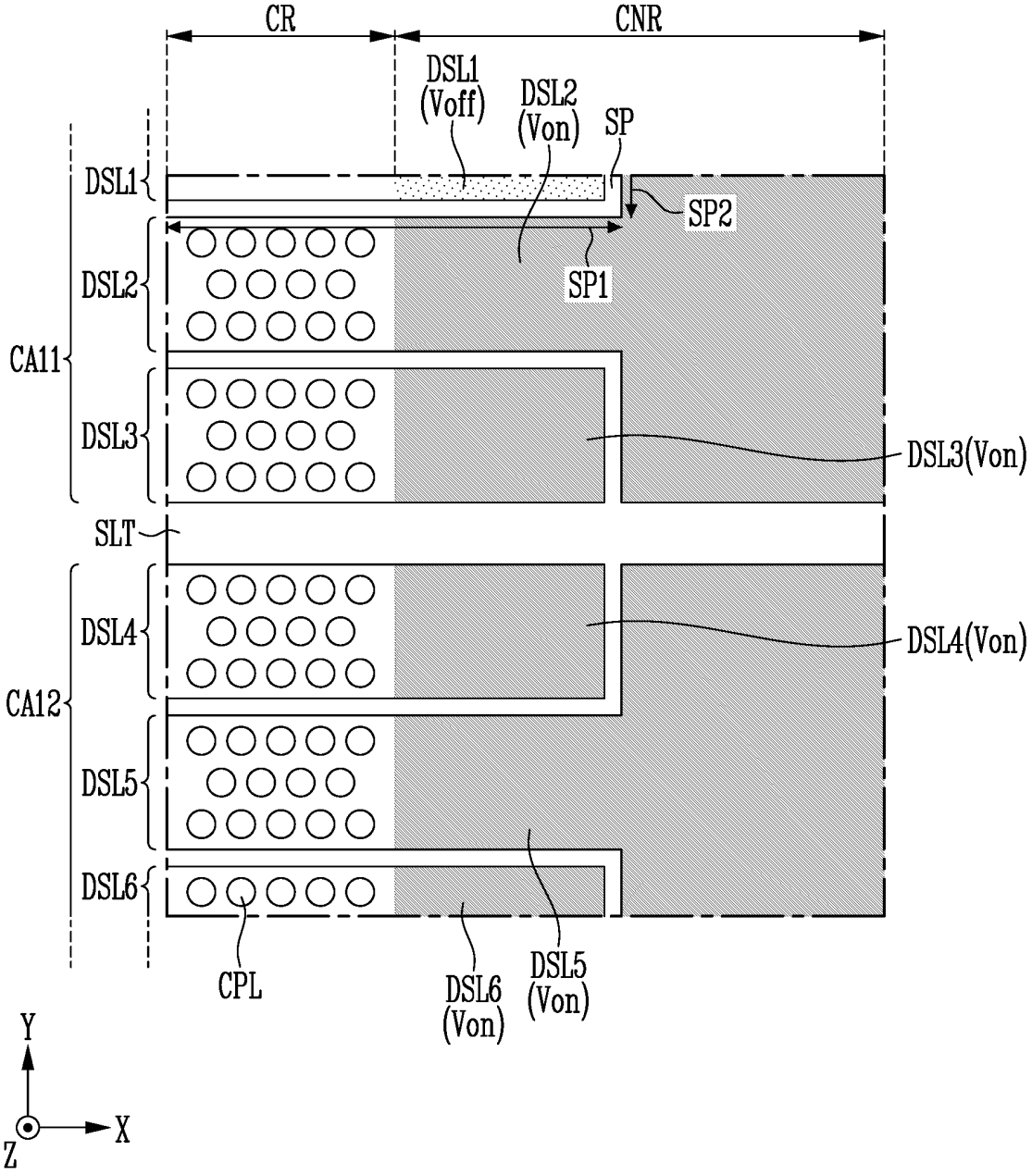

CR
CNR
DSL1 (Voff)
DSL2 (Von)
SP
SP2
SP1
DSL1
DSL2
CA11
DSL3
DSL3(Von)
SLT
DSL4
DSL4(Von)
CA12
DSL5
DSL6
CPL
DSL6 (Von)
DSL5 (Von)
Y
X
Z

FIG. 9

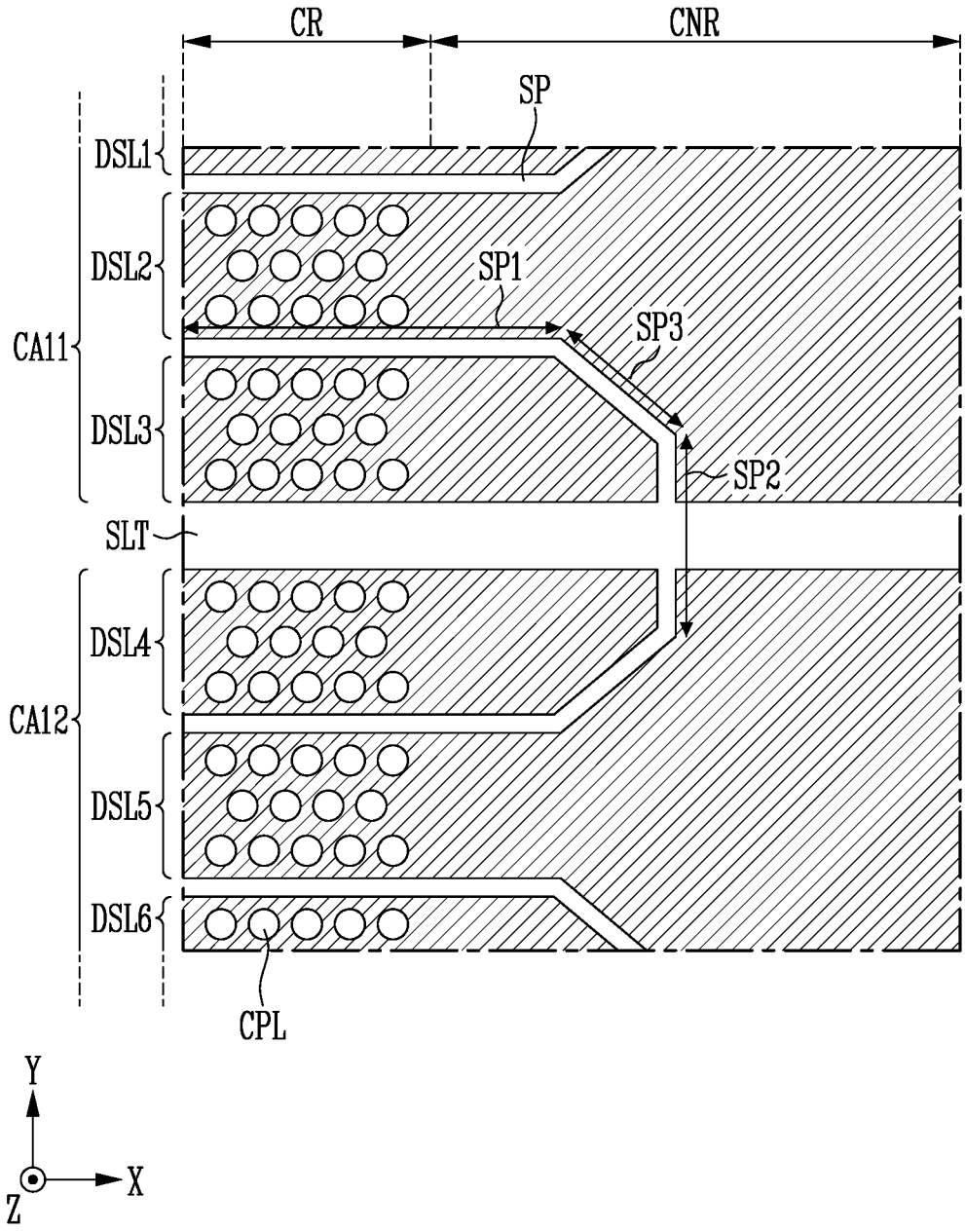

CR
CNR
SP
DSL1
DSL2
SP1
SP3
CA11
DSL3
SP2
SLT
DSL4
CA12
DSL5
DSL6
CPL
Y
X
Z

FIG. 10

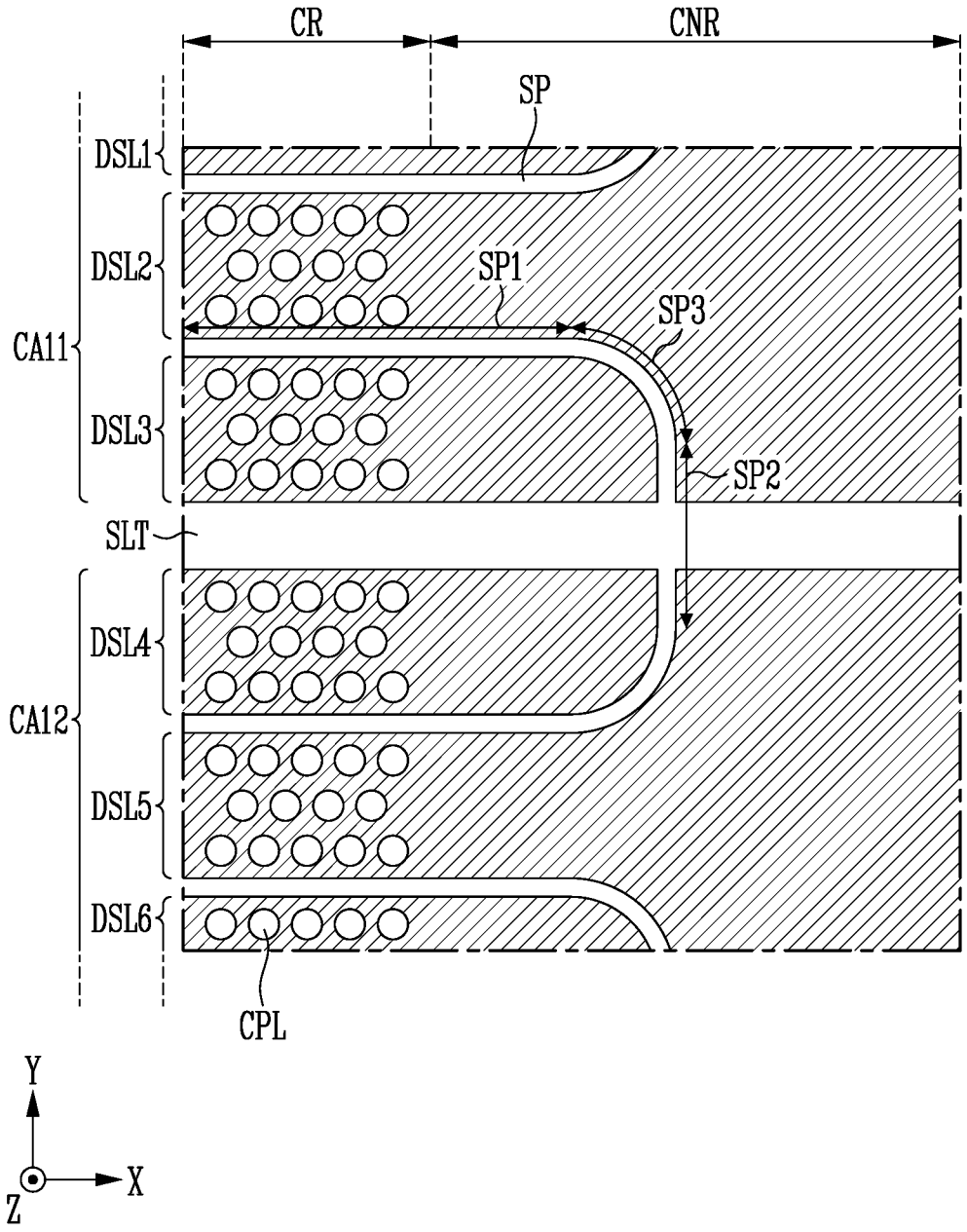

CR
CNR
SP
DSL1
DSL2
SP1
SP3
CA11
DSL3
SP2
SLT
DSL4
CA12
DSL5
DSL6
CPL
Y
X
Z

FIG. 11

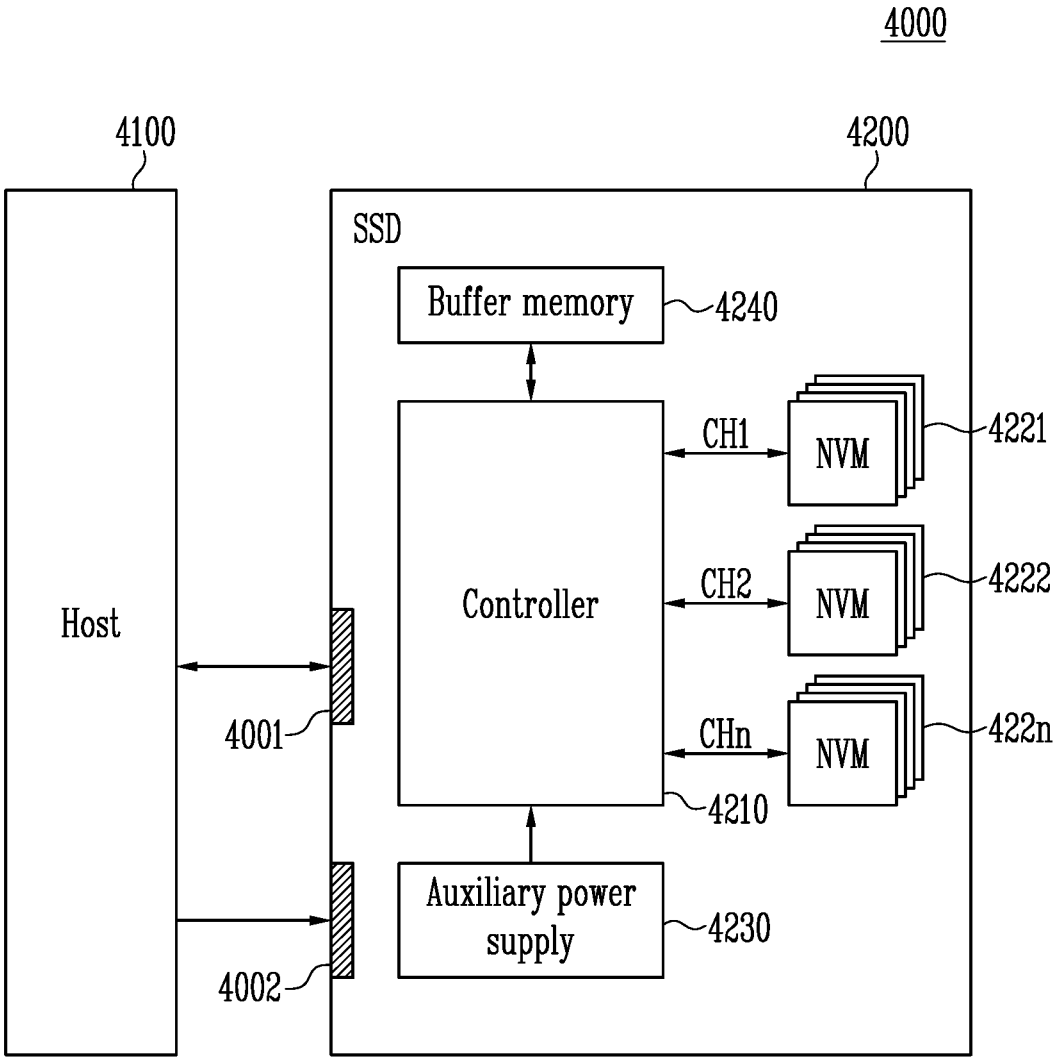
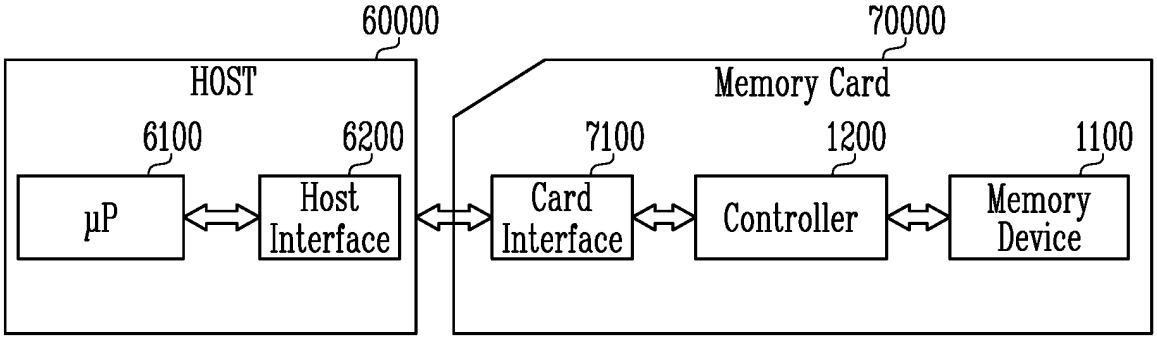

4000
4100
4200
SSD
Buffer memory
4240
Controller
CH1
CH2
CHn
NVM
NVM
NVM
4221
4222
422n
4210
Host
4001
4002
Auxiliary power supply
4230

FIG. 12


60000
70000
HOST
Memory Card
6100
6200
7100
1200
1100
µP
Host Interface
Card Interface
Controller
Memory Device