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- (54) Title: SYSTEM FOR AUTO-PARALLELIZATION OF PROCESSING CODES FOR MULTI-PROCESSOR SYSTEMS WITH
OPTIMIZED LATENCY, AND METHOD THEREOF

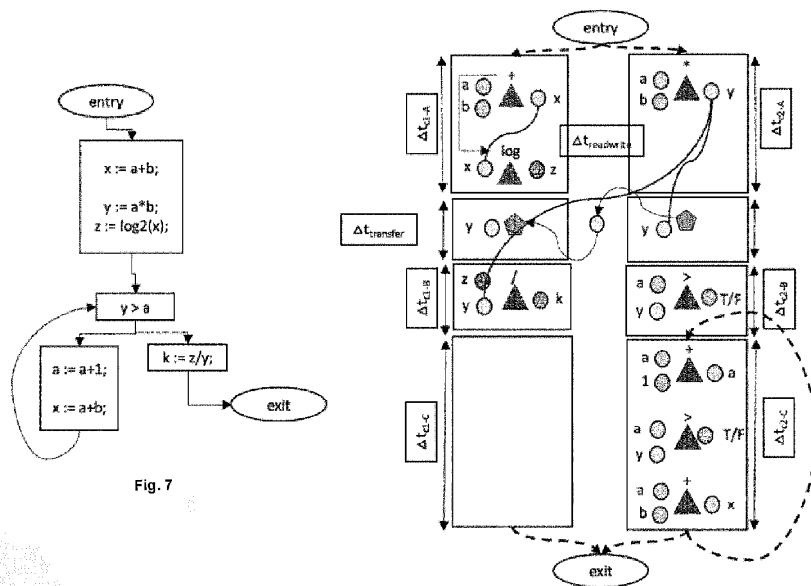


Fig. 7

- (57) Abstract: Compiler system (1) for optimized compiling of a computer program (31) for execution by a parallel processing system (2) having a plurality of processing units (21) and corresponding method. The compiler system (1) comprises means for translating a source programming language of the computer program (31) into a machine code (32) as target programming language thereby generating an optimized processing code (32) comprising a number of instructions executable by the plurality of processing units (21) of the parallel processing system (2) or controlling the operation of the plurality of processing units (21).

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