

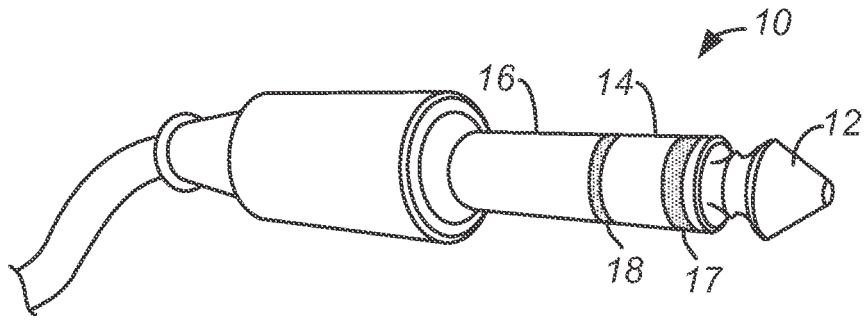


Fig. 1A

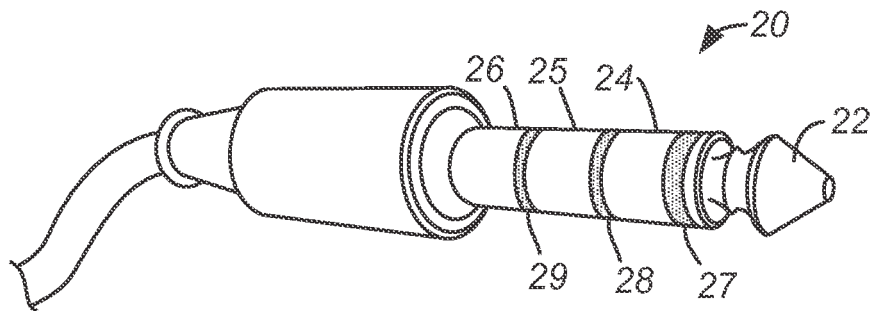


Fig. 1B

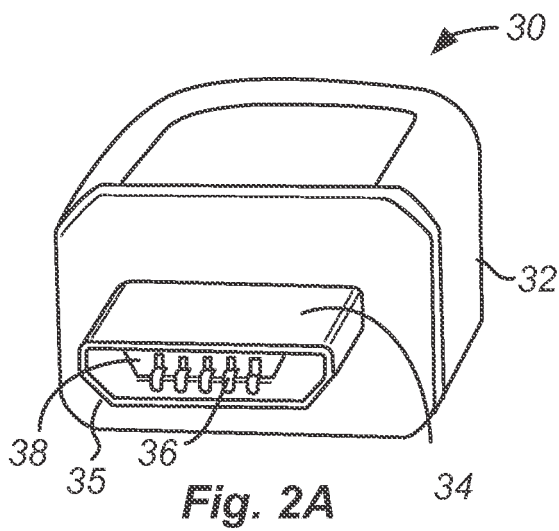


Fig. 2A

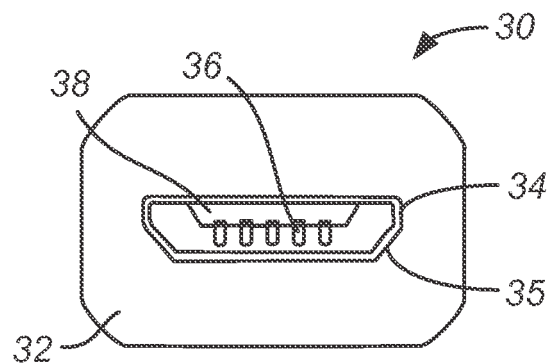
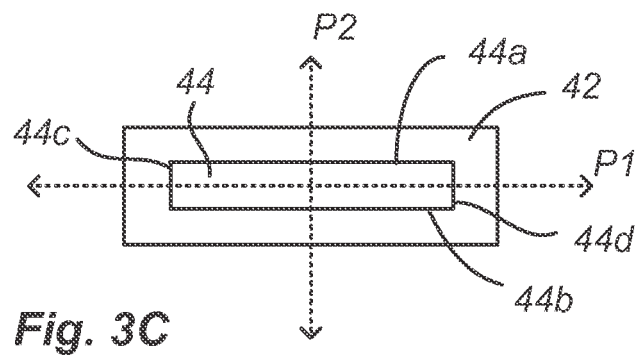
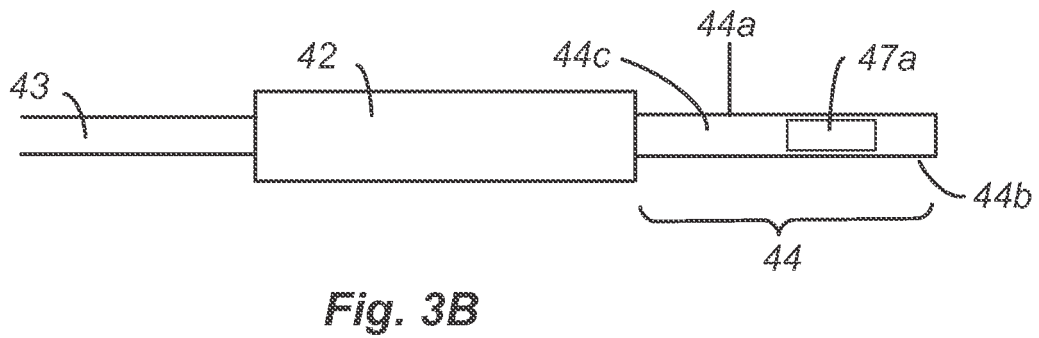
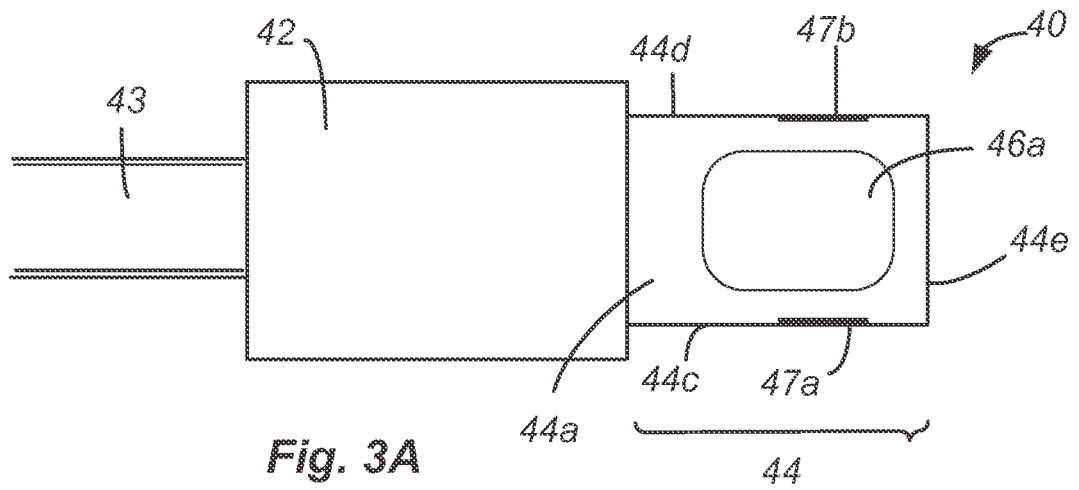


Fig. 2B



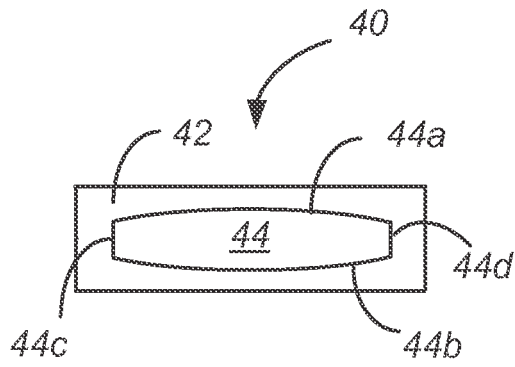


Fig. 4A

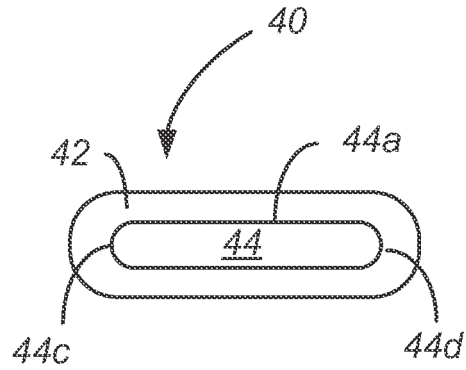


Fig. 4B

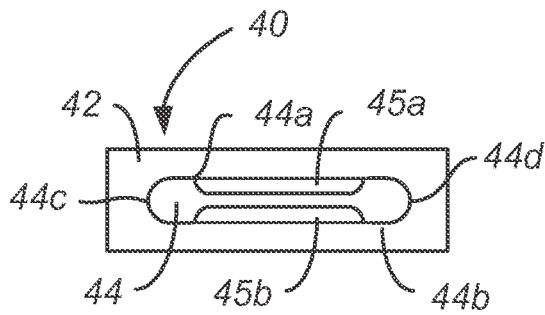


Fig. 4C

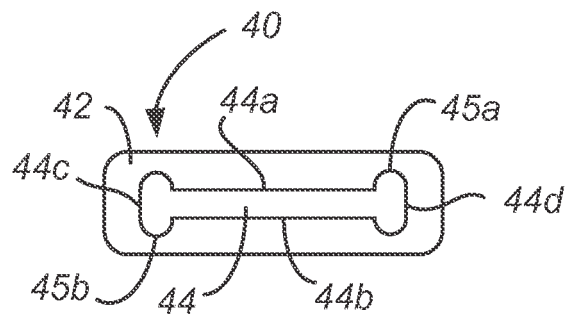


Fig. 4D

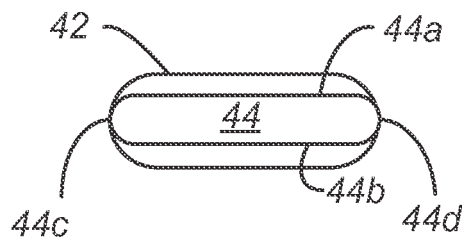
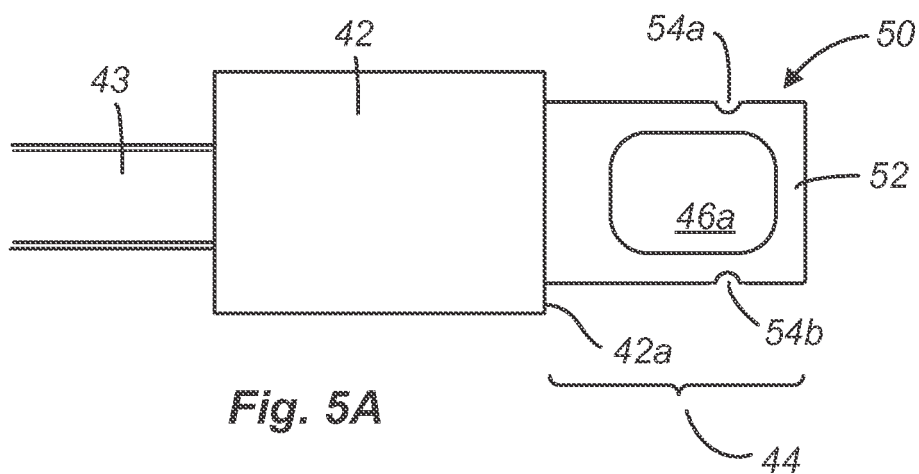
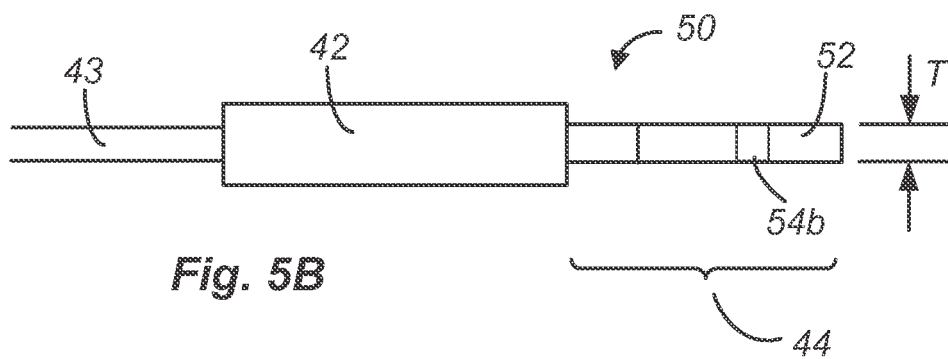
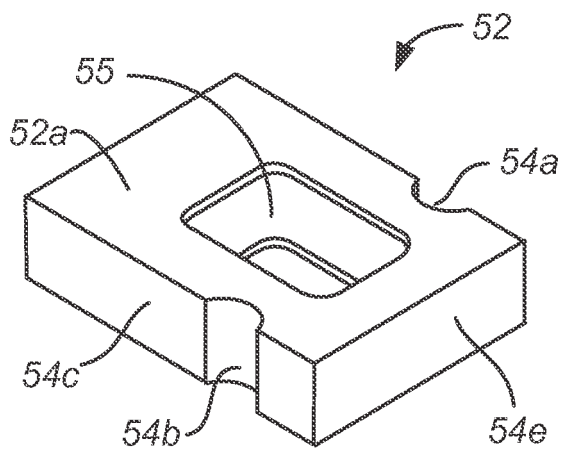
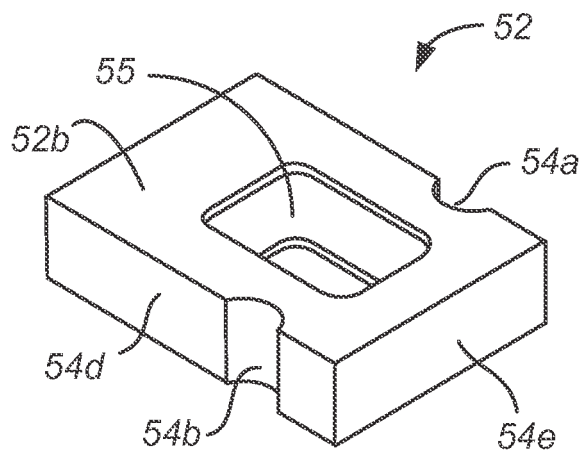
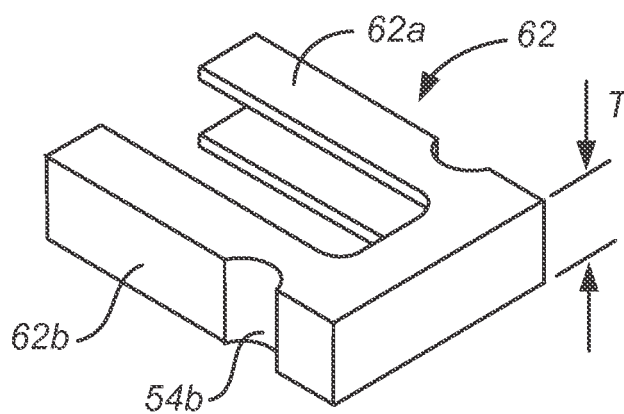
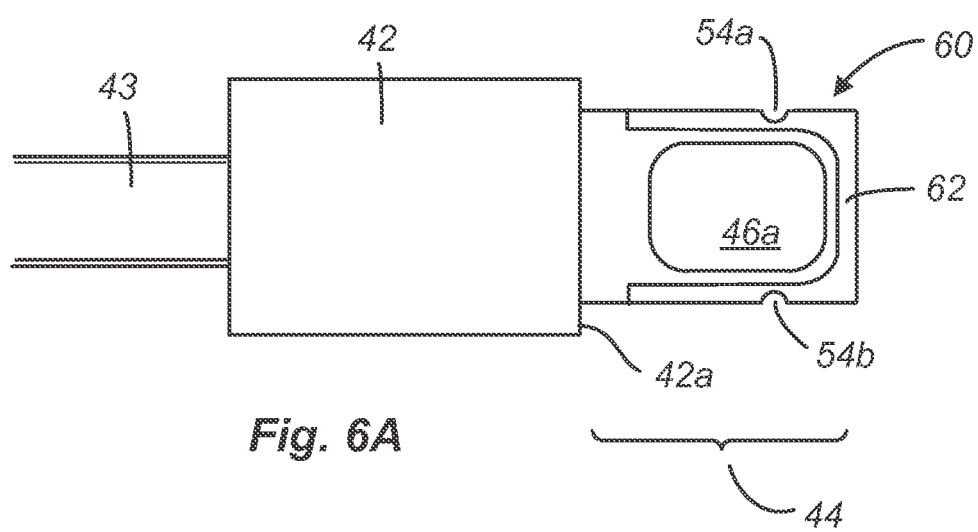


Fig. 4E

**Fig. 5A****Fig. 5B****Fig. 5C****Fig. 5D**



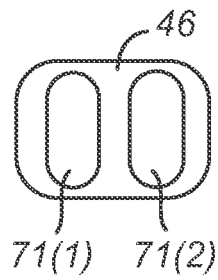


Fig. 7A

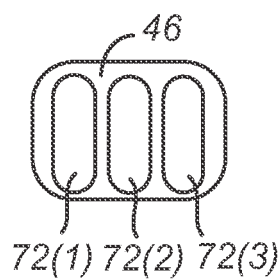


Fig. 7B

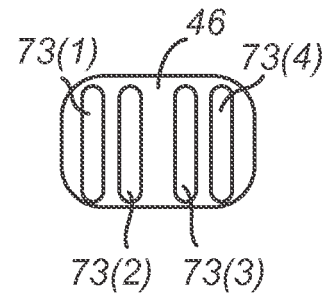


Fig. 7C

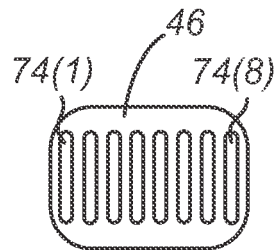


Fig. 7D

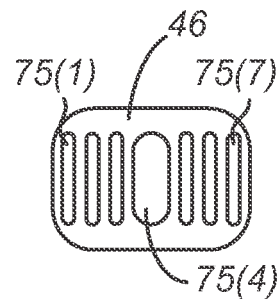


Fig. 7E

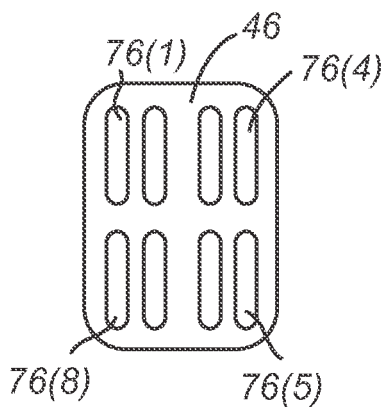


Fig. 7F

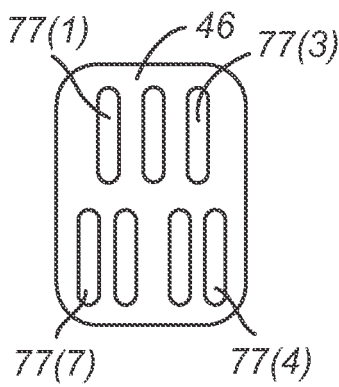


Fig. 7G

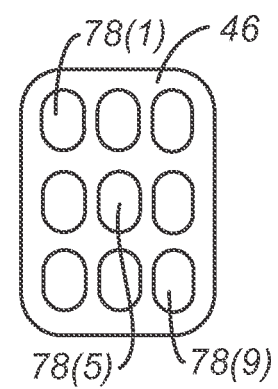
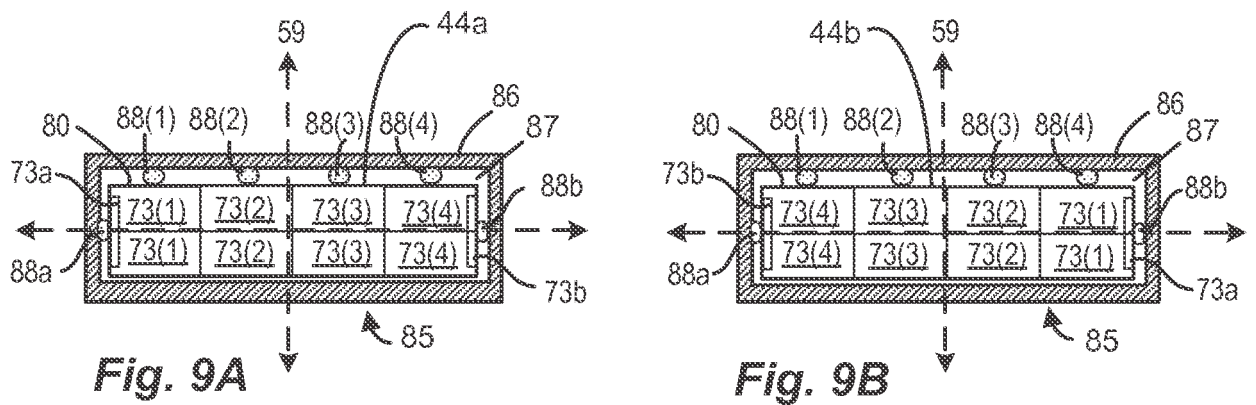
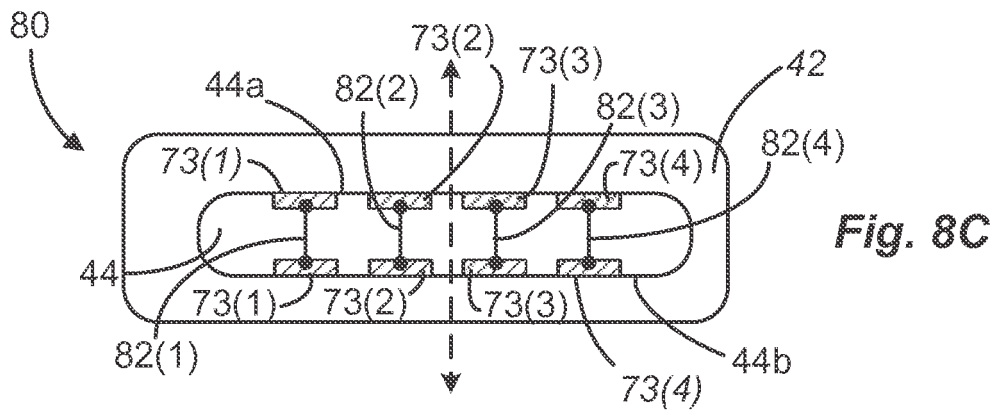
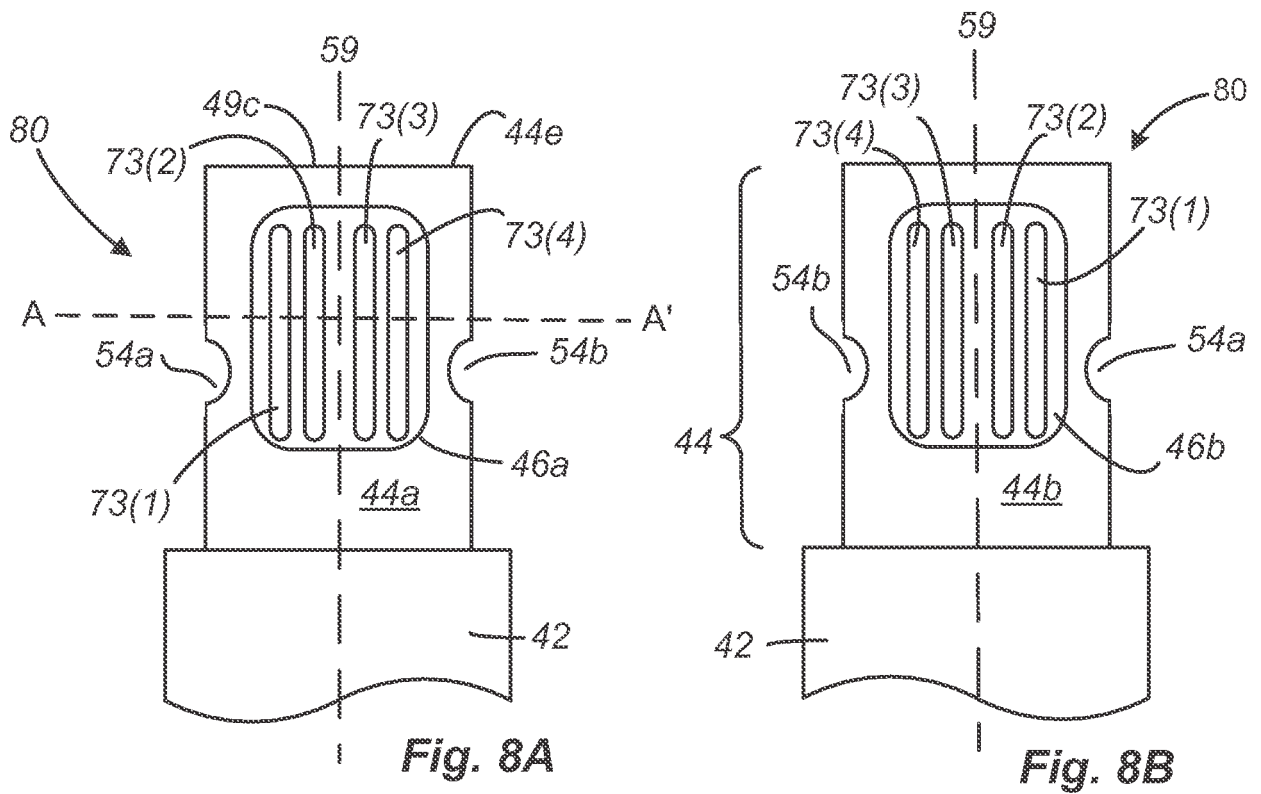
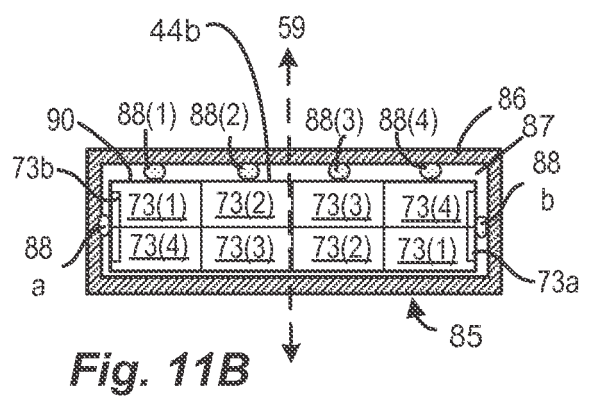
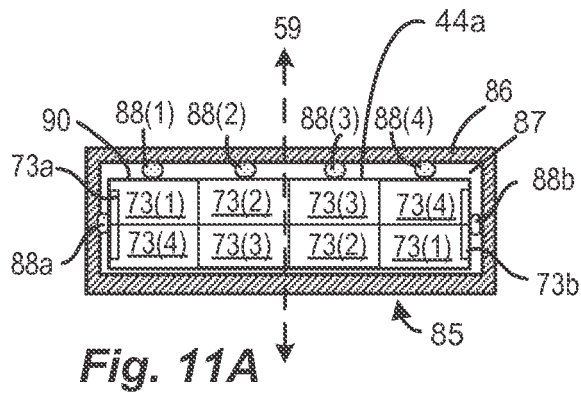
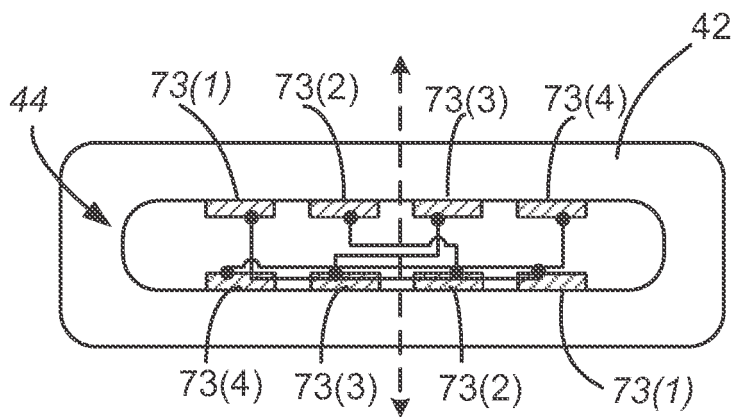
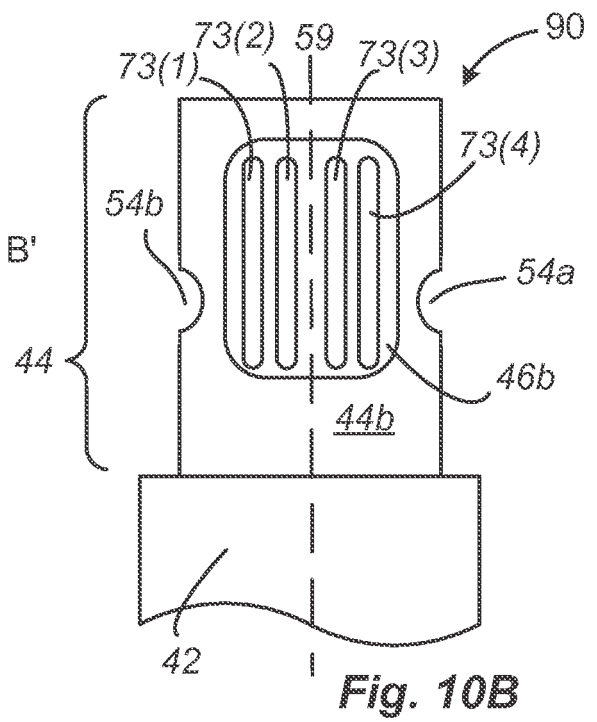
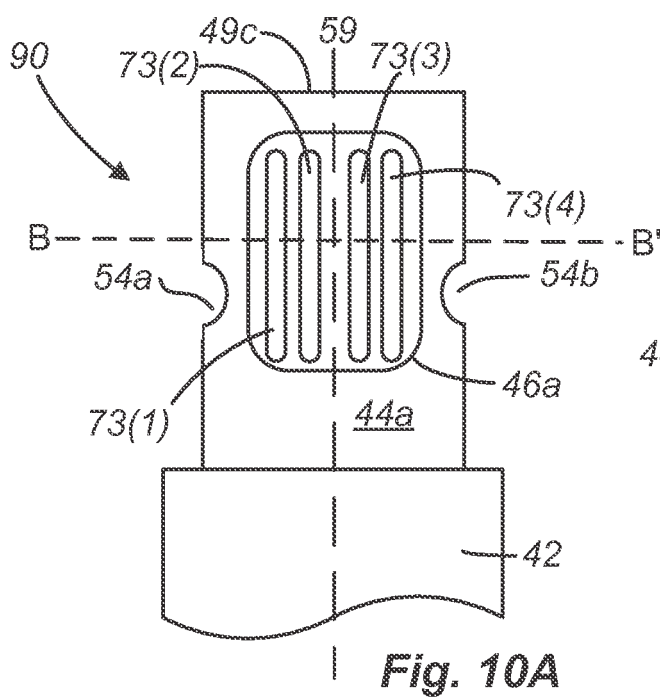


Fig. 7H





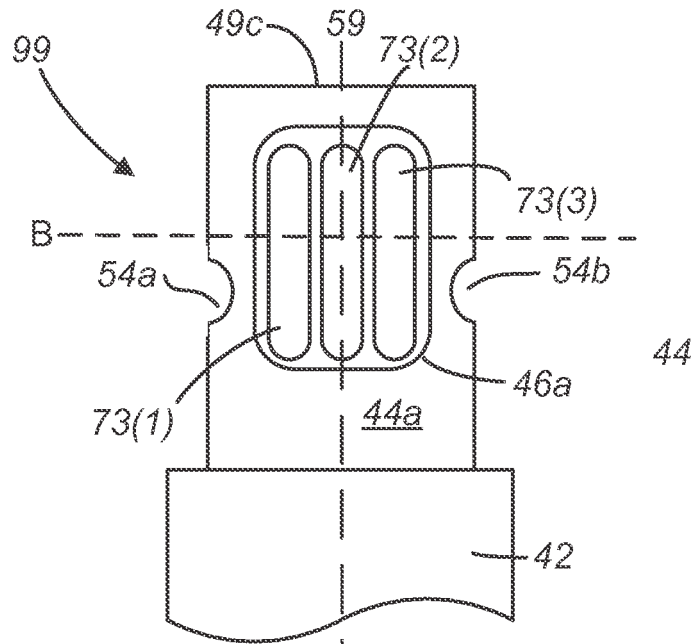


Fig. 12A

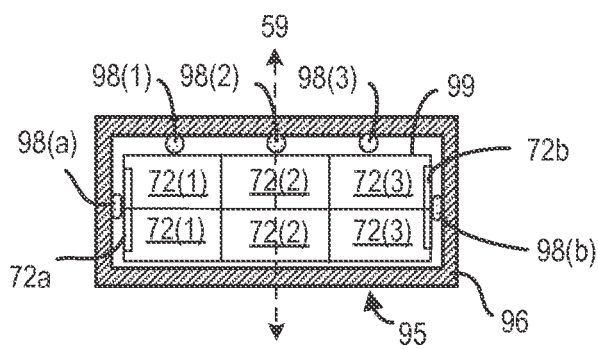


Fig. 12B

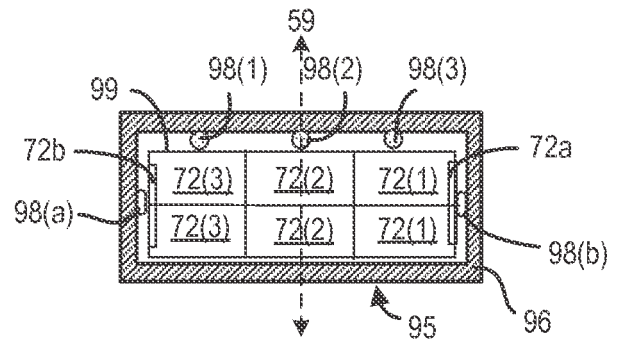
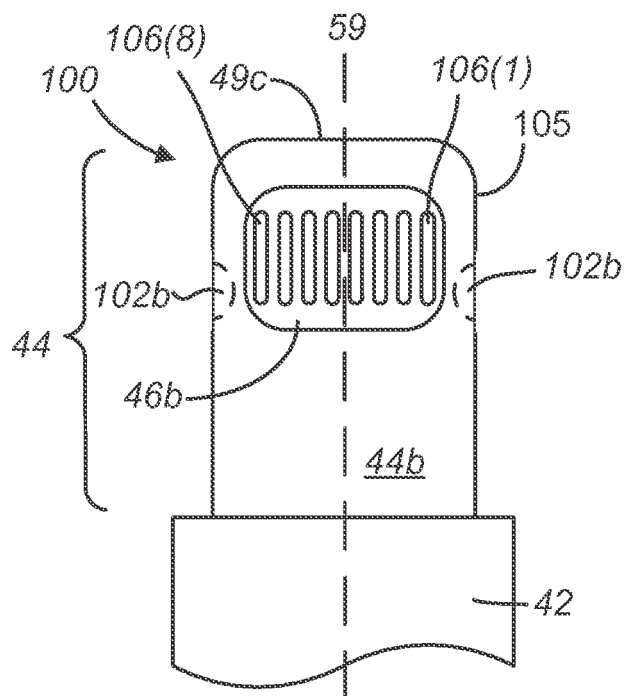
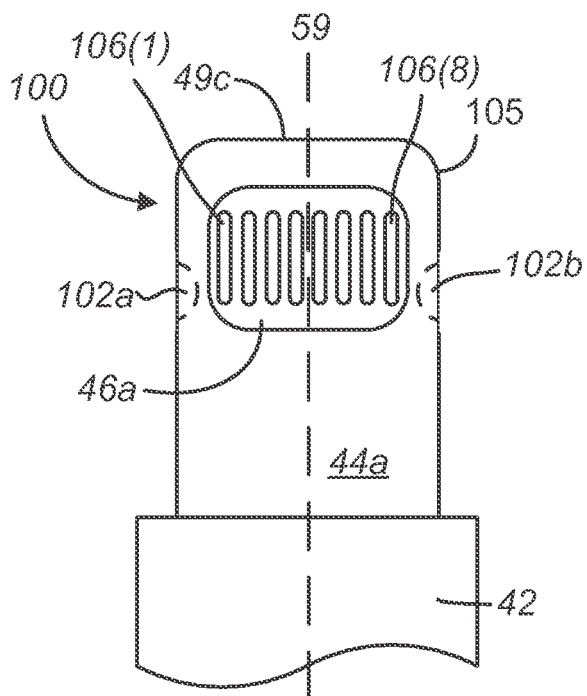
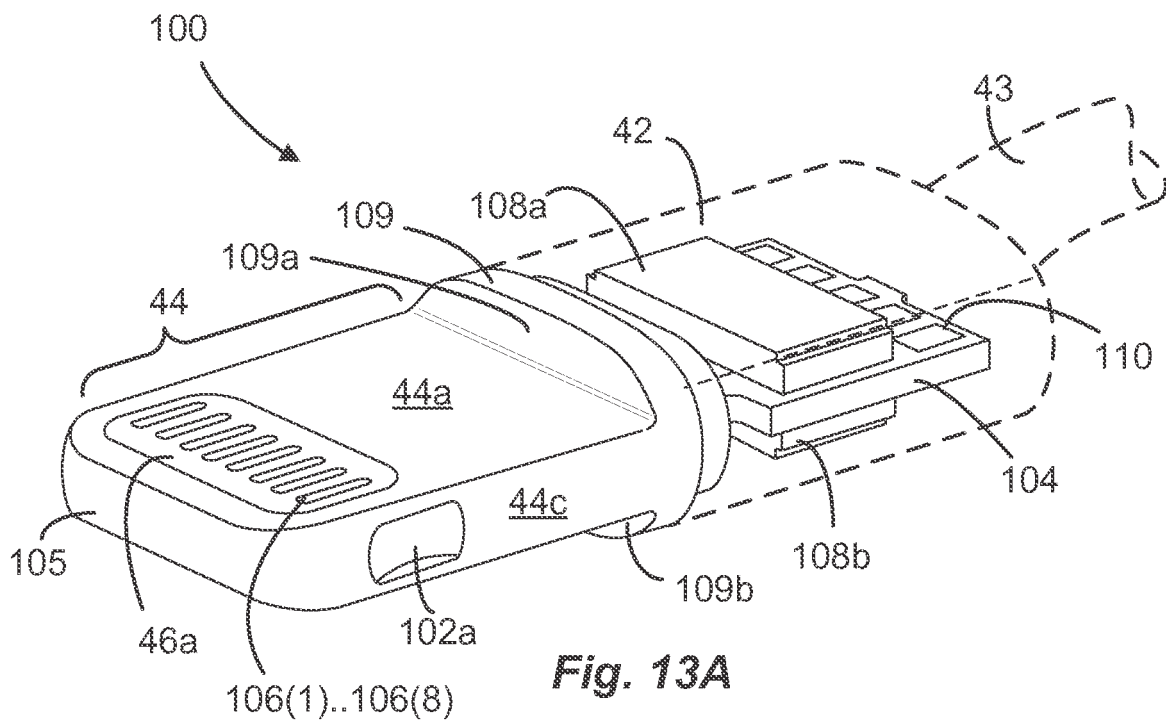
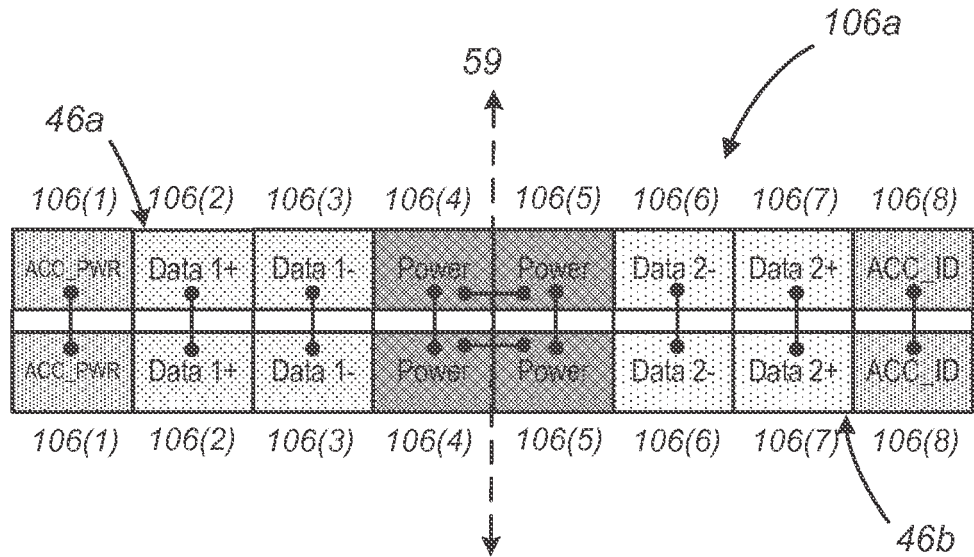
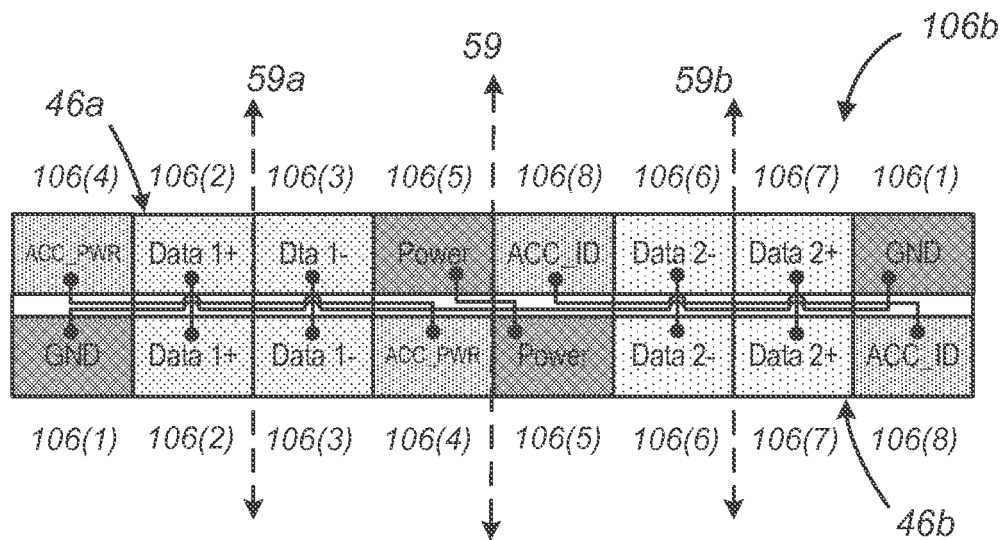
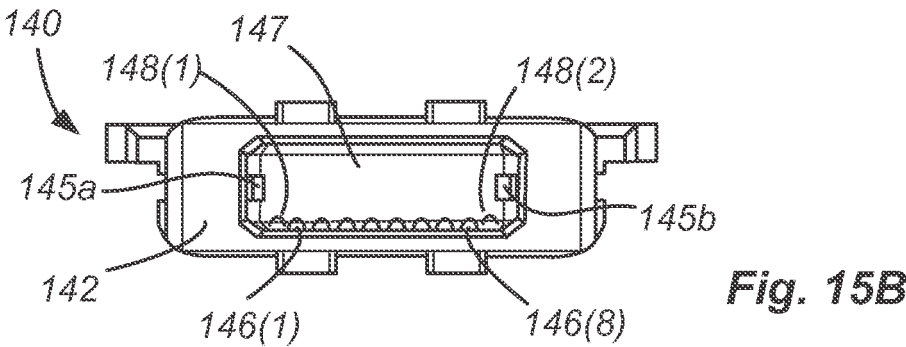
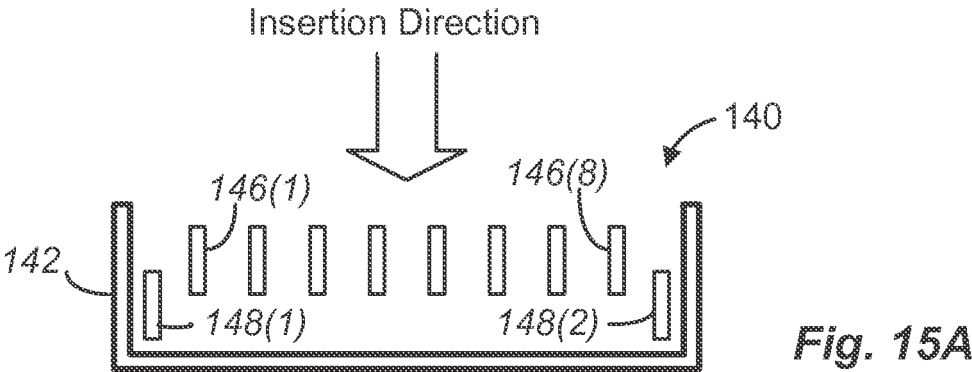


Fig. 12C



**Fig. 14A****Fig. 14B**



ACC1	Data A+	Data A-	P_IN	P_IN	Data B-	Data B+	ACC2
146(1)	146(2)	146(3)	146(4)	146(5)	146(6)	146(7)	146(8)

Fig. 15C

GND	Data A+	Data A-	ACC1	P_IN	Data B-	Data B+	ACC2
146(1)	146(2)	146(3)	146(4)	146(5)	146(6)	146(7)	146(8)

Fig. 15D

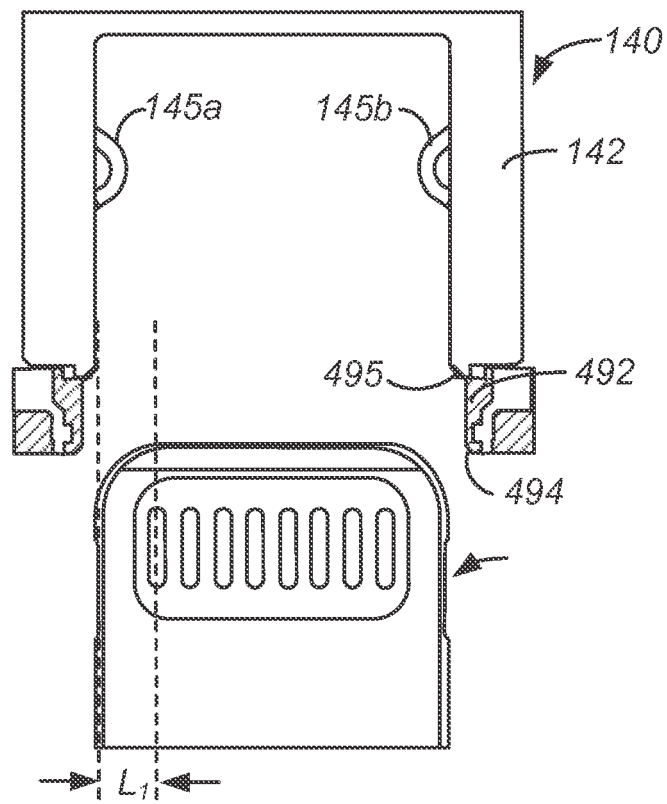


Fig. 16A

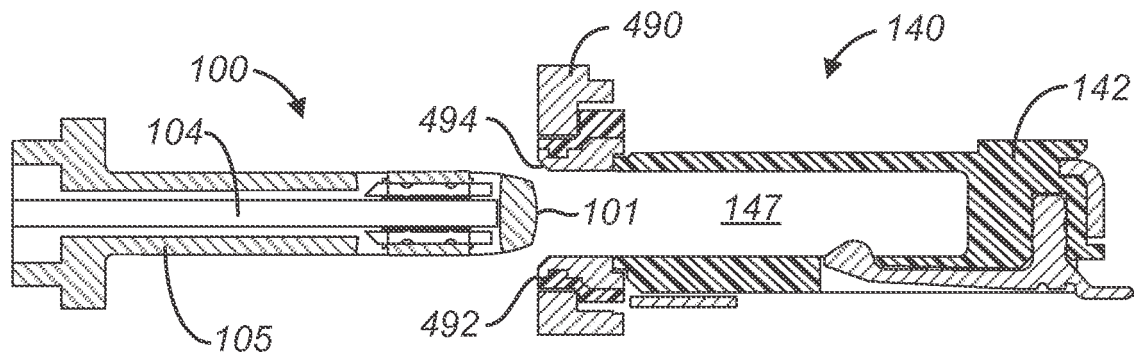


FIG. 16B

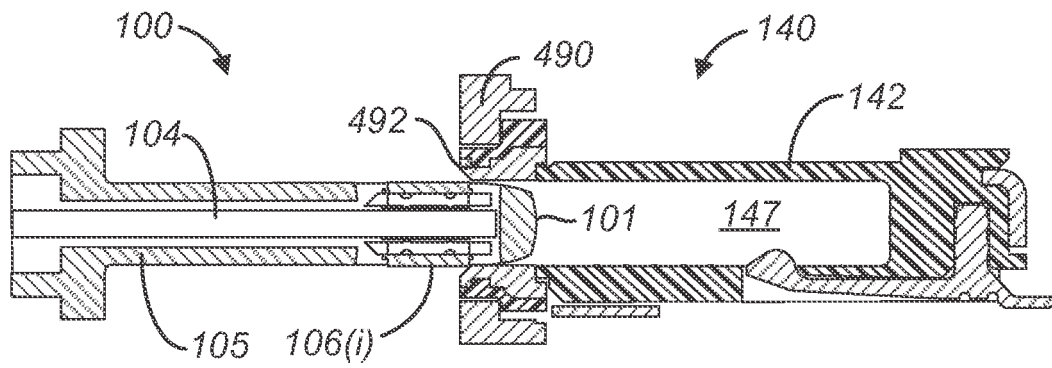


FIG. 16C

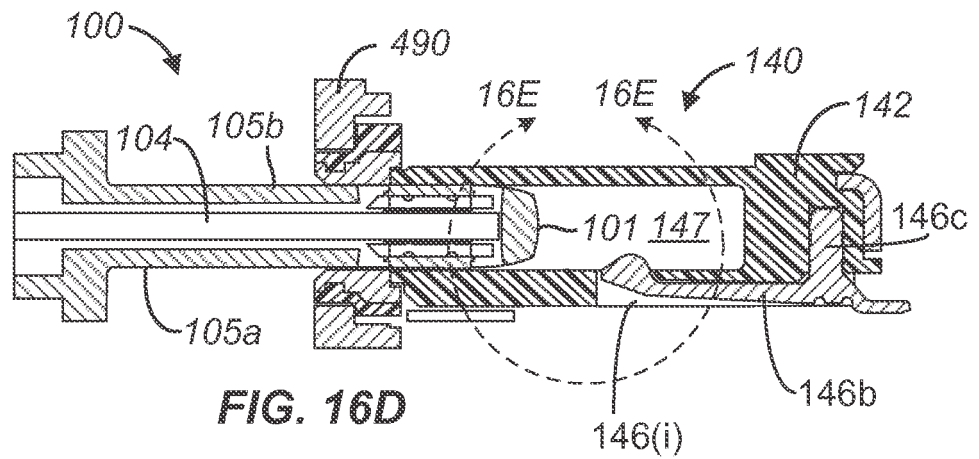


FIG. 16D

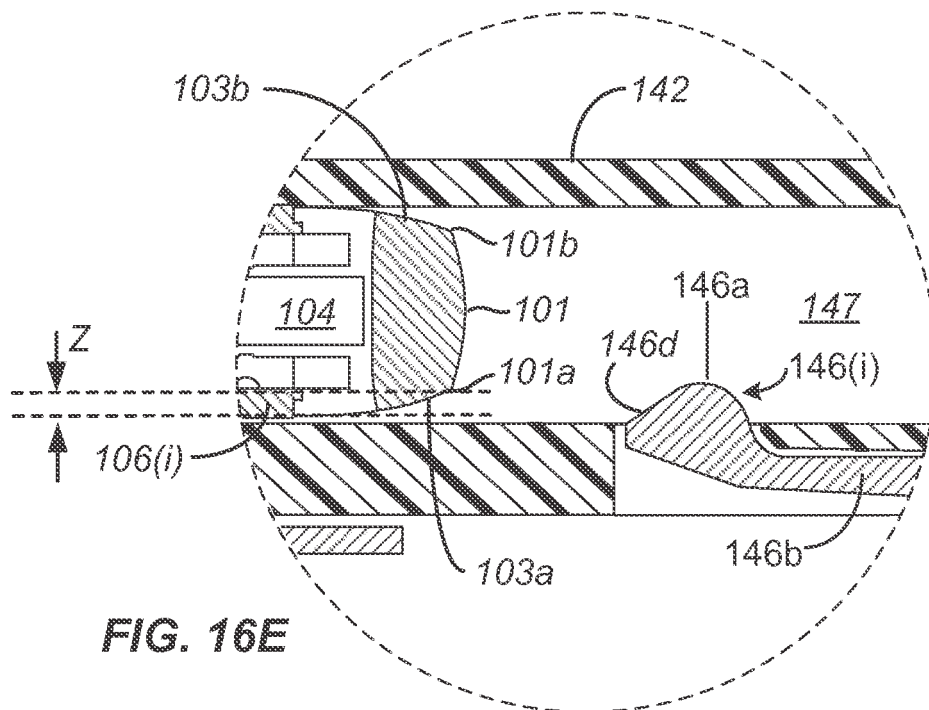


FIG. 16E

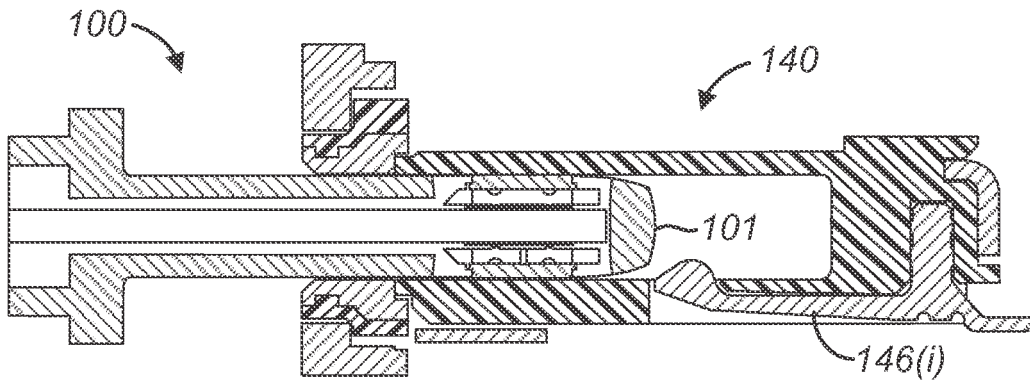


FIG. 16F

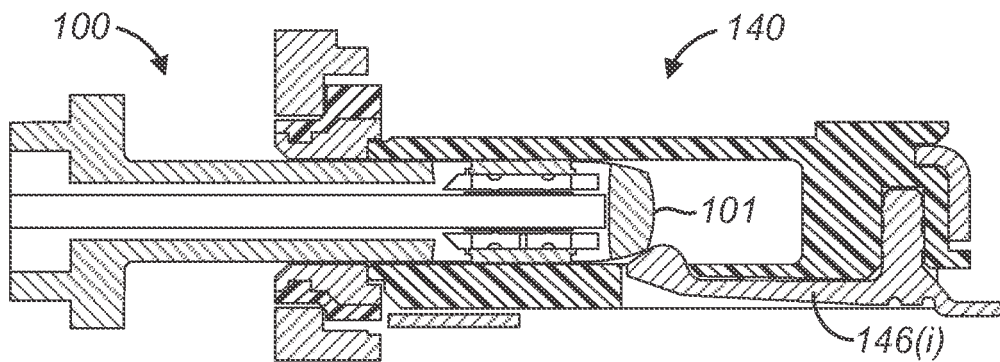


FIG. 16G

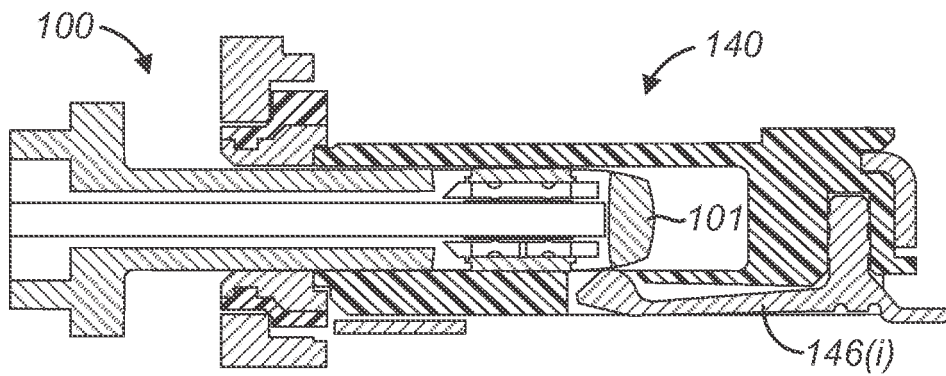


FIG. 16H

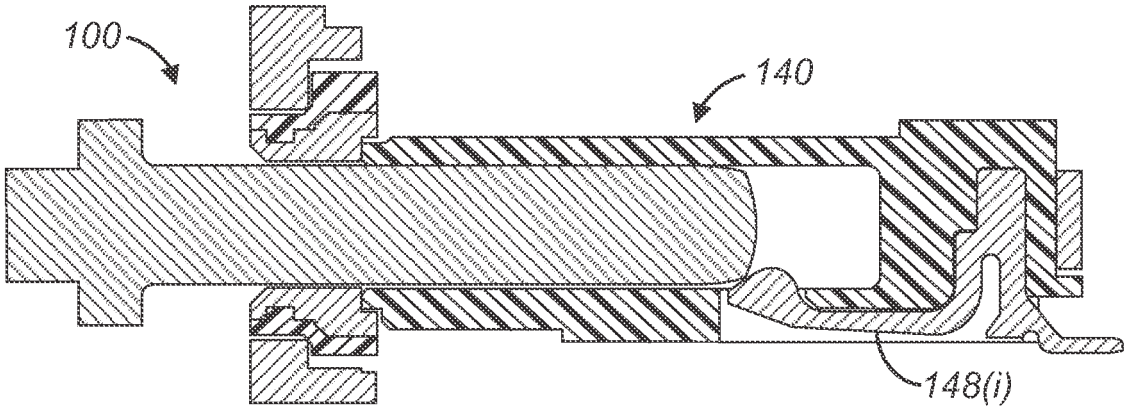


FIG. 16I

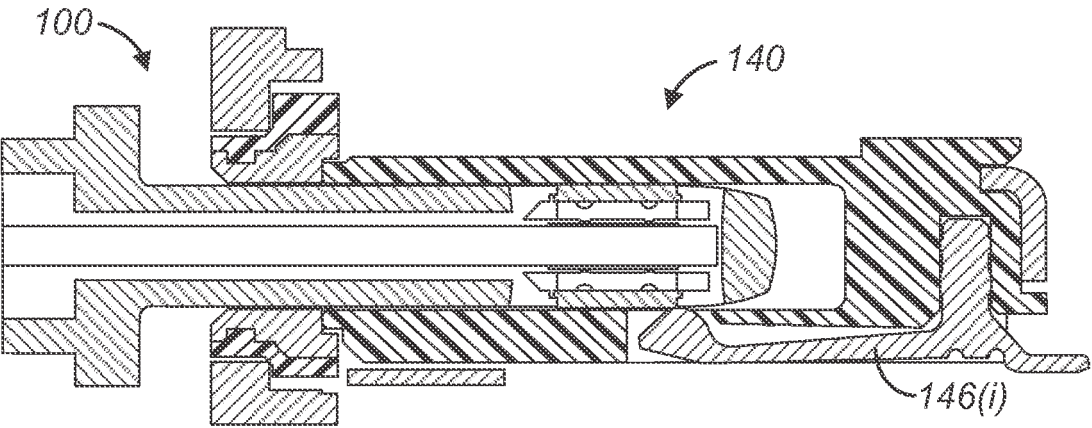


FIG. 16J

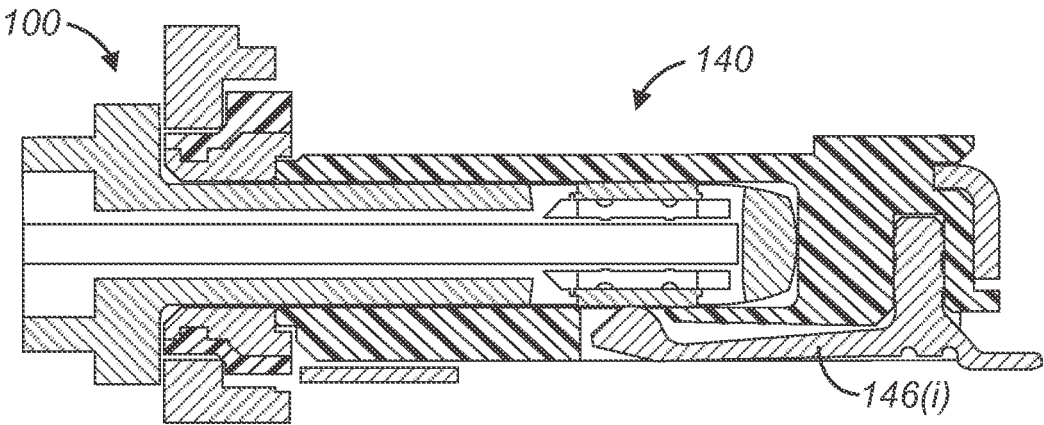
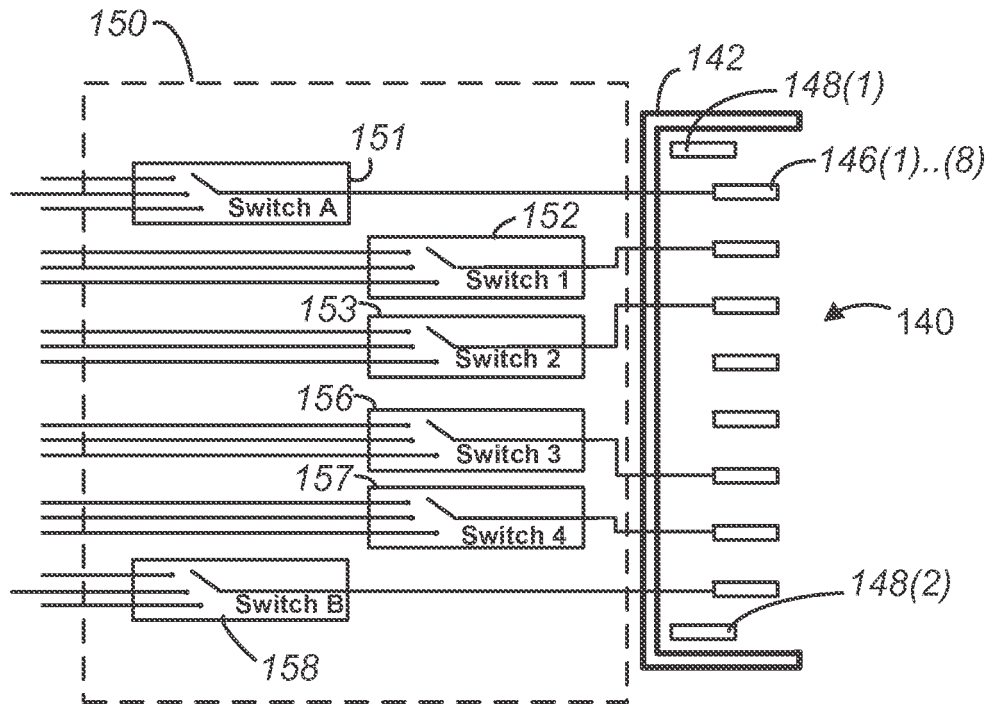
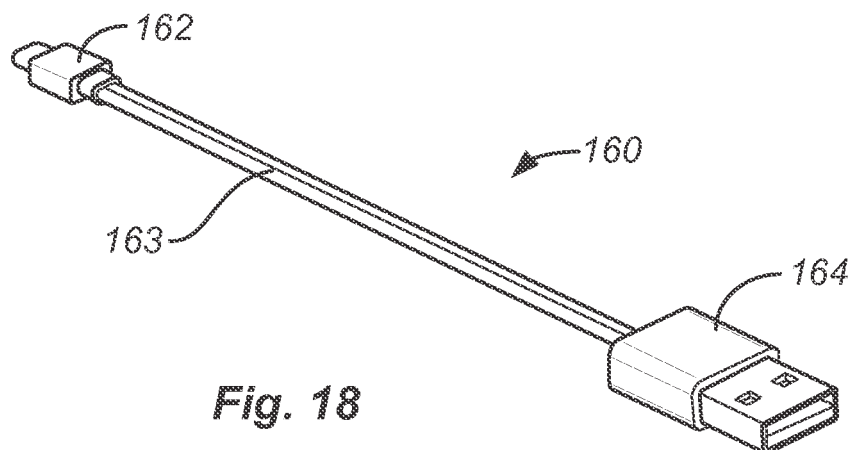


FIG. 16K

**Fig. 17****Fig. 18**

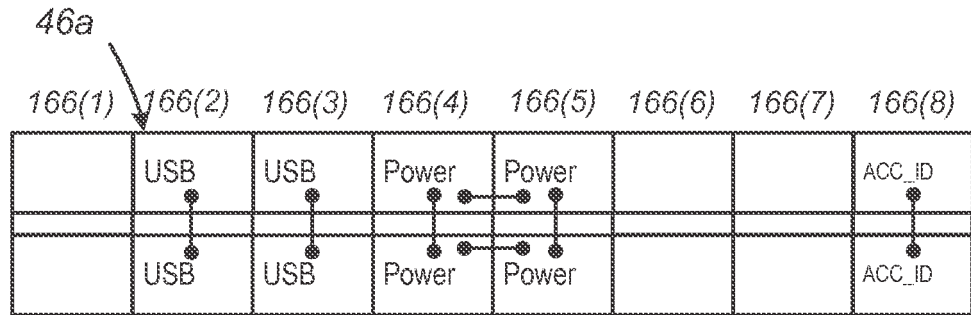


Fig. 19A

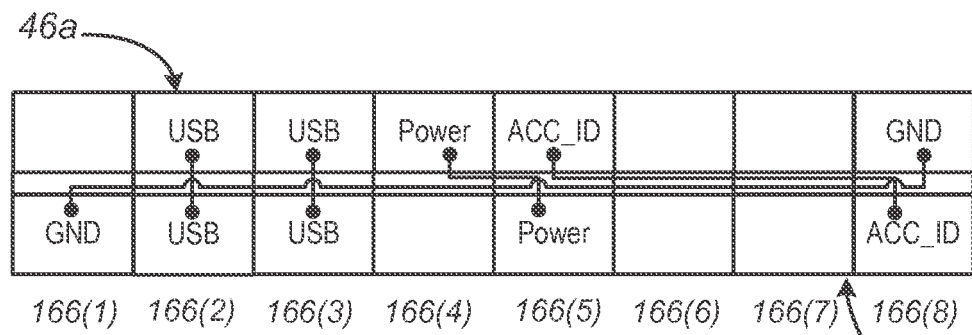


Fig. 19B

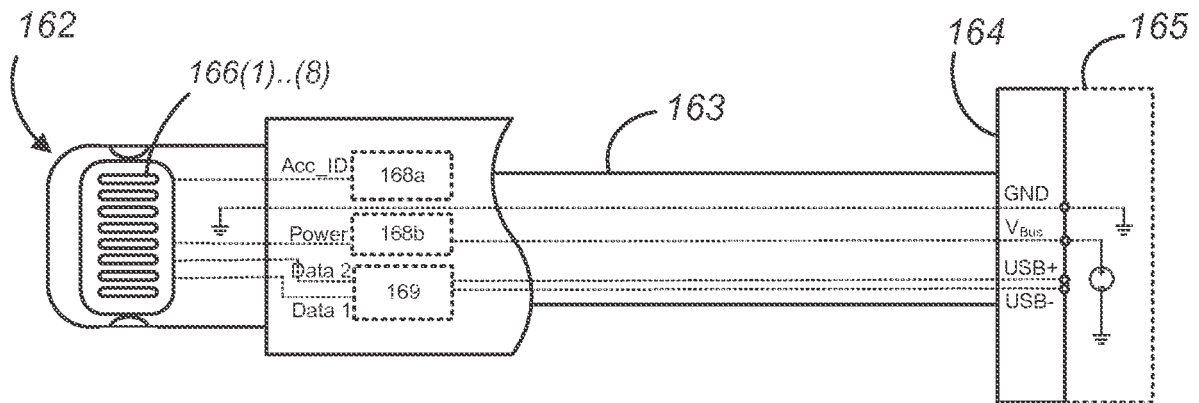


Fig. 20

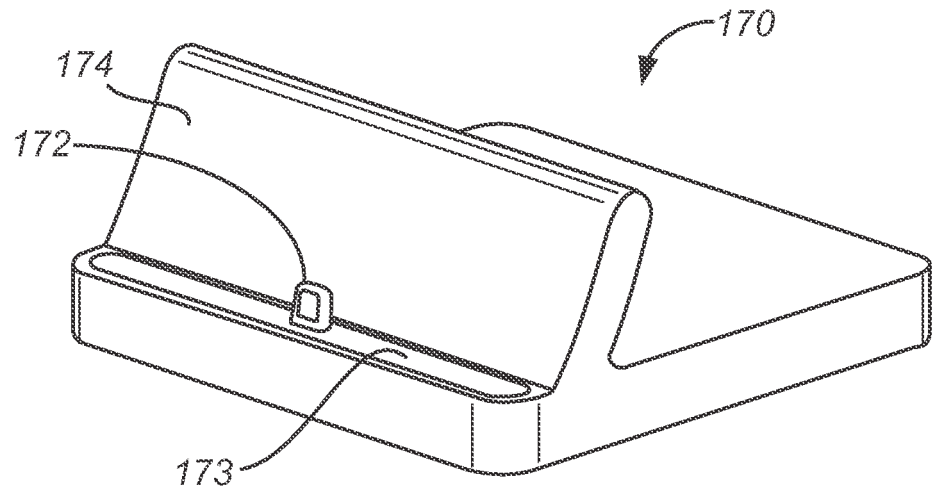


Fig. 21

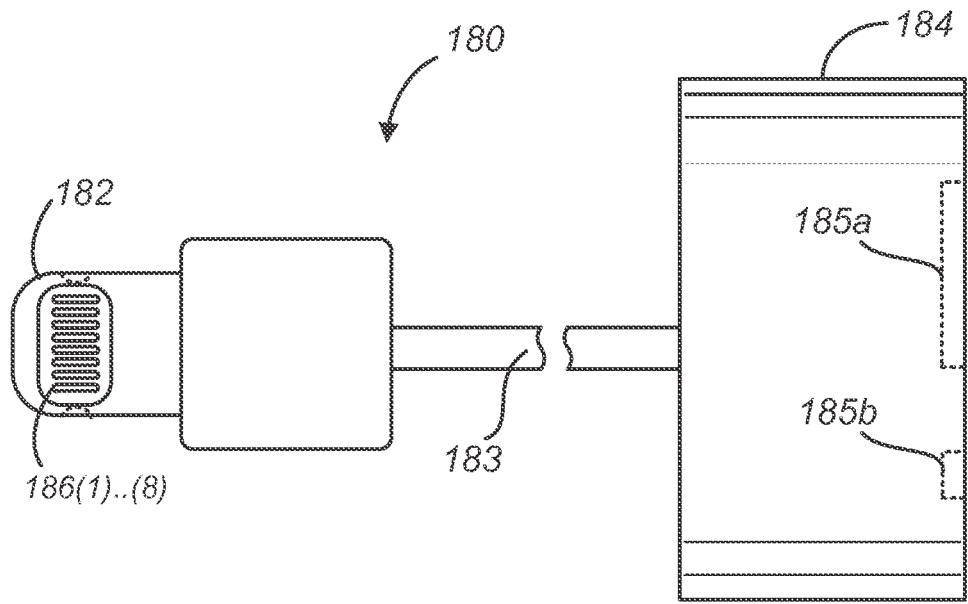


Fig. 22

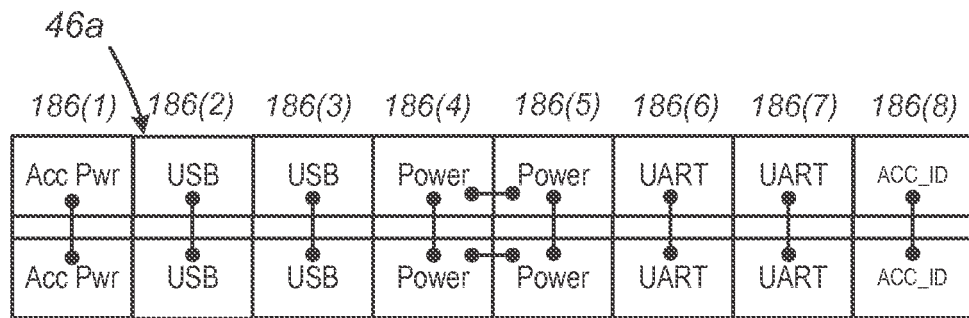


Fig. 23A

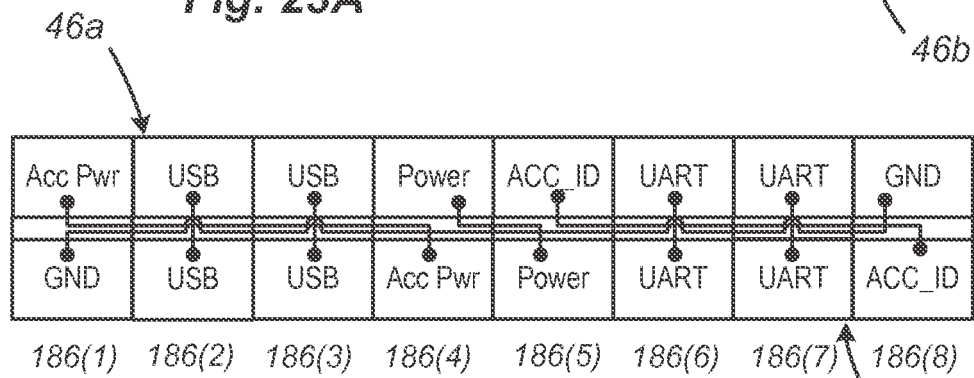


Fig. 23B

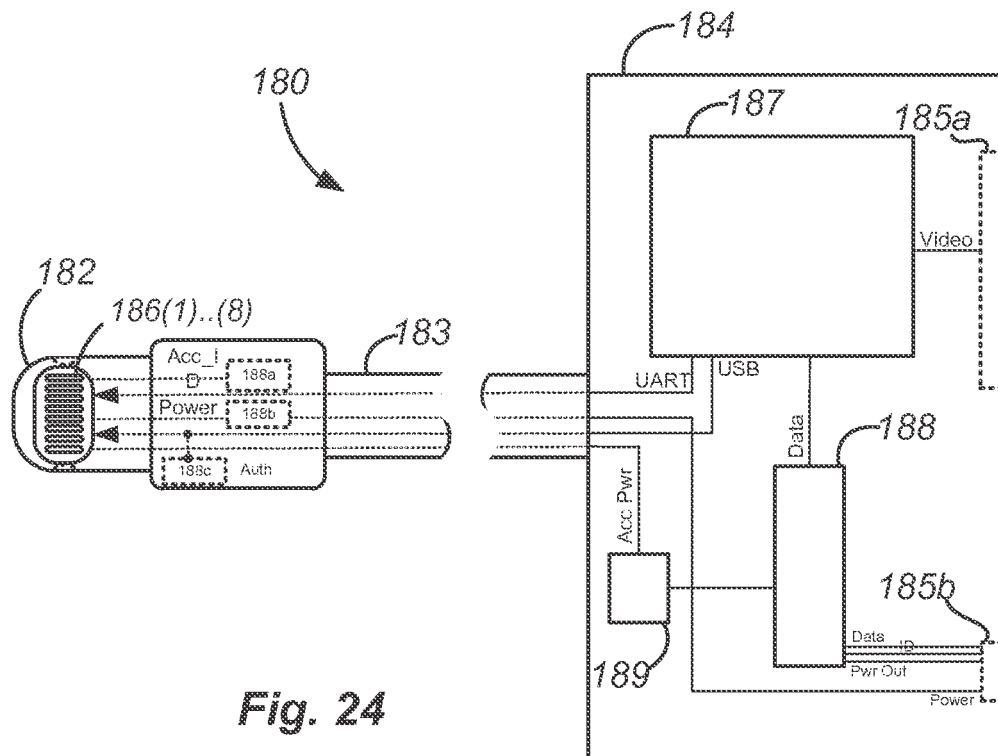


Fig. 24

Fig. 25

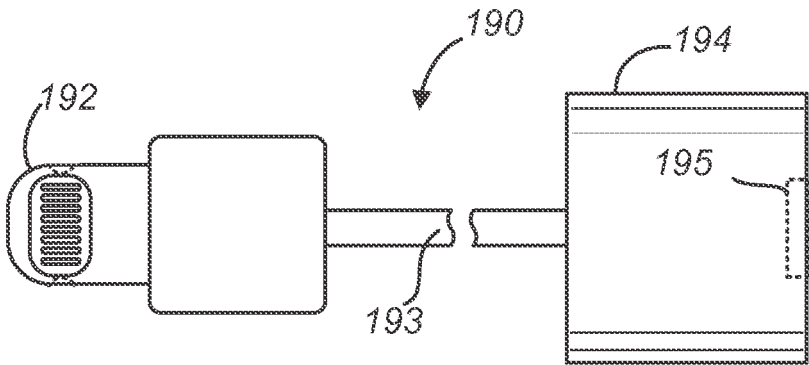


Fig. 26A

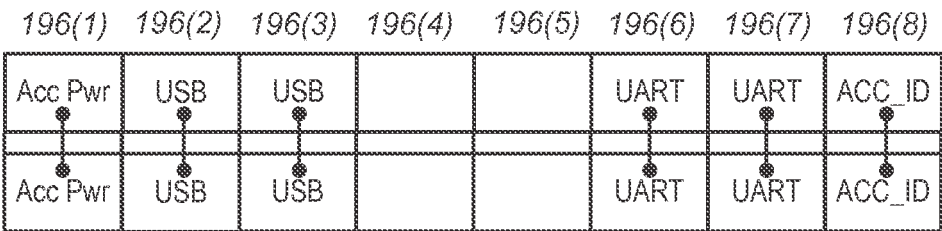


Fig. 26B

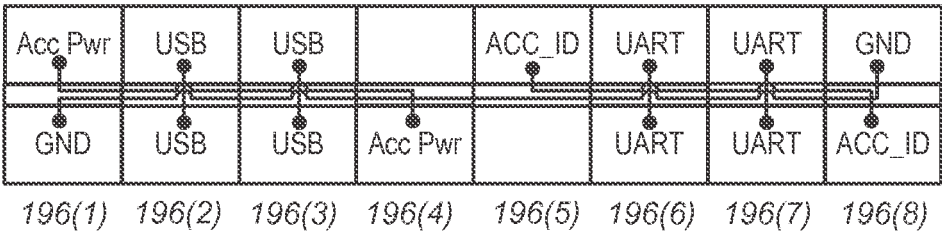
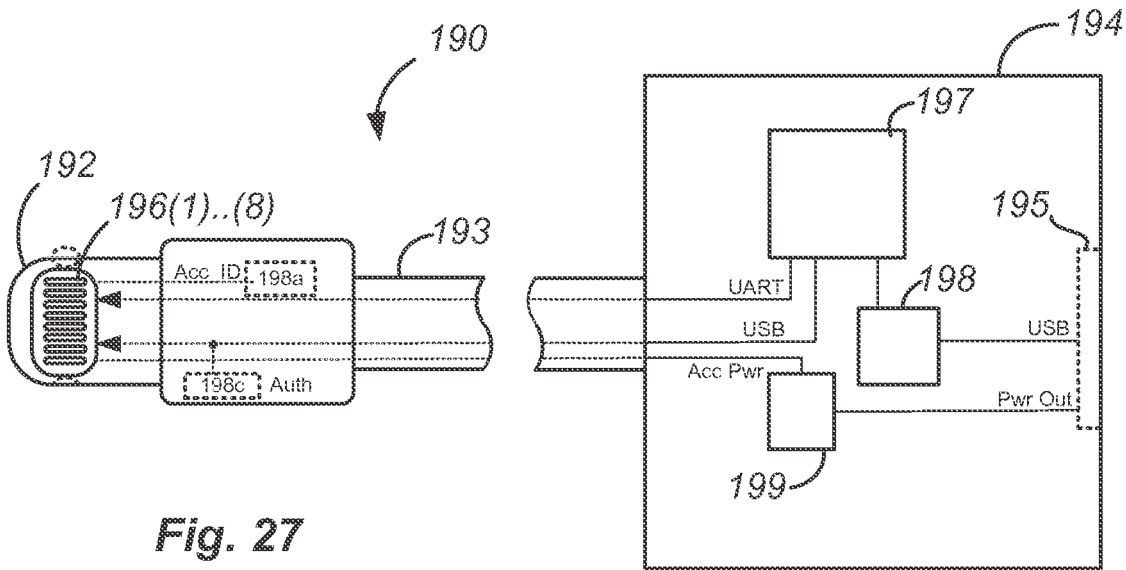
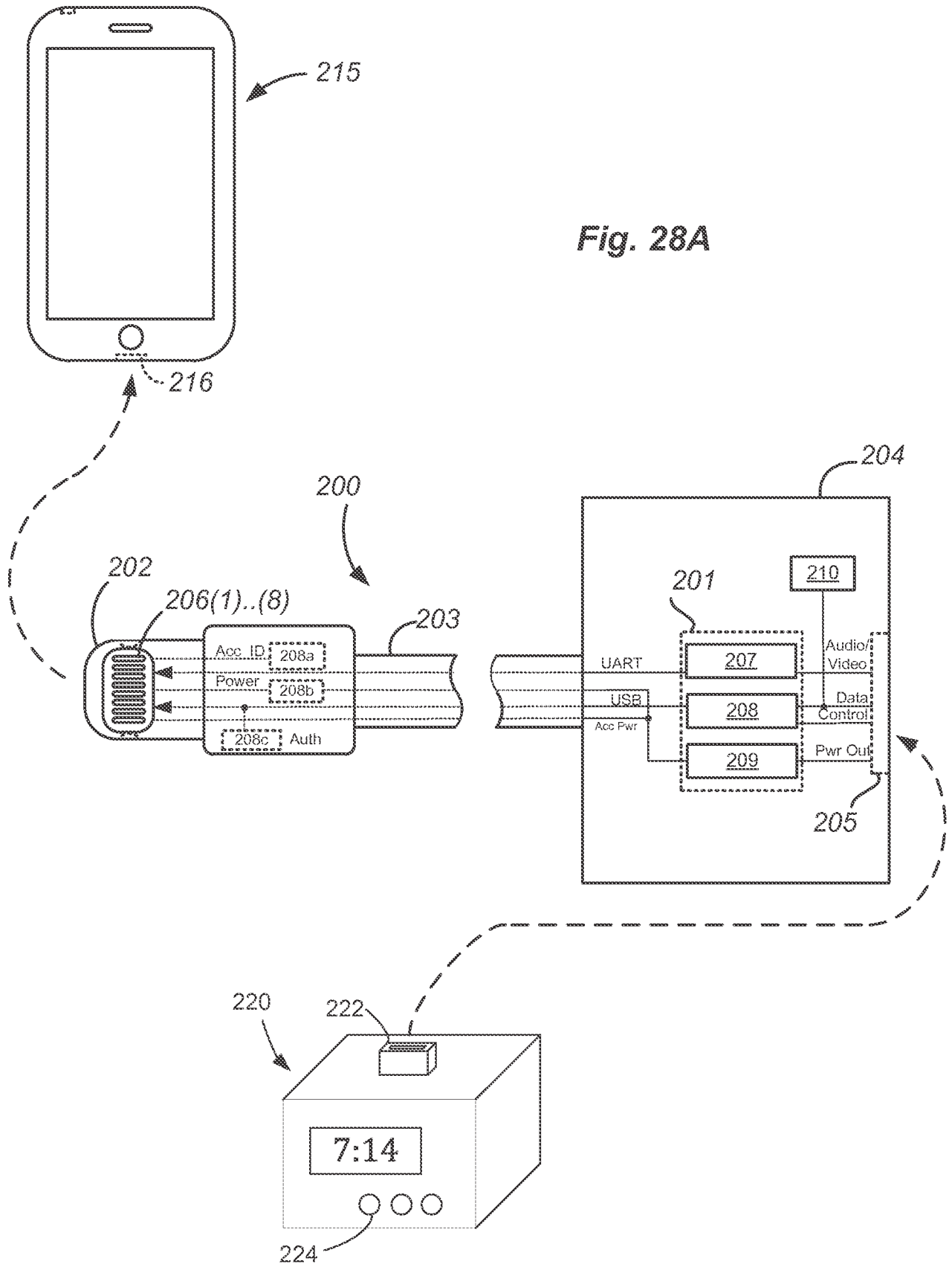


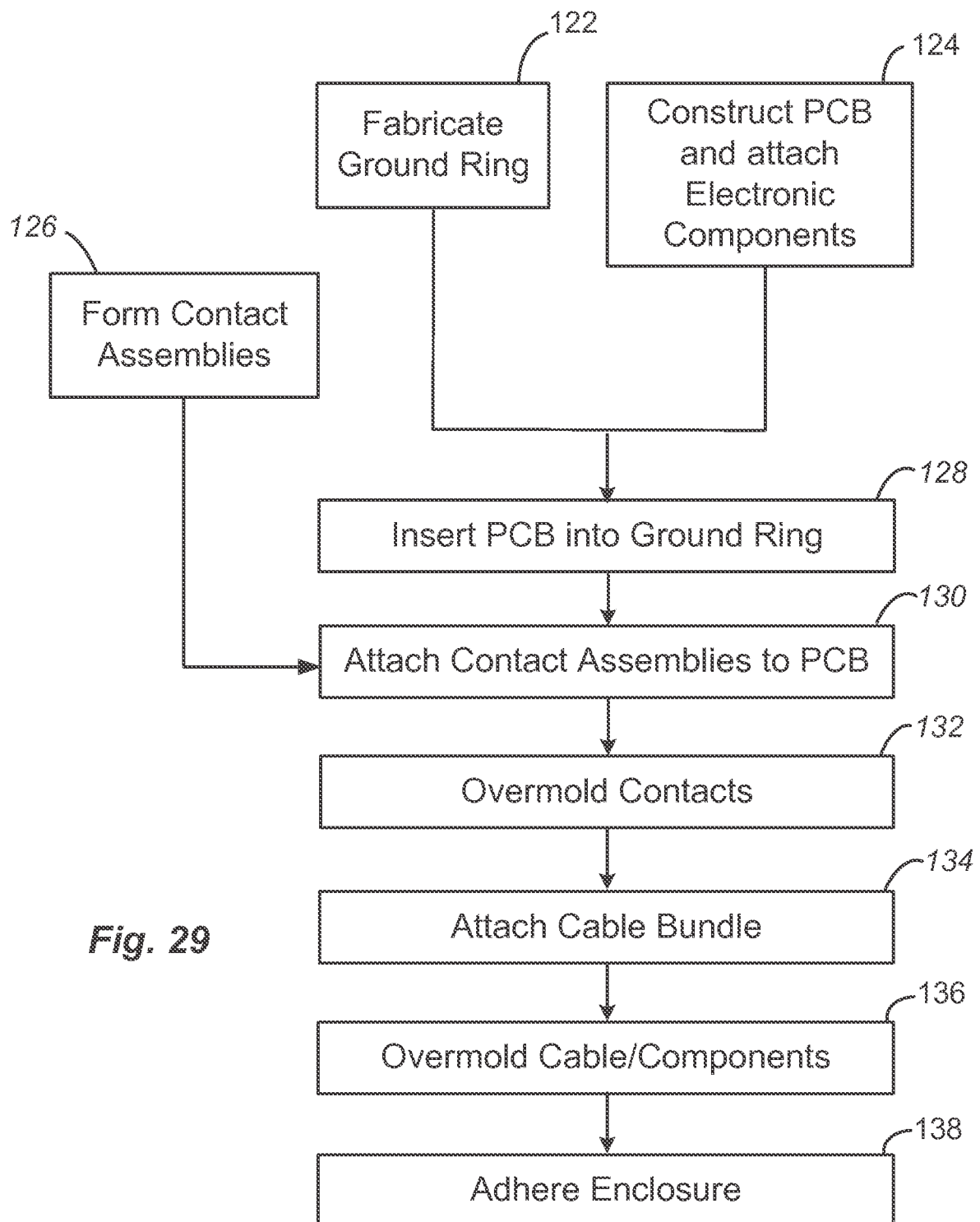
Fig. 27





Pin	Connector 205
1	Ground
2	Ground
3	DP Lane 0+
4	USB D+
5	DP Lane 0-
6	USB D-
7	DP Lane 1+
8	Vbus
9	DP Lane 1-
10	Accessory ID
11	DP Hot Plug Detect
12	--
13	Accessory Power
14	--
15	Ground
16	Ground
17	--
18	Accessory Receive
19	Accessory Transmit
20	Accessory Detect
21	Y/PR
22	C/Y
23	Comp/Pb
24	Rem_Sen
25	DP Auxillary Channel +
26	DP Auxillary Channel -
27	Audio Out Left
28	Audio Out Right
29	Audio Return
30	Ground

Fig. 28B

**Fig. 29**

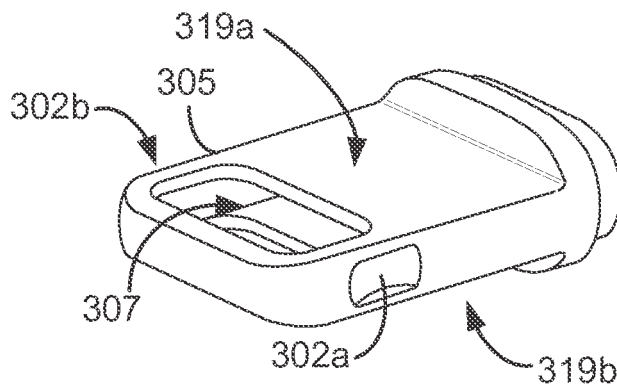


Fig. 30A

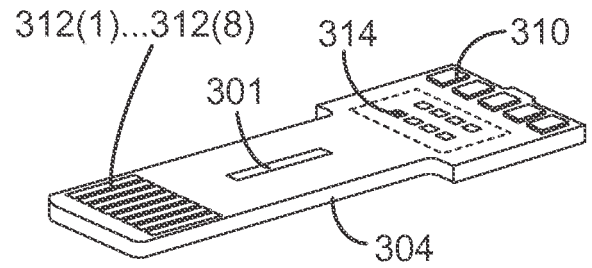


Fig. 30B

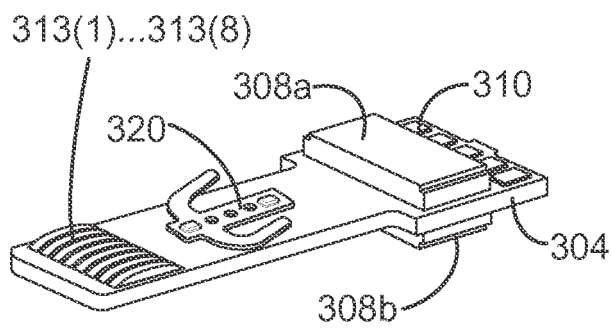


Fig. 30C

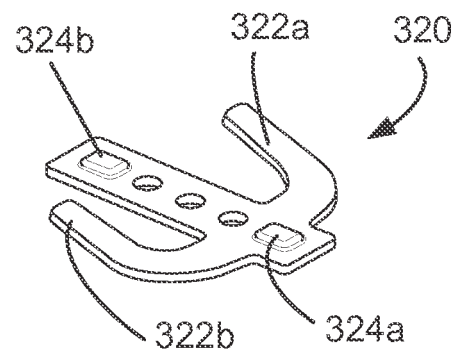


Fig. 30D

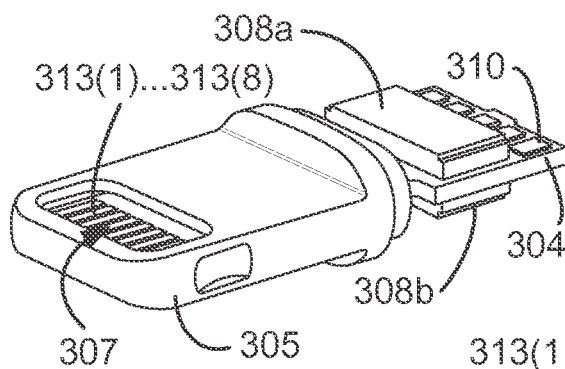


Fig. 30E

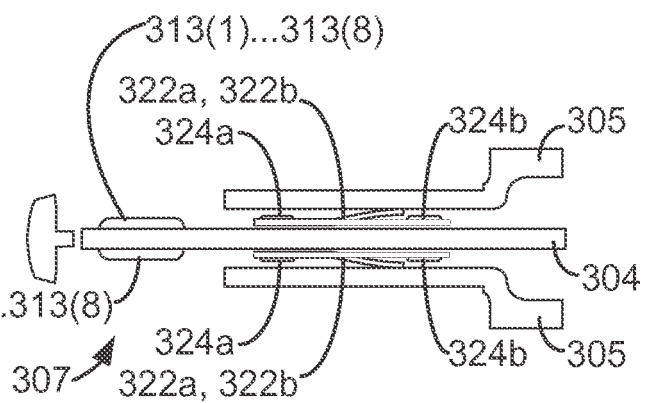
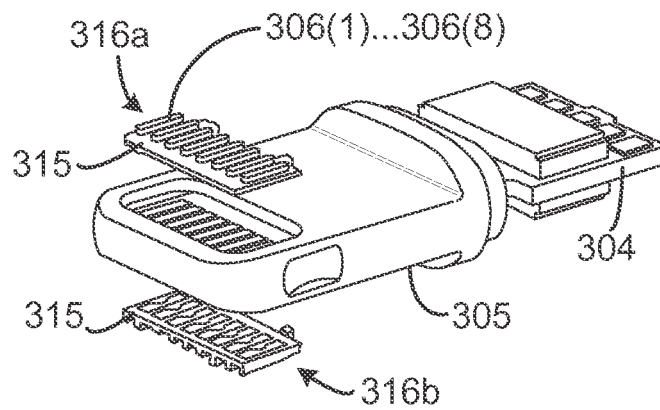
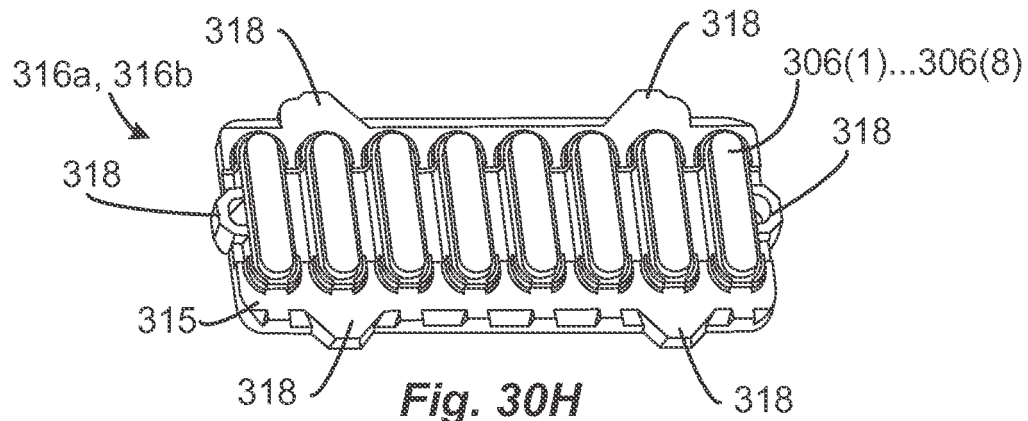
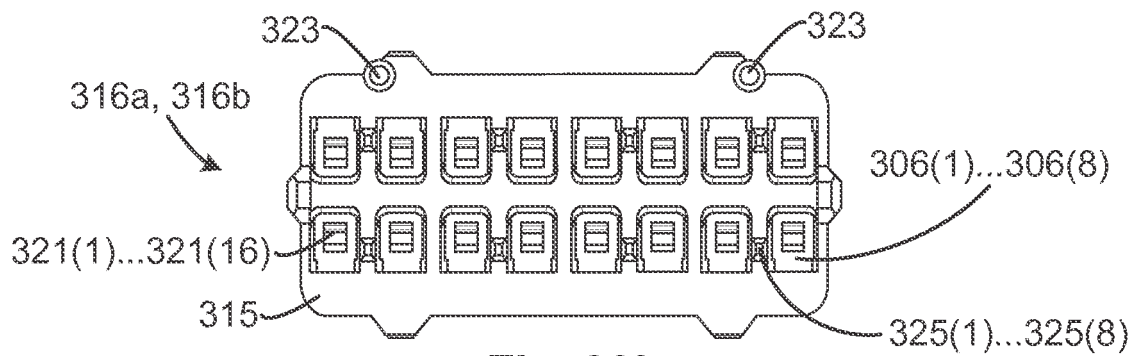
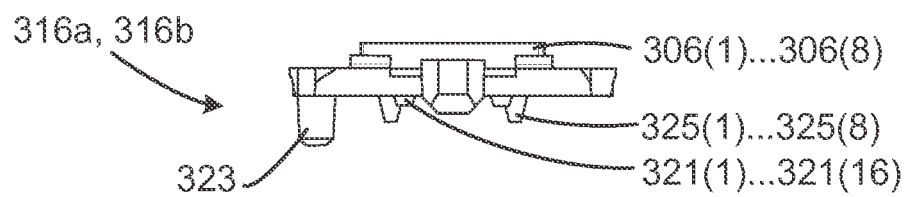
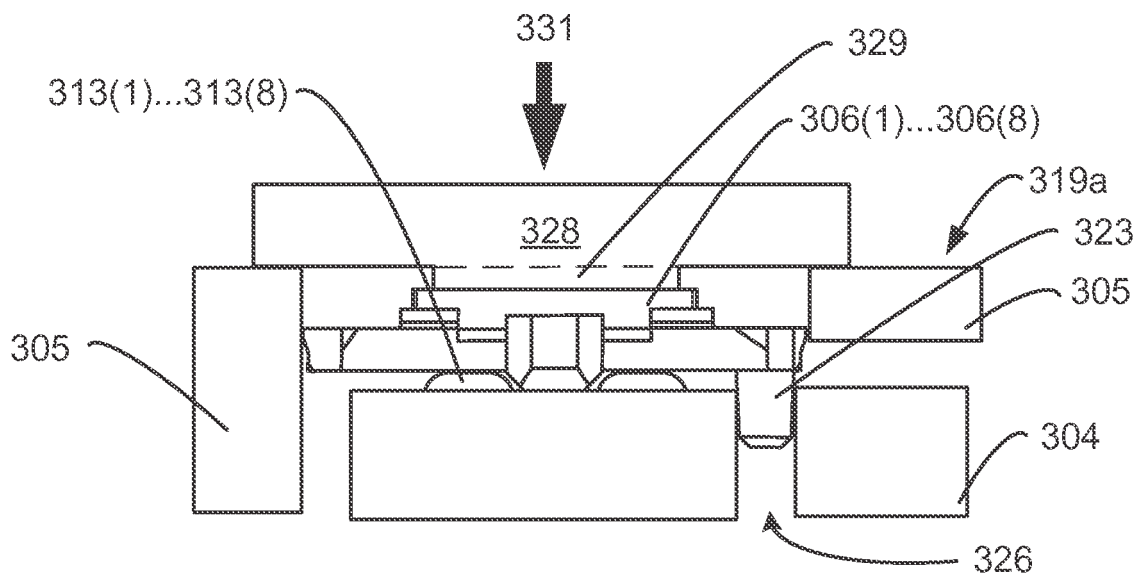
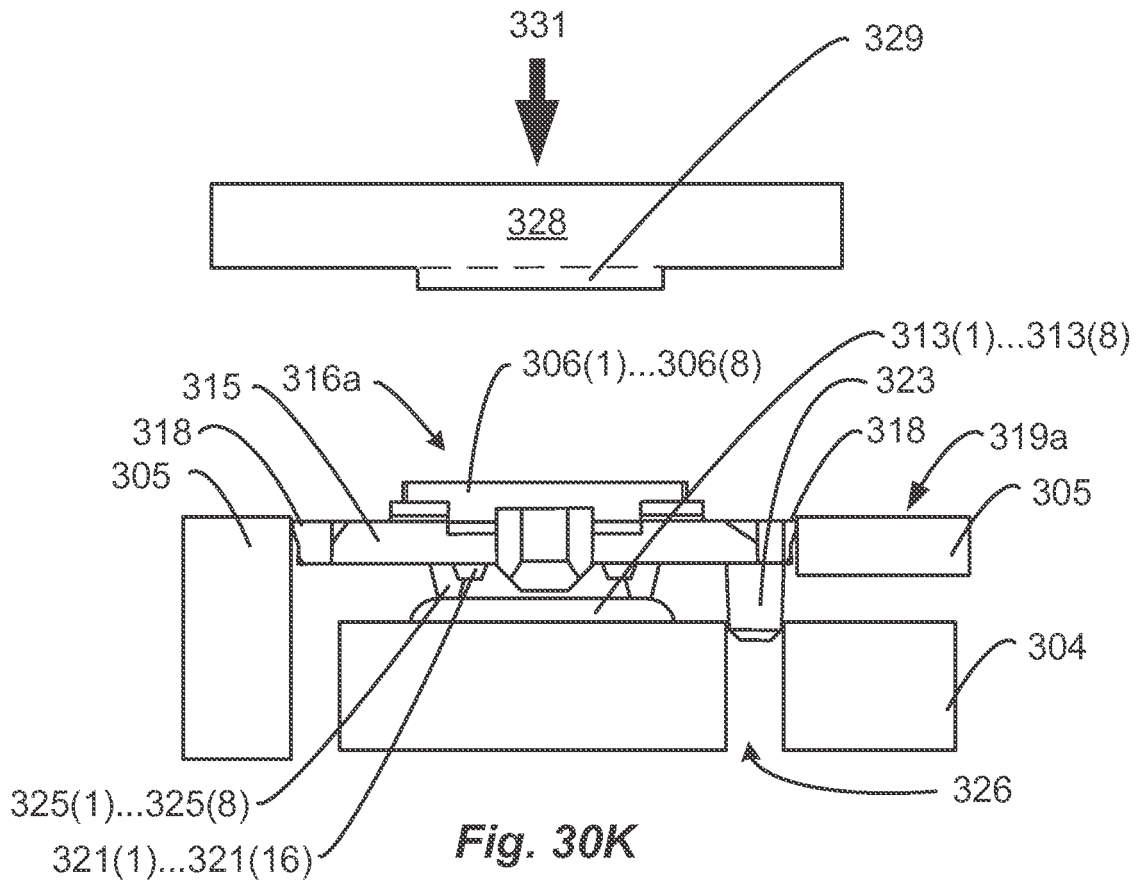
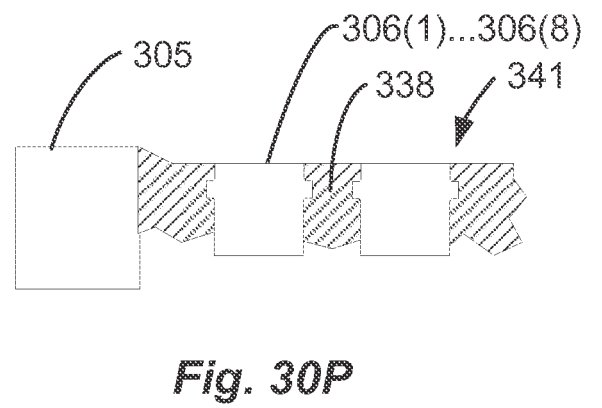
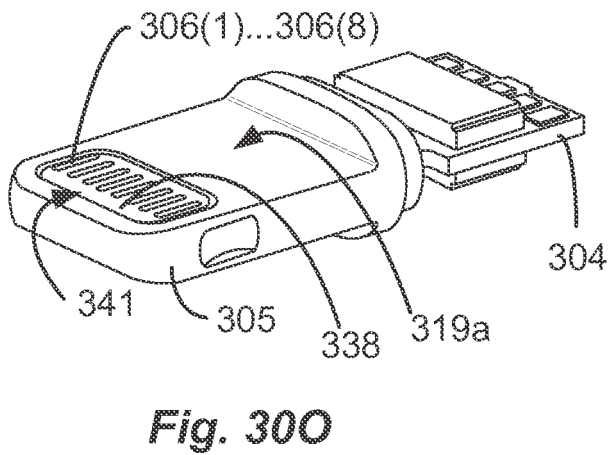
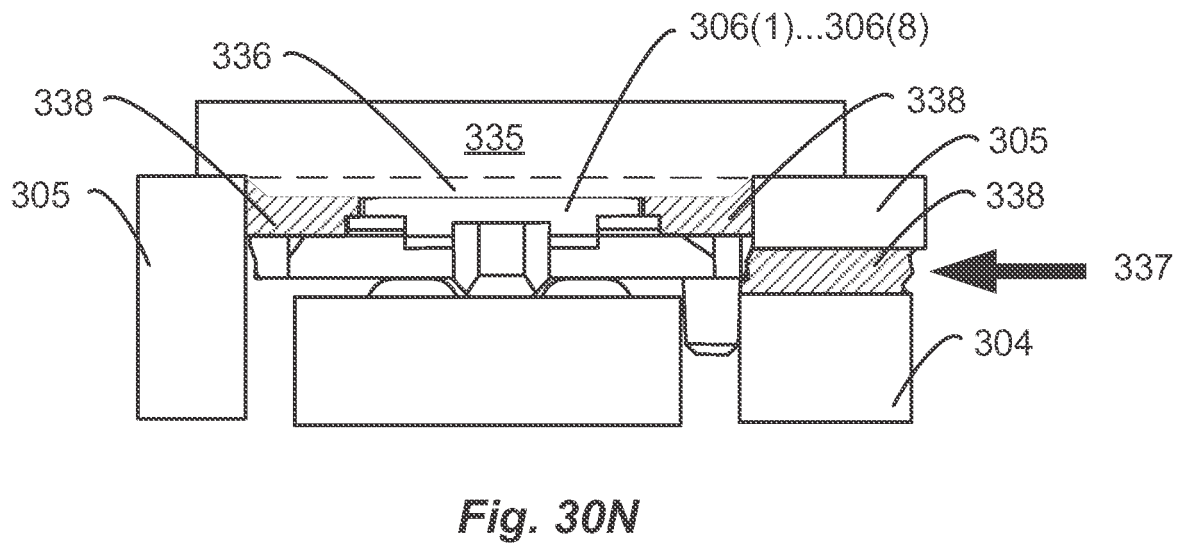
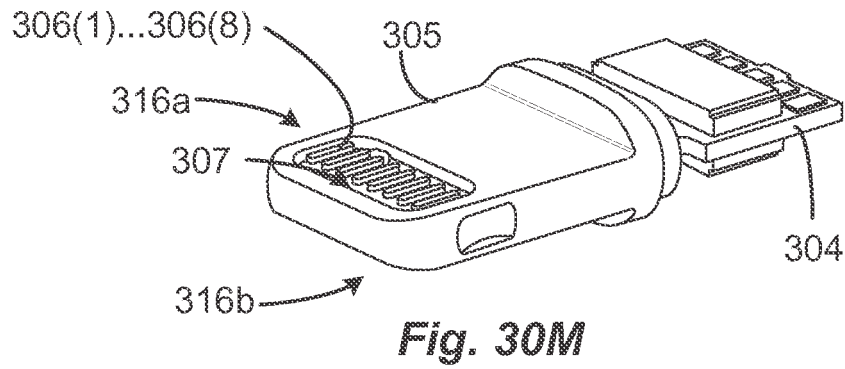


Fig. 30F

**Fig. 30G****Fig. 30H****Fig. 30I****Fig. 30J**

27/31





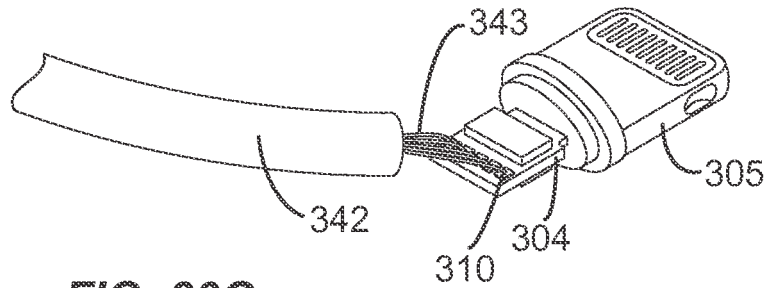


FIG. 30Q

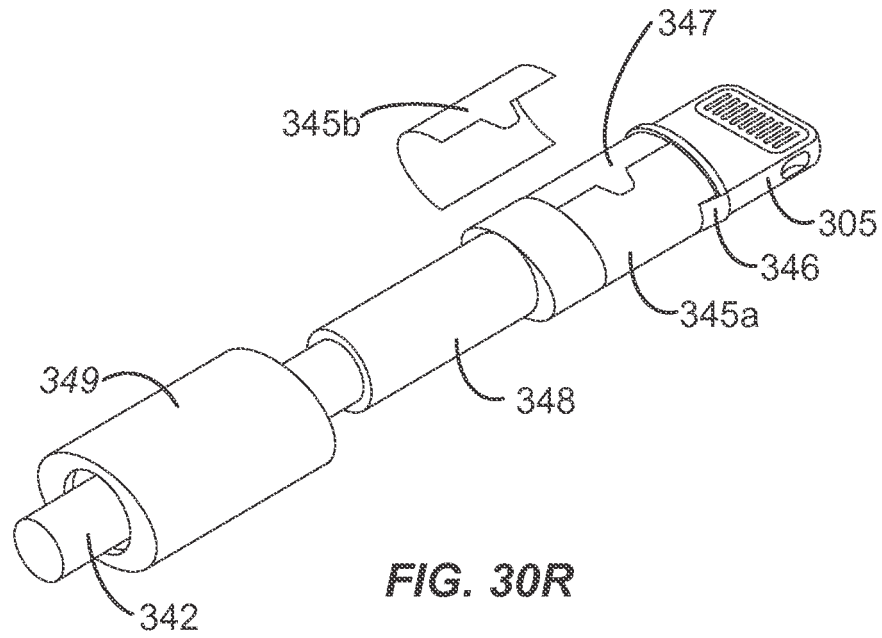


FIG. 30R

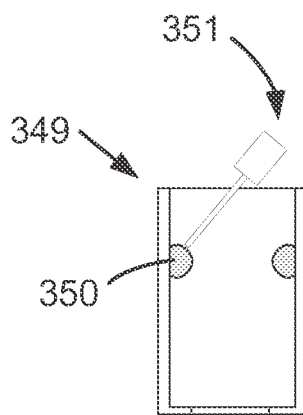


FIG. 30S

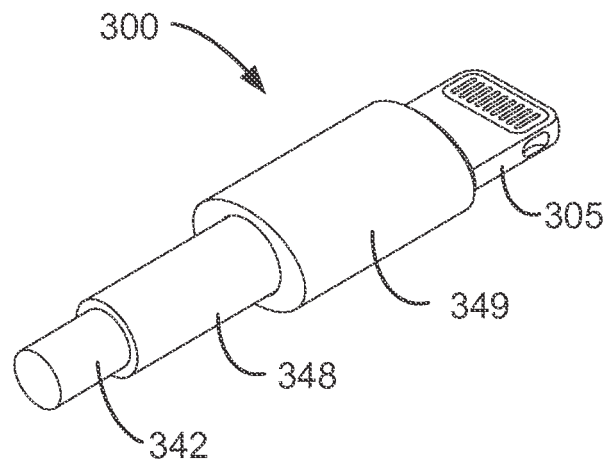
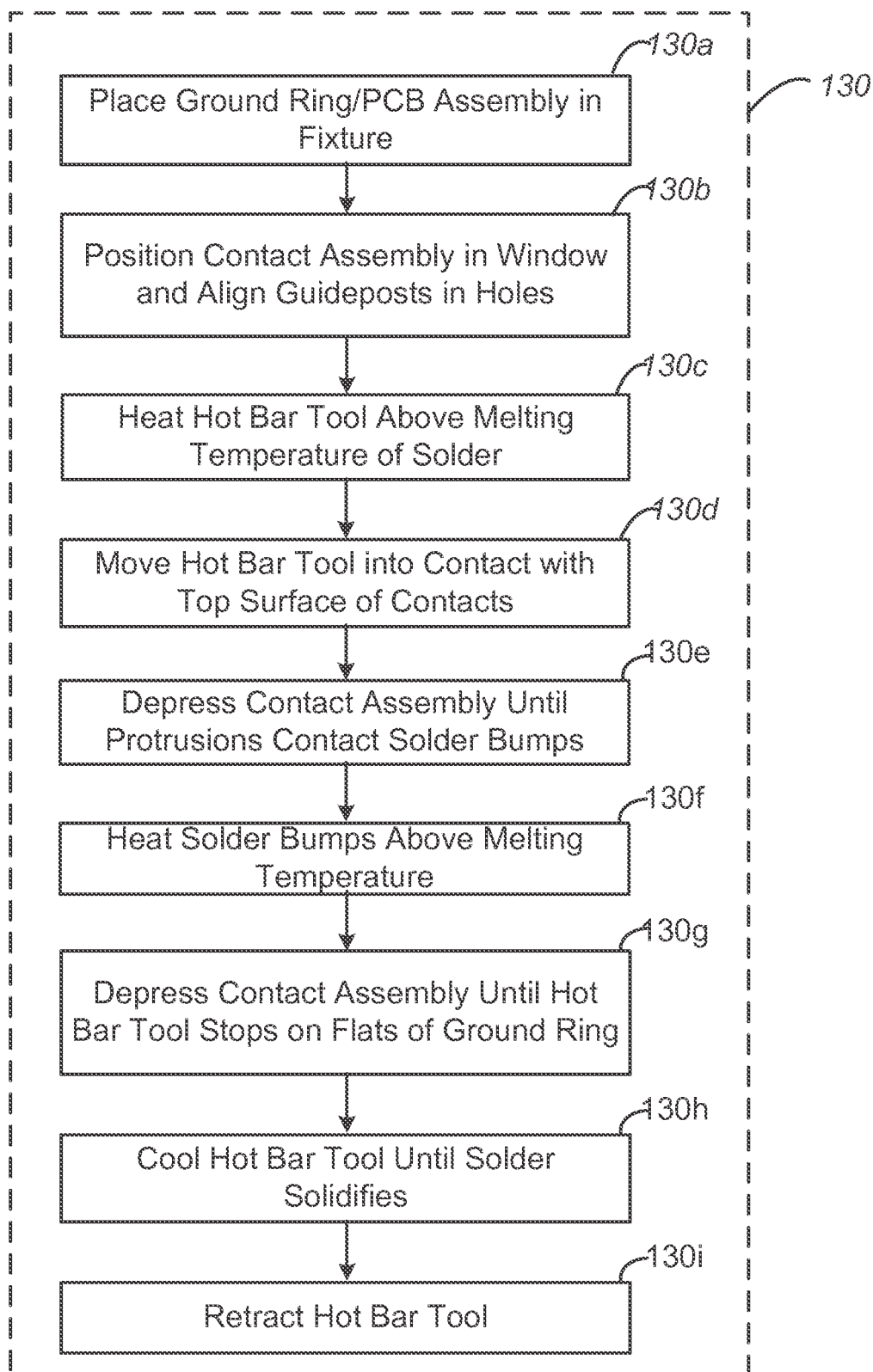


FIG. 30T

**Fig. 31**

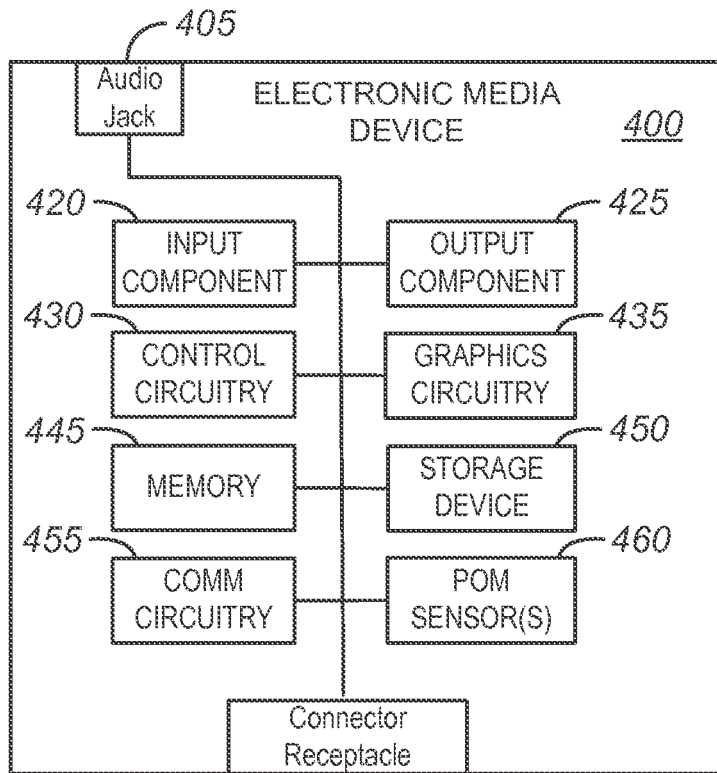


FIG. 32

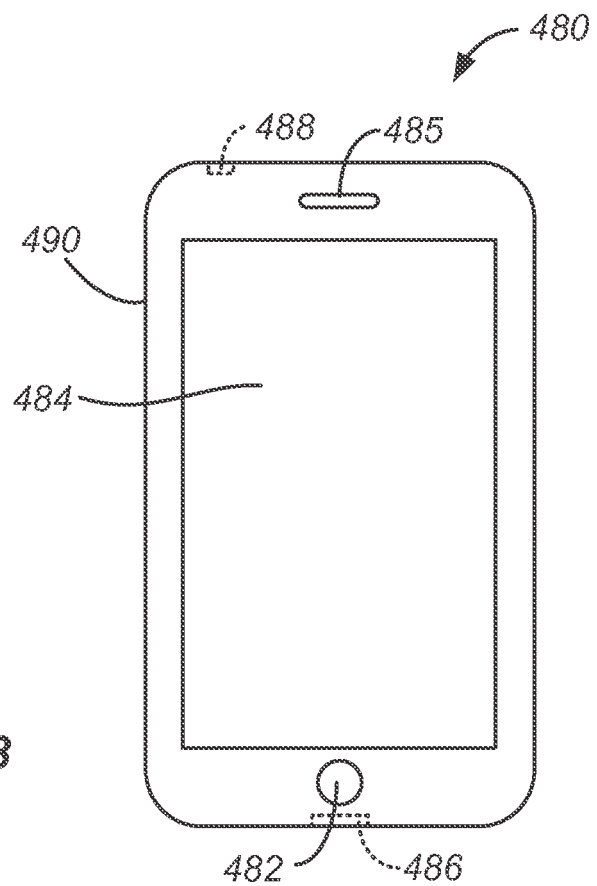


FIG. 33