

Abstract

Monitor Geometry Correction Circuit and Method

5 A monitor geometry correction circuit includes a counter (16) for counting horizontal scan lines (HSYNC) and providing the total number of such lines within a vertical frame to a store (20). A position calculating circuit (23) calculates the actual position x in the vertical frame that has been reached and a correction signal
10 calculating circuit (24) calculates a monitor geometry correction signal $Y(x)$ based on the expression:

$$Y = a_0 + a_1 \cdot x + a_2 \cdot x^2 + a_3 \cdot x^3 + \dots + a_n \cdot x^n,$$

where a_i are predetermined coefficients.

15 FIG. 7