

Abstract of the Disclosure

A composite video input signal (S1) is separated into luminance (Y1) and chrominance (C1) components by a signal separation filter (12) and the components are applied to a display processor (16) having a video display (18) device coupled thereto for providing displayed images. For enhancement of detail in dark and light areas of the displayed images, the luminance signal is coupled to the display processor via a non-linear luminance signal processor (20) which provides additional amplification in the dark and light areas but tends also to degrade the signal to noise ratio in those areas. To avoid a loss of signal to noise ratio in the enhanced areas the receiver (10) includes a by-pass circuit (50) for by-passing high frequency components of the luminance signal around the non-linear luminance signal processor (20) and a filter (R1,R2,C3) in the non-linear luminance signal processor for attenuating the high frequency components.

