Fig. 1
Prior Art

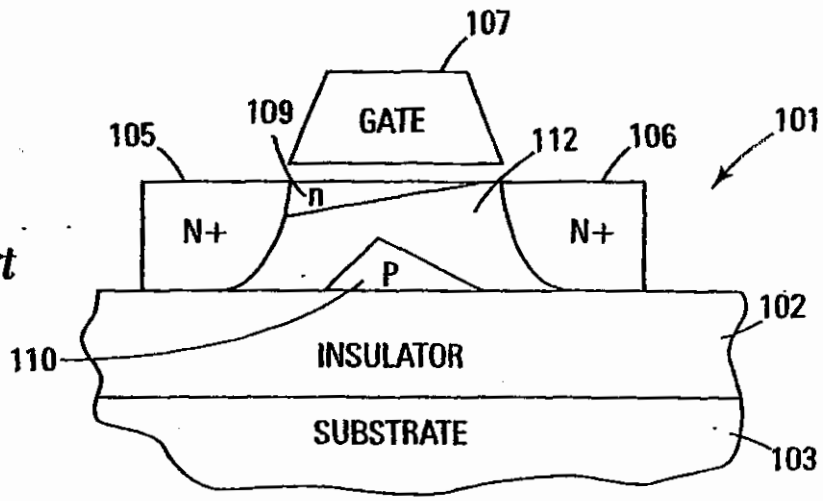


Fig. 2

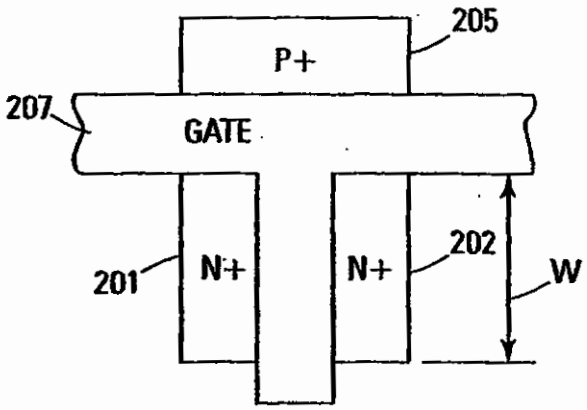
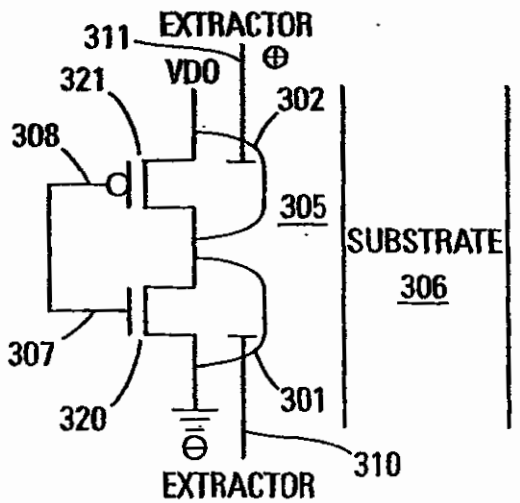


Fig. 3



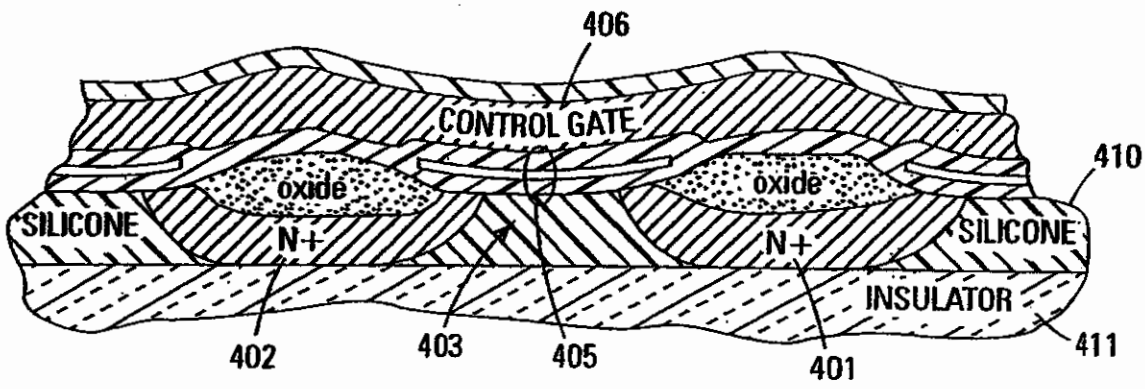

406
CONTROL GATE
410
oxide
oxide
SILICONE
N+
N+
SILICONE
INSULATOR
402
403
405
401
411

Fig. 4

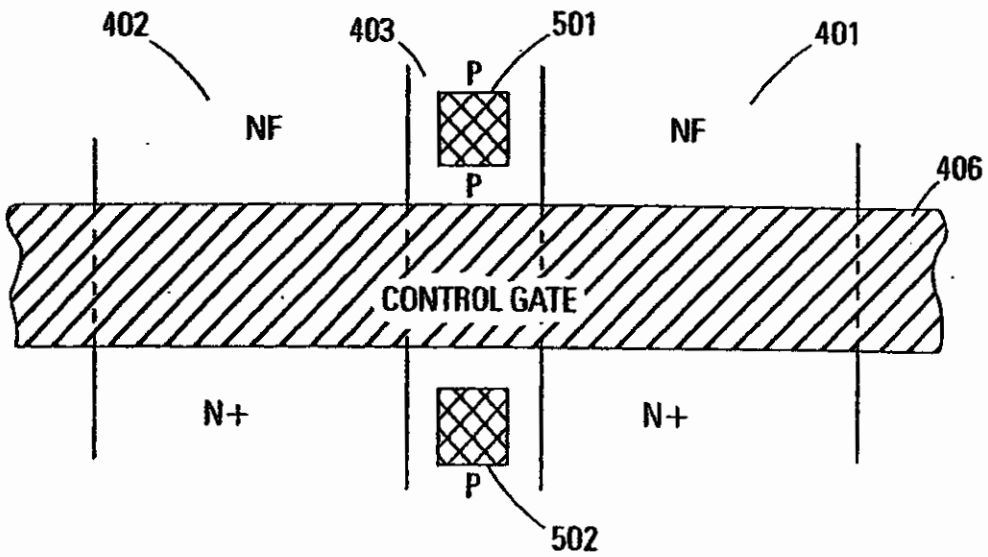

402
403
501
401
P
NF
NF
406
P
CONTROL GATE
N+
N+
P
502

Fig. 5

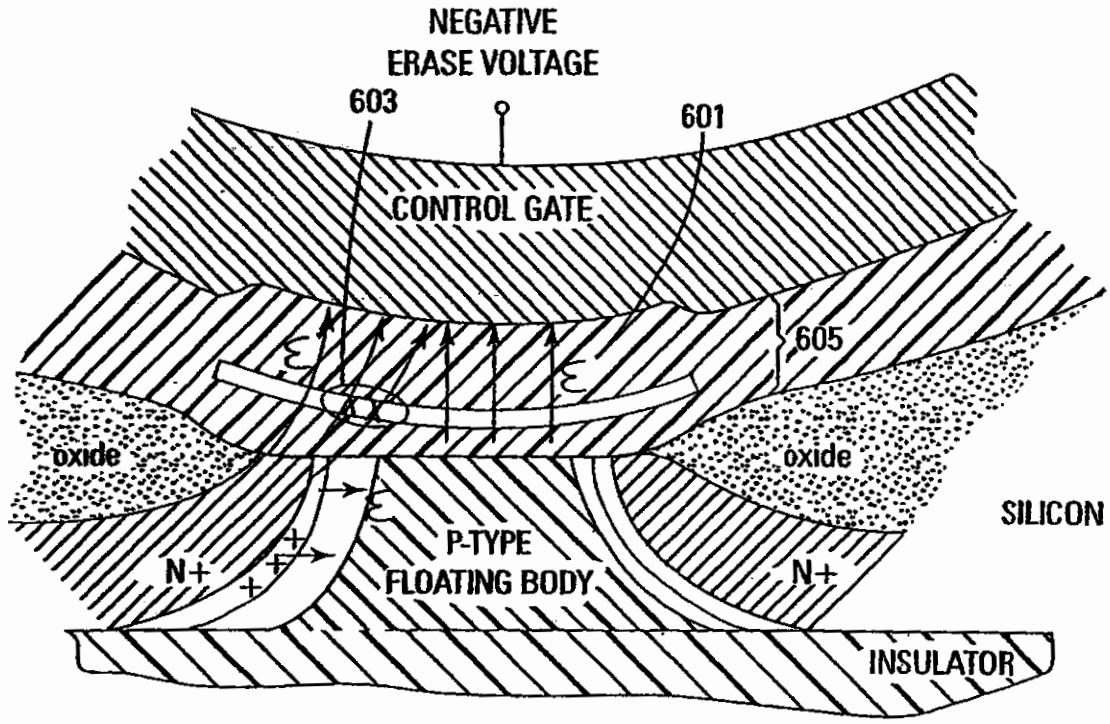


Fig. 6
Prior Art

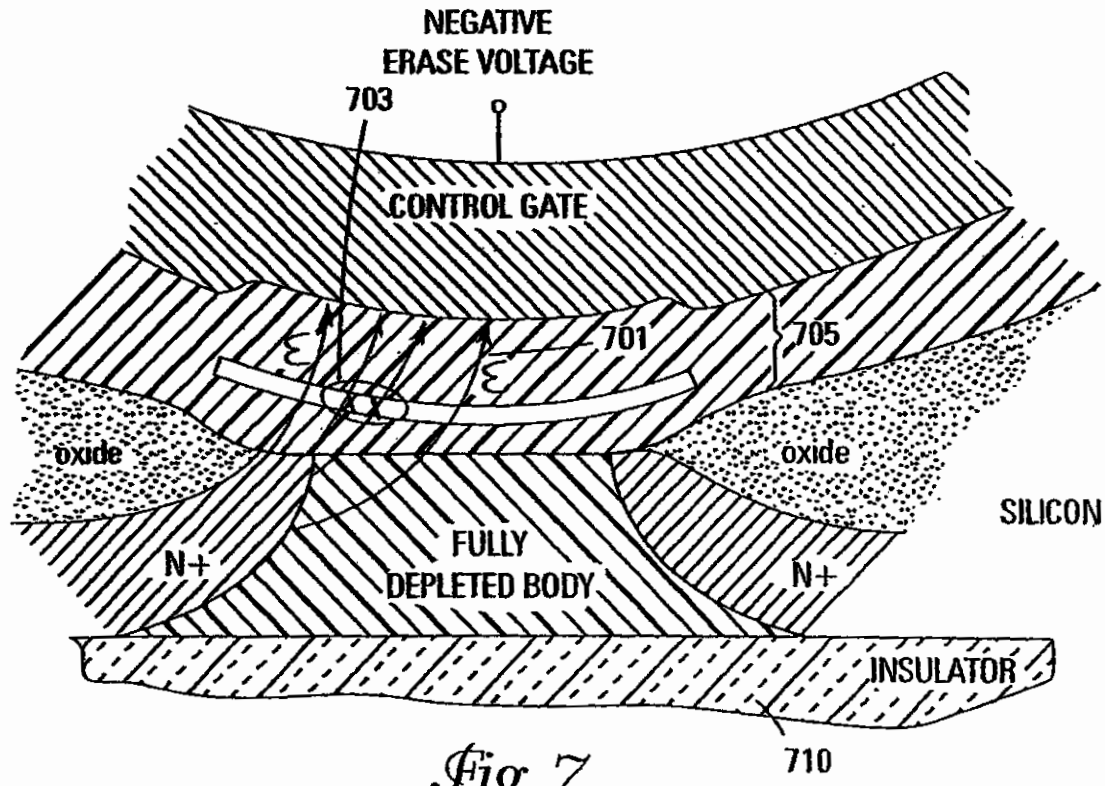


Fig. 7

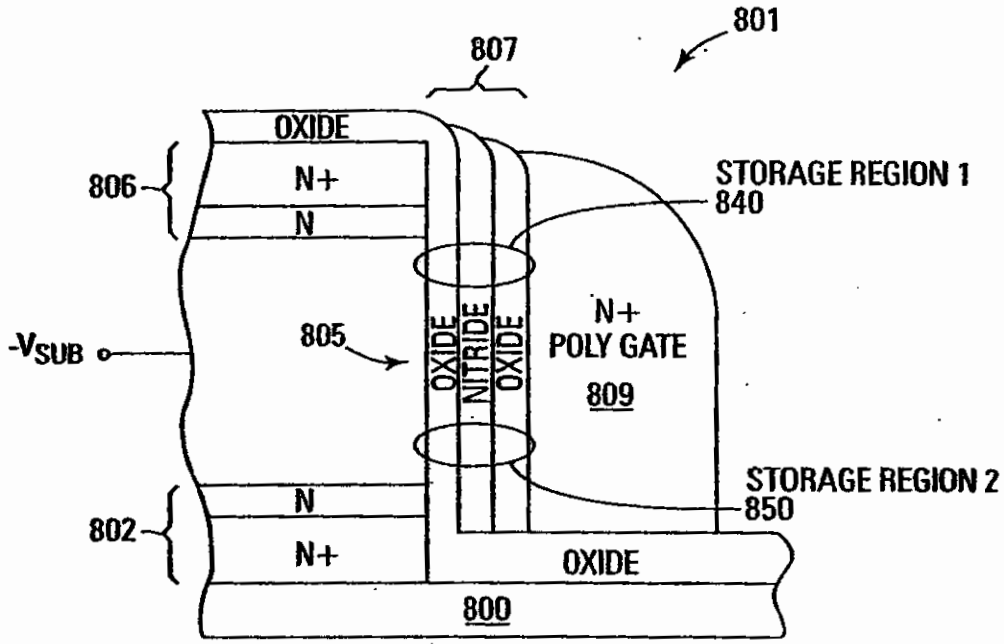

801
807
OXIDE
806
N+
N
STORAGE REGION 1
840
-VSUB
805
OXIDE
NITRIDE
OXIDE
N+
POLY GATE
809
STORAGE REGION 2
850
802
N
N+
OXIDE
800

Fig. 8

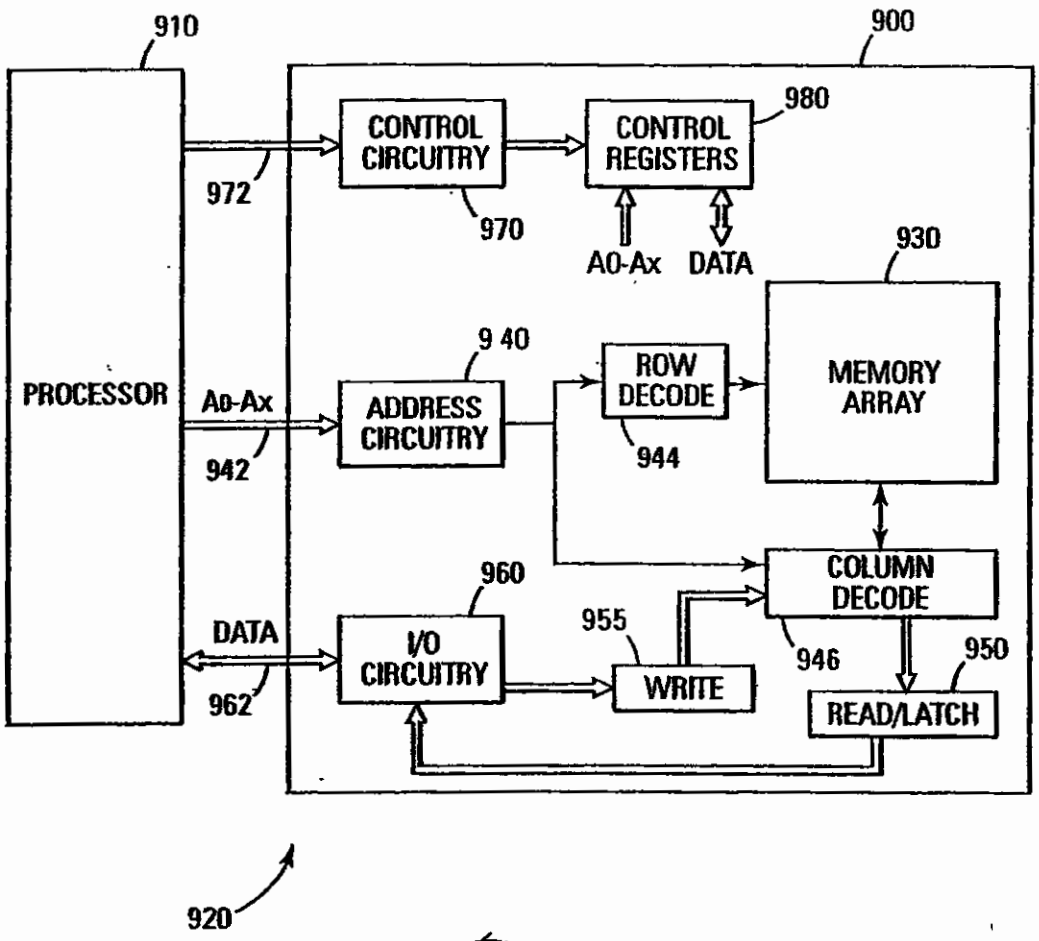

910
900
PROCESSOR
CONTROL CIRCUITRY
CONTROL REGISTERS
980
972
970
A0-Ax
DATA
930
940
ROW DECODE
MEMORY ARRAY
A0-Ax
ADDRESS CIRCUITRY
942
944
COLUMN DECODE
960
955
DATA
I/O CIRCUITRY
WRITE
946
950
962
READ/LATCH
920

Fig. 9