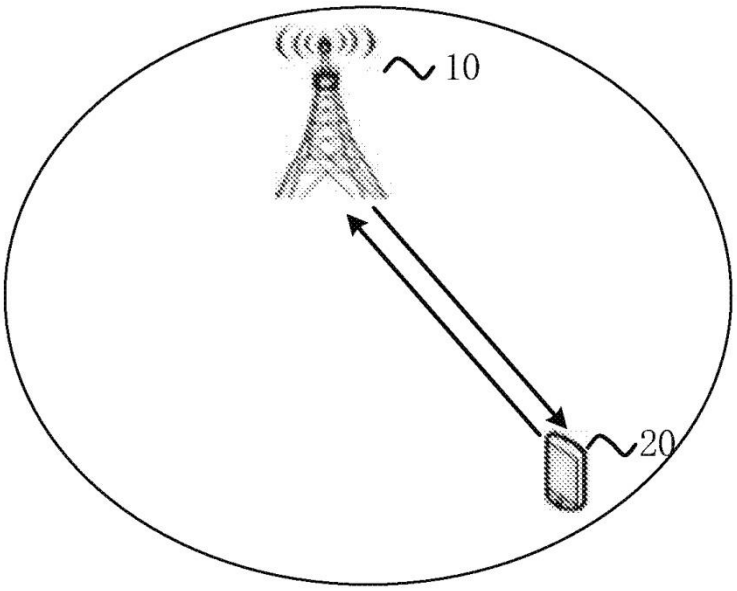
10
20

FIG. 1

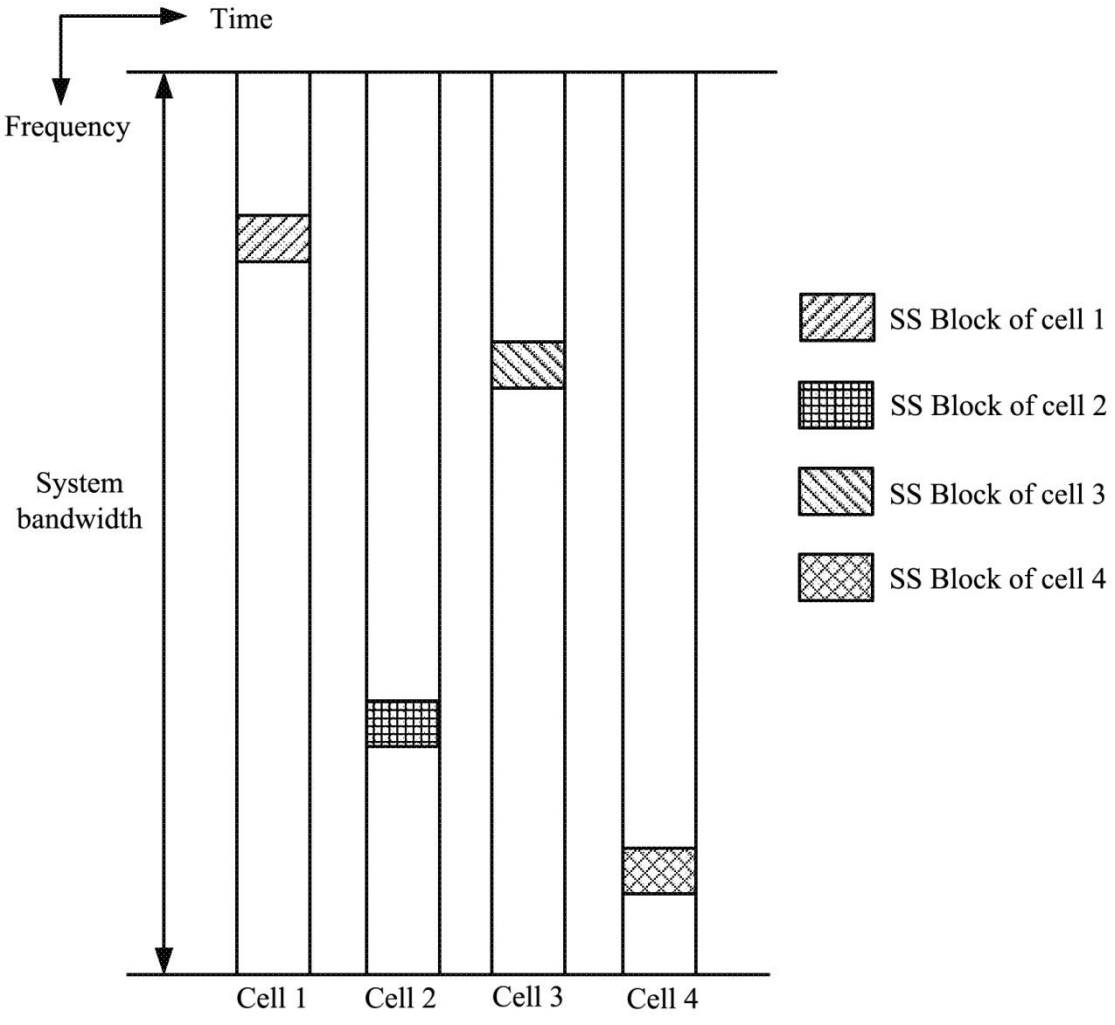
Time
Frequency
System bandwidth
Cell 1
Cell 2
Cell 3
Cell 4
SS Block of cell 1
SS Block of cell 2
SS Block of cell 3
SS Block of cell 4

FIG. 2

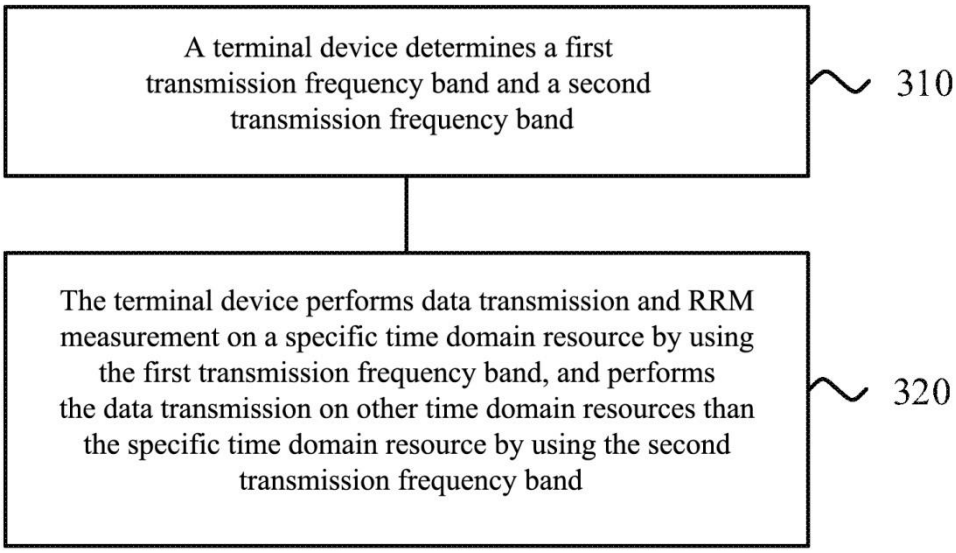
A terminal device determines a first transmission frequency band and a second transmission frequency band
310
The terminal device performs data transmission and RRM measurement on a specific time domain resource by using the first transmission frequency band, and performs the data transmission on other time domain resources than the specific time domain resource by using the second transmission frequency band
320

FIG. 3

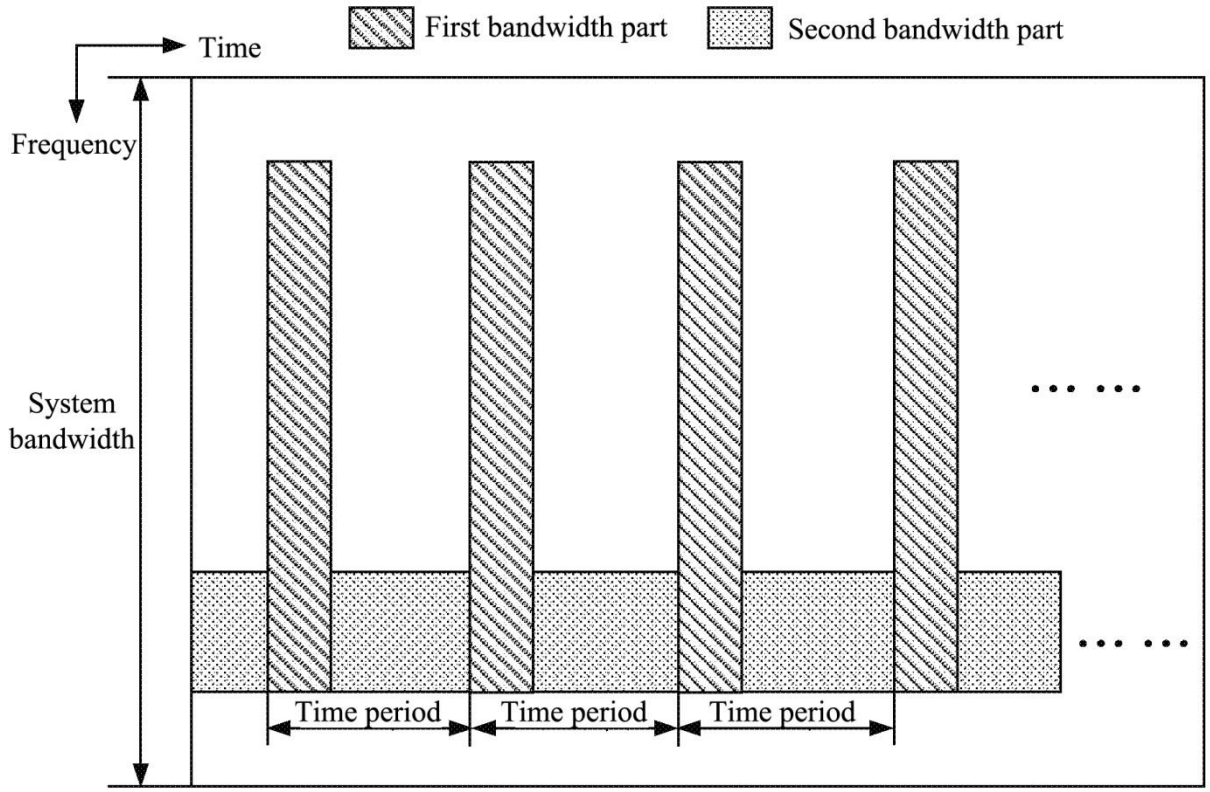
First bandwidth part
Second bandwidth part
Time
Frequency
System bandwidth
Time period
Time period
Time period

FIG. 4

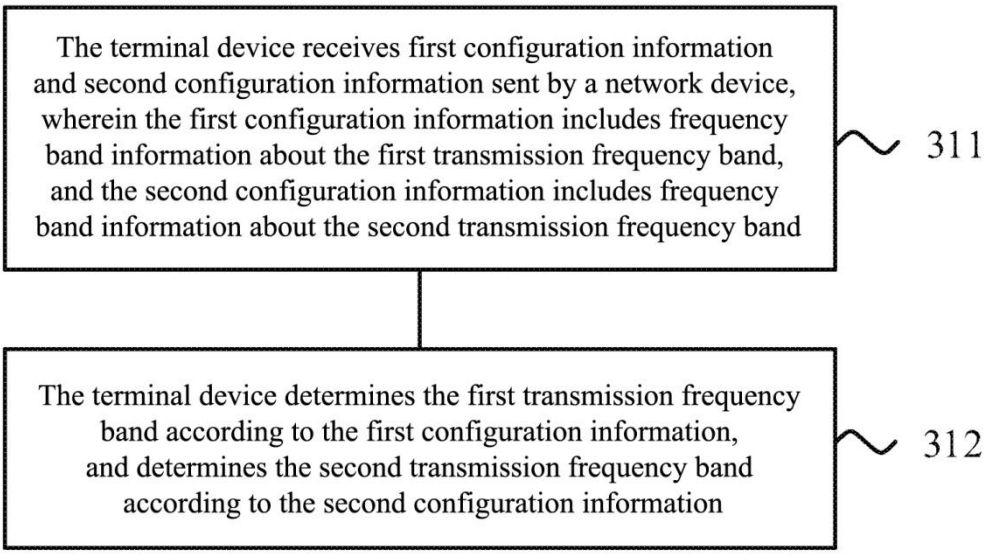
The terminal device receives first configuration information and second configuration information sent by a network device, wherein the first configuration information includes frequency band information about the first transmission frequency band, and the second configuration information includes frequency band information about the second transmission frequency band
311
The terminal device determines the first transmission frequency band according to the first configuration information, and determines the second transmission frequency band according to the second configuration information
312

FIG. 5

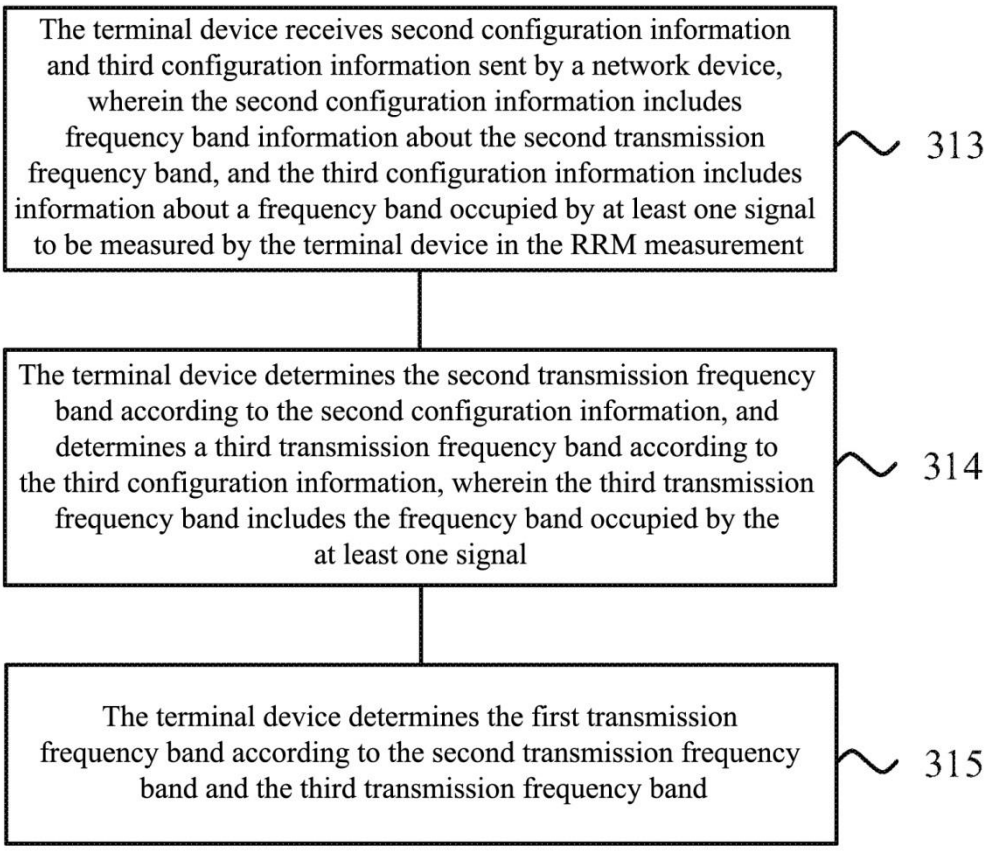
The terminal device receives second configuration information and third configuration information sent by a network device, wherein the second configuration information includes frequency band information about the second transmission frequency band, and the third configuration information includes information about a frequency band occupied by at least one signal to be measured by the terminal device in the RRM measurement
313
The terminal device determines the second transmission frequency band according to the second configuration information, and determines a third transmission frequency band according to the third configuration information, wherein the third transmission frequency band includes the frequency band occupied by the at least one signal
314
The terminal device determines the first transmission frequency band according to the second transmission frequency band and the third transmission frequency band
315

FIG. 6

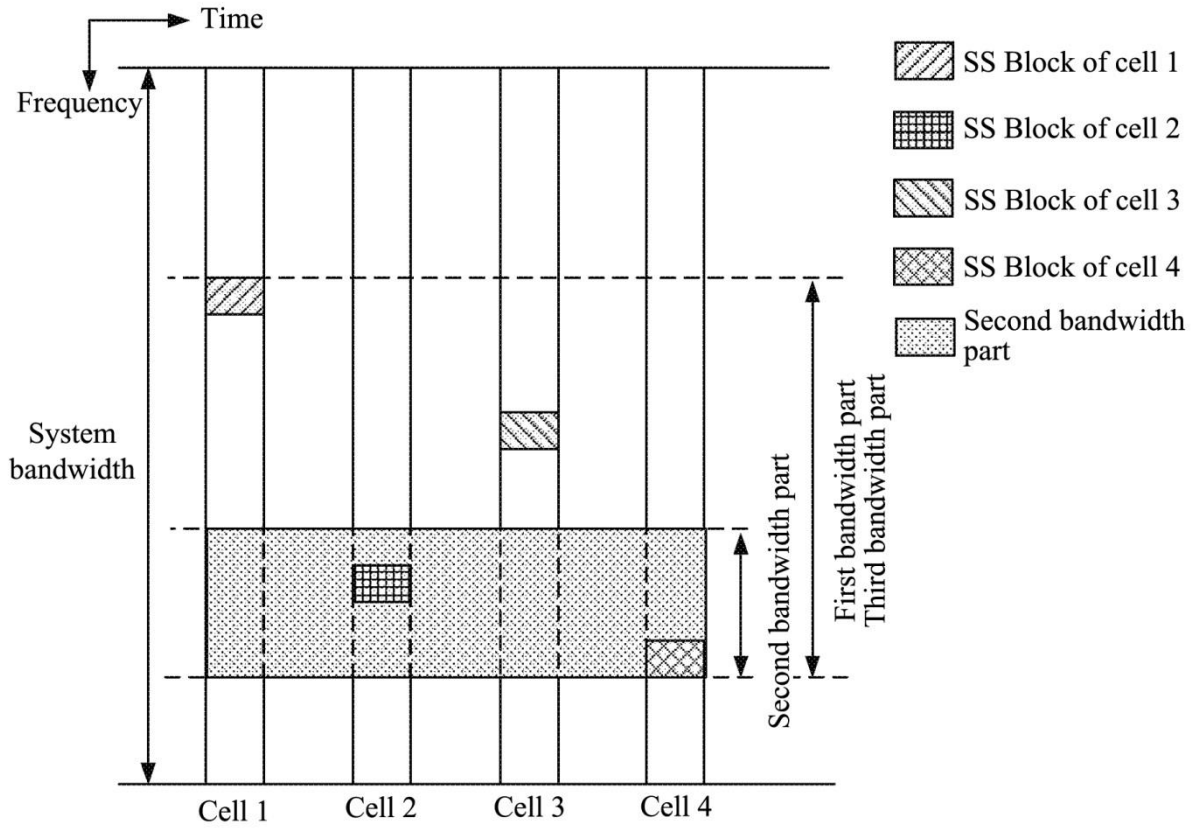
Time
Frequency
System bandwidth
Cell 1
Cell 2
Cell 3
Cell 4
SS Block of cell 1
SS Block of cell 2
SS Block of cell 3
SS Block of cell 4
Second bandwidth part
First bandwidth part
Third bandwidth part
Second bandwidth part

FIG. 7

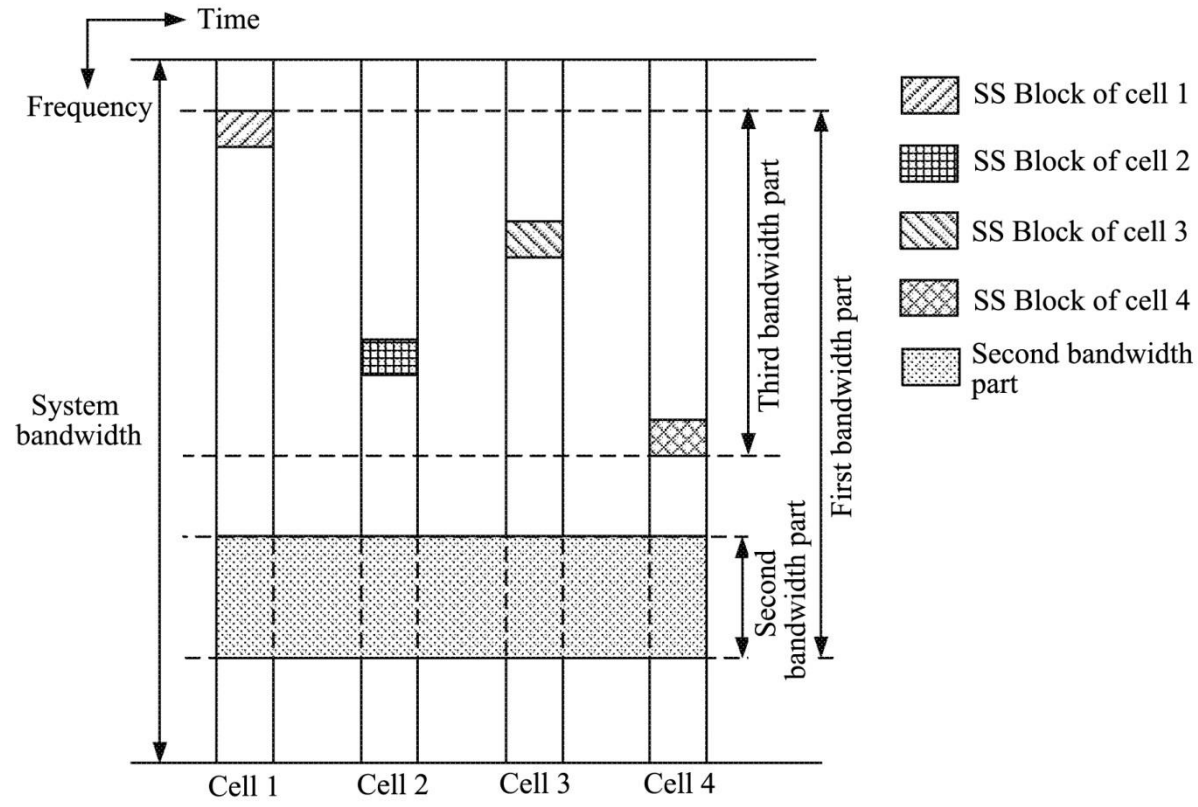
Time
Frequency
System bandwidth
Cell 1
Cell 2
Cell 3
Cell 4
SS Block of cell 1
SS Block of cell 2
SS Block of cell 3
SS Block of cell 4
Second bandwidth part
Third bandwidth part
First bandwidth part
Second bandwidth part

FIG. 8

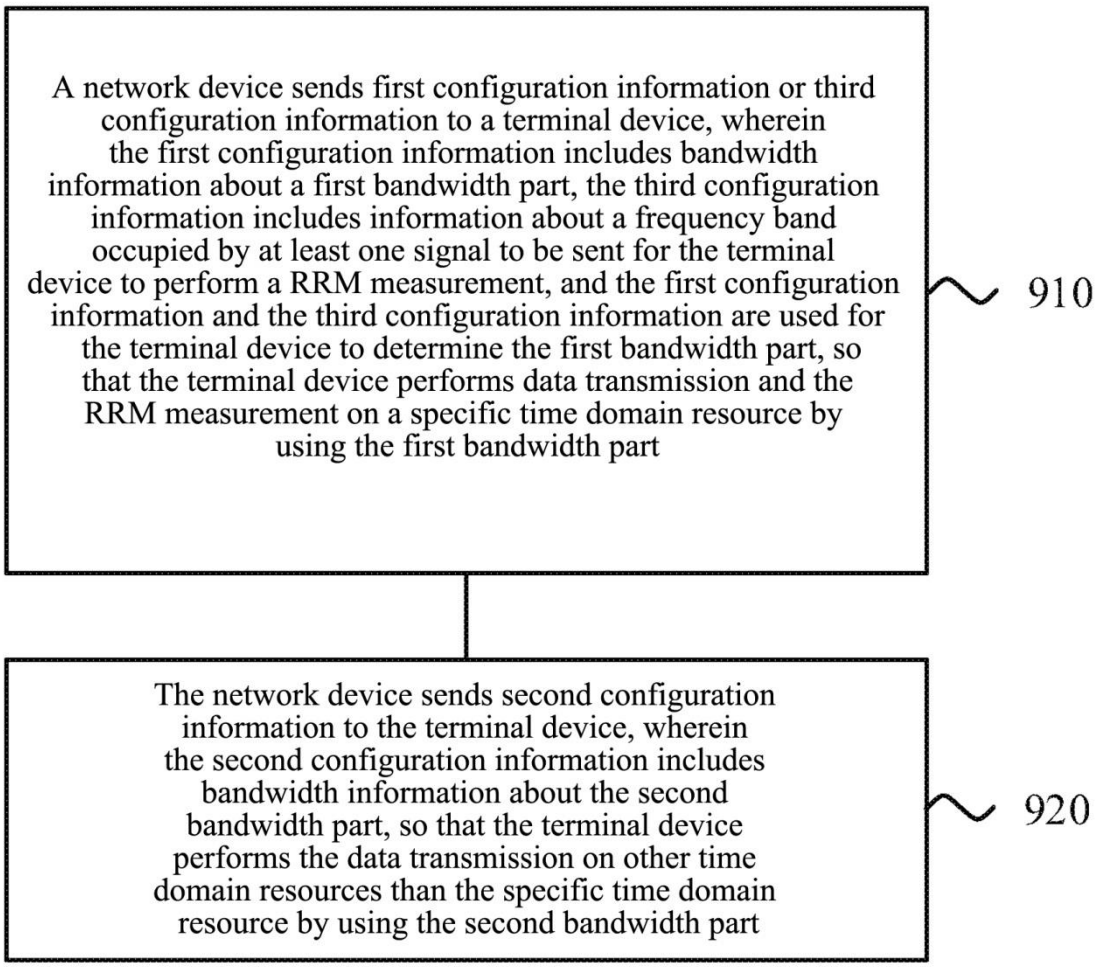
A network device sends first configuration information or third configuration information to a terminal device, wherein the first configuration information includes bandwidth information about a first bandwidth part, the third configuration information includes information about a frequency band occupied by at least one signal to be sent for the terminal device to perform a RRM measurement, and the first configuration information and the third configuration information are used for the terminal device to determine the first bandwidth part, so that the terminal device performs data transmission and the RRM measurement on a specific time domain resource by using the first bandwidth part
910
The network device sends second configuration information to the terminal device, wherein the second configuration information includes bandwidth information about the second bandwidth part, so that the terminal device performs the data transmission on other time domain resources than the specific time domain resource by using the second bandwidth part
920

FIG. 9

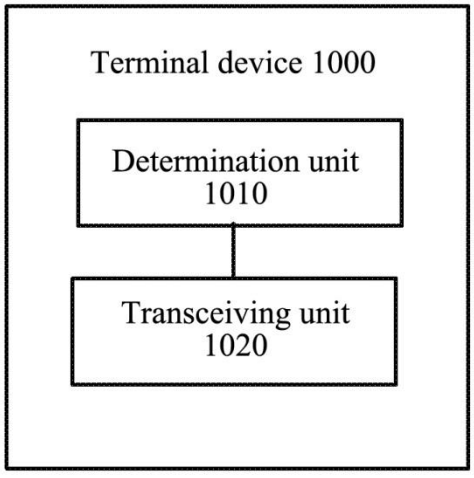
Terminal device 1000
Determination unit 1010
Transceiving unit 1020

FIG. 10

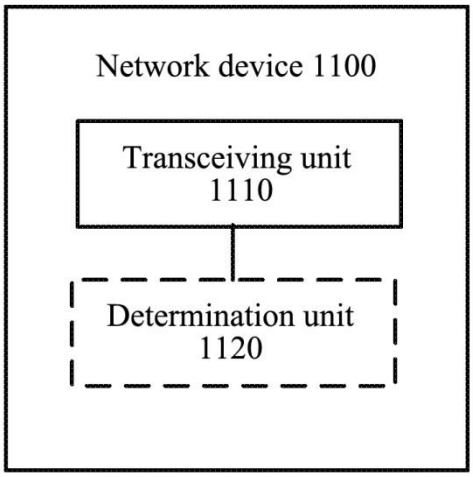
Network device 1100
Transceiving unit
1110
Determination unit
1120

FIG. 11

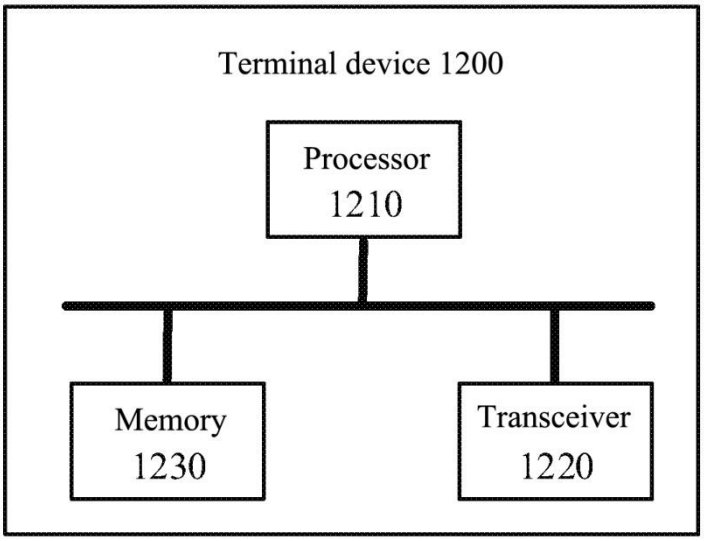
Terminal device 1200
Processor
1210
Memory
1230
Transceiver
1220

FIG. 12

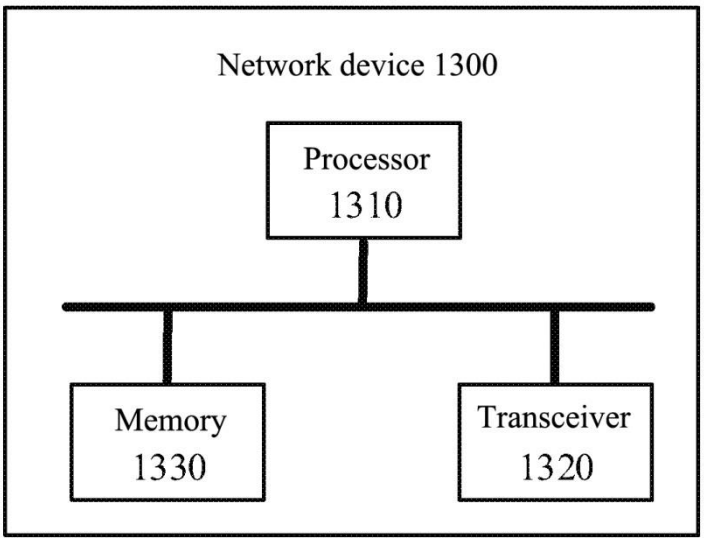
Network device 1300
Processor
1310
Memory
1330
Transceiver
1320

FIG. 13

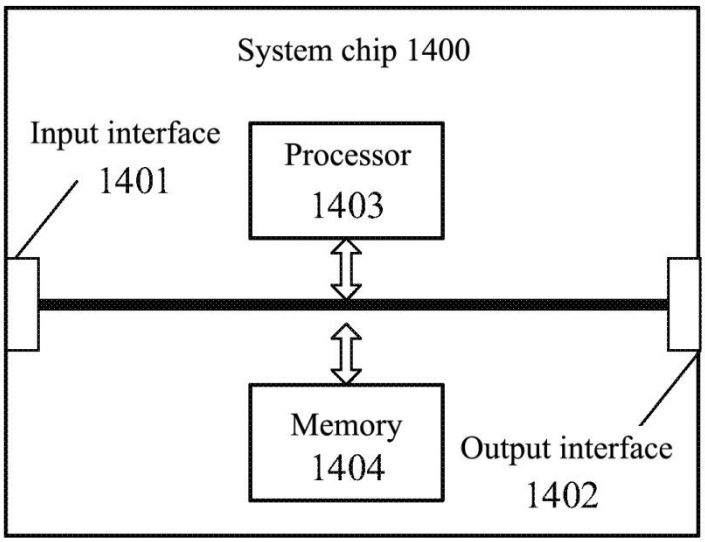
System chip 1400
Input interface
1401
Processor
1403
Memory
1404
Output interface
1402

FIG. 14