**CLAIMS**

1. A method for decoding a video comprising:

- decoding (920) a partitioning tree structure corresponding to a partitioning process of a block of an image of the video;
- decoding (930) said block based at least on said partitioning tree structure;

wherein said partitioning tree structure is partially shared between luma and chroma blocks wherein a single partitioning tree structure is shared between luma and chroma blocks from a root node of the partitioning tree down to a switching node and separate partitioning tree structures are determined for luma blocks and for chroma blocks from the switching node down to a leaf node of the partitioning tree; the switching node being determined based on a partitioning block size.

2. The method according to claim 1 wherein said partitioning tree structure is used for decoding a block belonging to a part of the video image coded in Intra.

3. The method according to claim 1 wherein said partitioning tree structure is used for decoding a block belonging to a part of the video image coded in Inter.

4. The method according to claim 1 wherein the switching node is implicitly determined.

5. The method according to claim 1 wherein said partitioning block size is set to 64x64.

6. The method according to claim 1 wherein the switching node is signaled.

7. An apparatus comprising a memory and one or more processors configured to decode a video, wherein the one or more processors are further configured to:

- decode a partitioning tree structure corresponding to a partitioning process of a block of an image of the video;
- decode said block based at least on said partitioning tree structure;

wherein a single partitioning tree structure is shared between luma and chroma blocks from a root node of the partitioning tree down to a switching node and separate partitioning tree structures are determined for luma blocks and for chroma blocks from the switching node down to a leaf

node of the partitioning tree; the switching node being determined based on a partitioning block size.

8. The apparatus according to claim 7 wherein said partitioning tree structure is used for decoding a block belonging to a part of the video image coded in Intra.

9. The apparatus according to claim 7 wherein said partitioning tree structure is used for decoding a block belonging to a part of the video image coded in Inter.

10. The apparatus according to claim 7 wherein the switching node is implicitly determined.

11. The apparatus according to claim 7 wherein said partitioning block size is set to 64x64.

12. The apparatus according to claim 7 wherein the switching node is signaled.

13. A method comprising encoding a video, said encoding further comprising:

- determining (720) a partitioning tree structure corresponding to a partitioning process of a block of an image of the video;
- coding (730) said block based at least on said partitioning tree structure;

wherein a single partitioning tree structure is shared between luma and chroma blocks from a root node of the partitioning tree down to a switching node and separate partitioning tree structures are determined for luma blocks and for chroma blocks from the switching node down to a leaf node of the partitioning tree; the switching node being determined based on a partitioning block size.

14. The method according to claim 13 wherein said partitioning tree structure is used for coding a block belonging to a part of the video image coded in Intra.

15. The method according to claim 13 wherein said partitioning tree structure is used for coding a block belonging to a part of the video image coded in Inter.

16. The method according to claim 13 wherein the switching node is implicitly determined.

17. The method according to claim 13 wherein said partitioning block size is set to 64x64.

18. The method according to claim 13, wherein the switching node is signaled.

19. An apparatus comprising a memory and one or more processors configured to encode a video, wherein the one or more processors are further configured to:

- determine a partitioning tree structure corresponding to a partitioning process a block of an image of the video,
- code said block based at least on said partitioning tree structure,

wherein a single partitioning tree structure is shared between luma and chroma blocks from a root node of the partitioning tree down to a switching node and separate partitioning tree structures are determined for luma blocks and for chroma blocks from the switching node down to a leaf node of the partitioning tree; the switching node being determined based on a partitioning block size.

20. The apparatus according to claim 19 wherein said partitioning tree structure is used for coding a block belonging to a part of the video image coded in Intra.

21. The apparatus according to claim 19 wherein said partitioning tree structure is used for coding a block belonging to a part of the video image coded in Inter.

22. The apparatus according to claim 19 wherein the switching node is implicitly determined.

23. The apparatus according to claim 19 wherein said partitioning block size is set to 64x64.

24. The apparatus according to claim 19 wherein the switching node is signaled.

25. A non-transitory program storage device, readable by a computer, tangibly embodying a program of instructions executable by the computer for performing the method according to any one of claims 1 to 6 or claims 13 to 18.