

DISPLAY PANEL

ABSTRACT OF THE DISCLOSURE

A display panel includes a substrate, a first circuit routing area, a second circuit routing area, a plurality of first gate lines, a plurality of second gate lines, a plurality of first drive lines and a plurality of second drive lines. Each of the first gate lines and one of the second gate lines intersect at a first intersect point. Each of the first drive lines and one of the second drive lines electrically connect at a second intersect point. The distance on the first direction between one of the first intersect points and one of the second intersect points on the second direction is smaller than the width of a liquid crystal subpixel on the first direction.

Fig. 2