

ABSTRACT

Pager

The present invention provides a pager in which a user can record a set phrase which the user wants to record and thereby can improve the usability of the pager.

A decoder decides whether a received address code belongs to the pager or not. When it is decided that the selective address code is belongs to the pager, microprocessor 17 stores a message signal which follows this selective address code in a message memory area of RAM 7. When a set phrase is stored, the microprocessor 17 records into set phrase recording area 6a of EEPROM 6 a portion of the message designated by function switch 8 as a set phrase through a microprocessor 17.

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