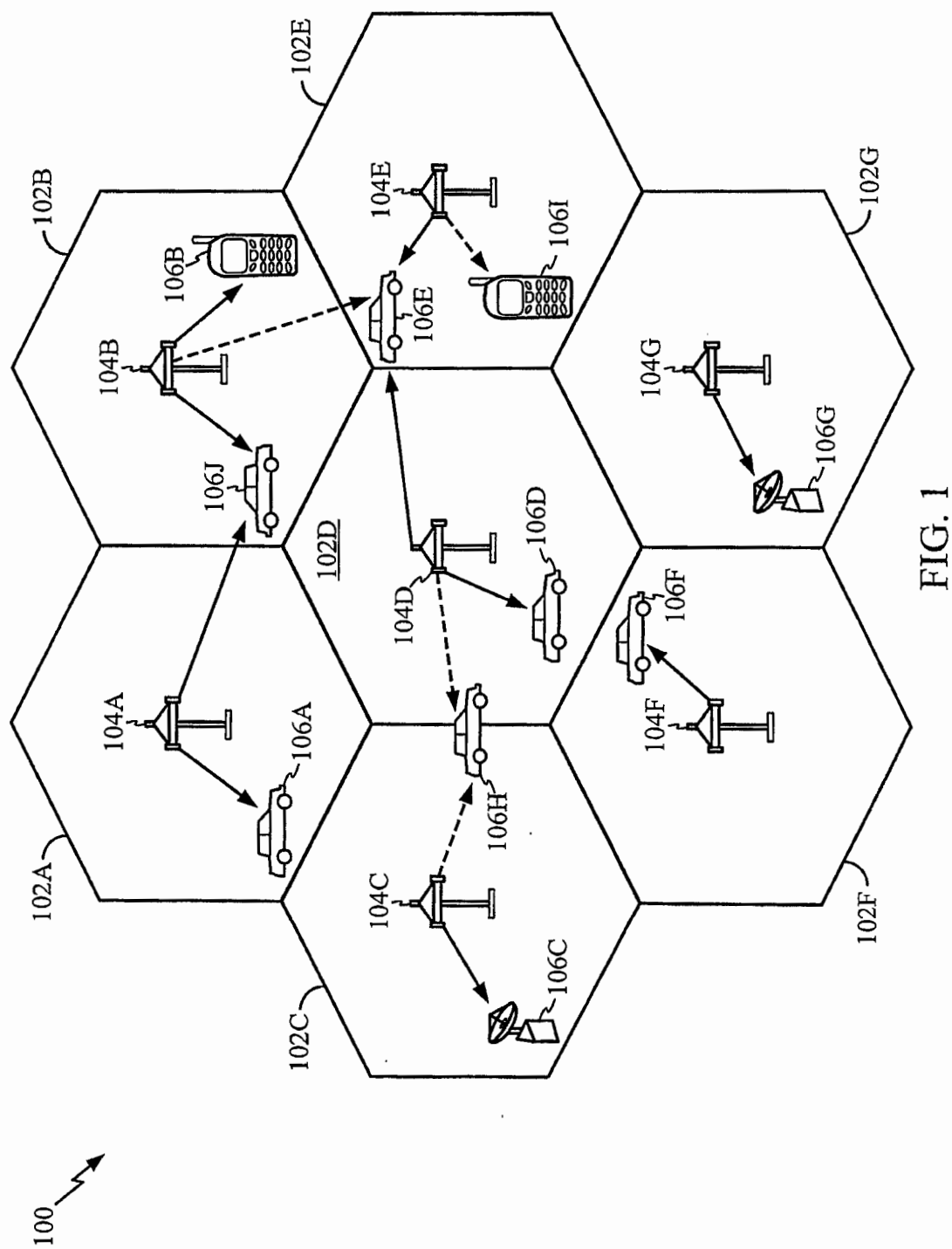
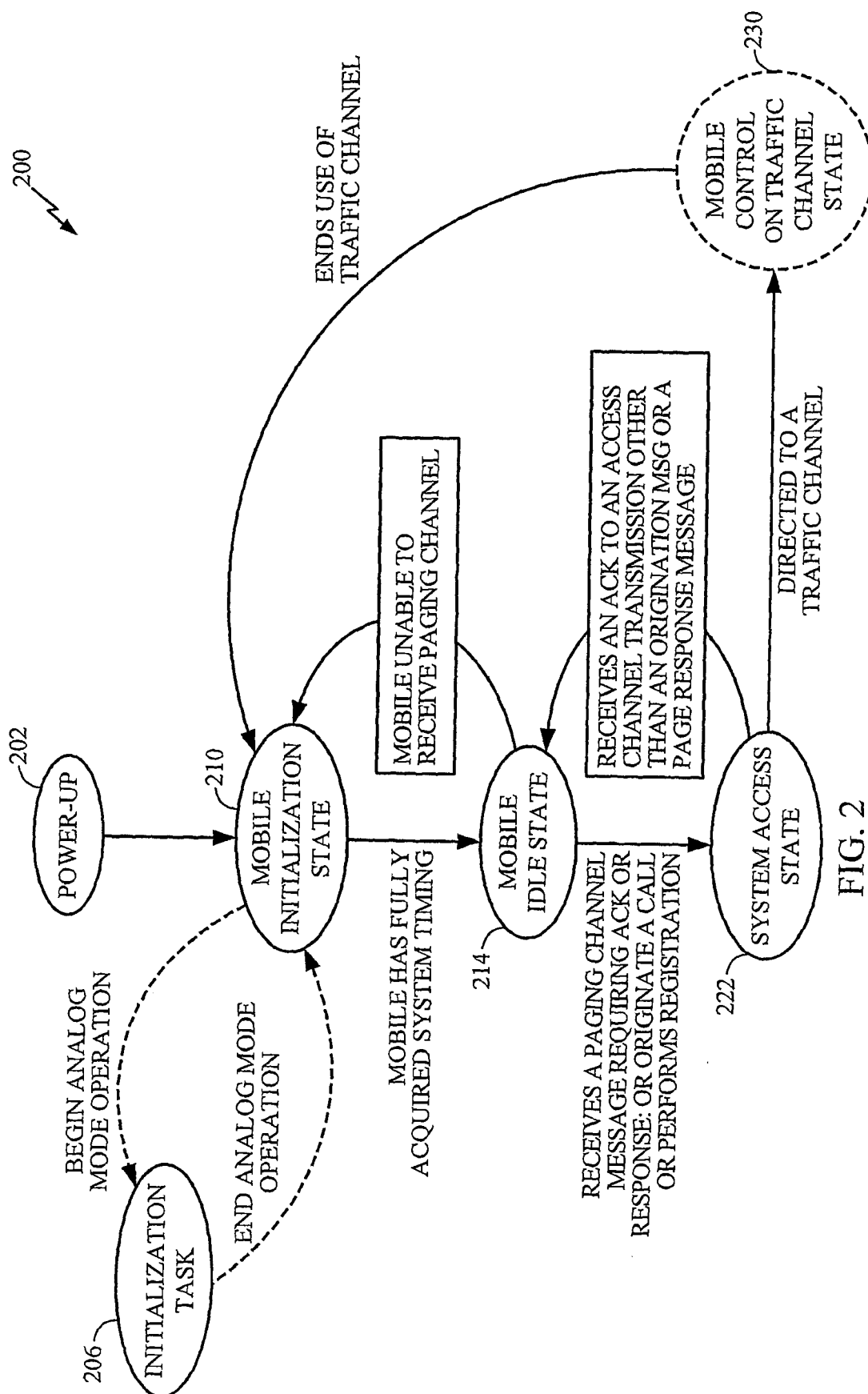






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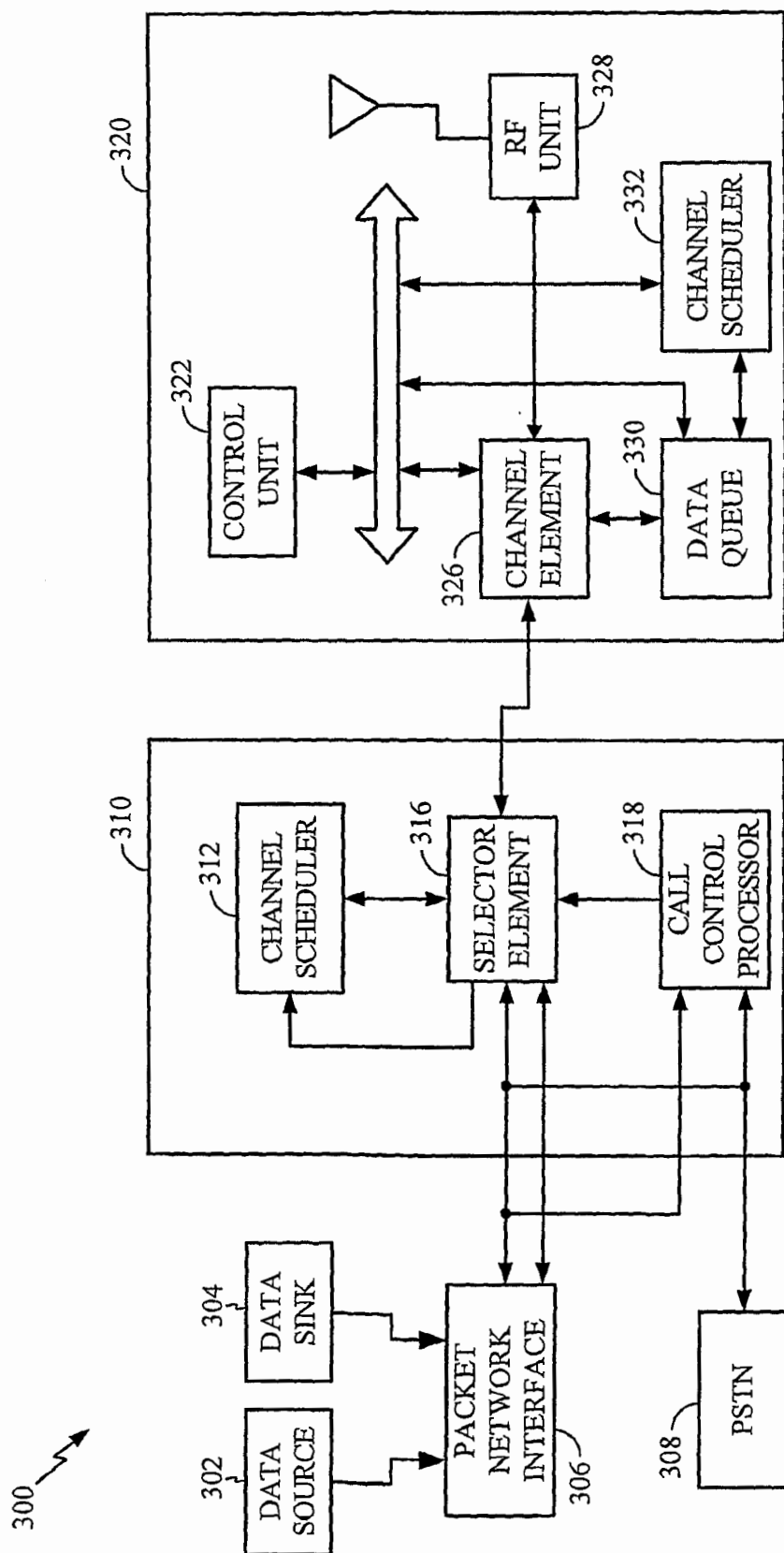


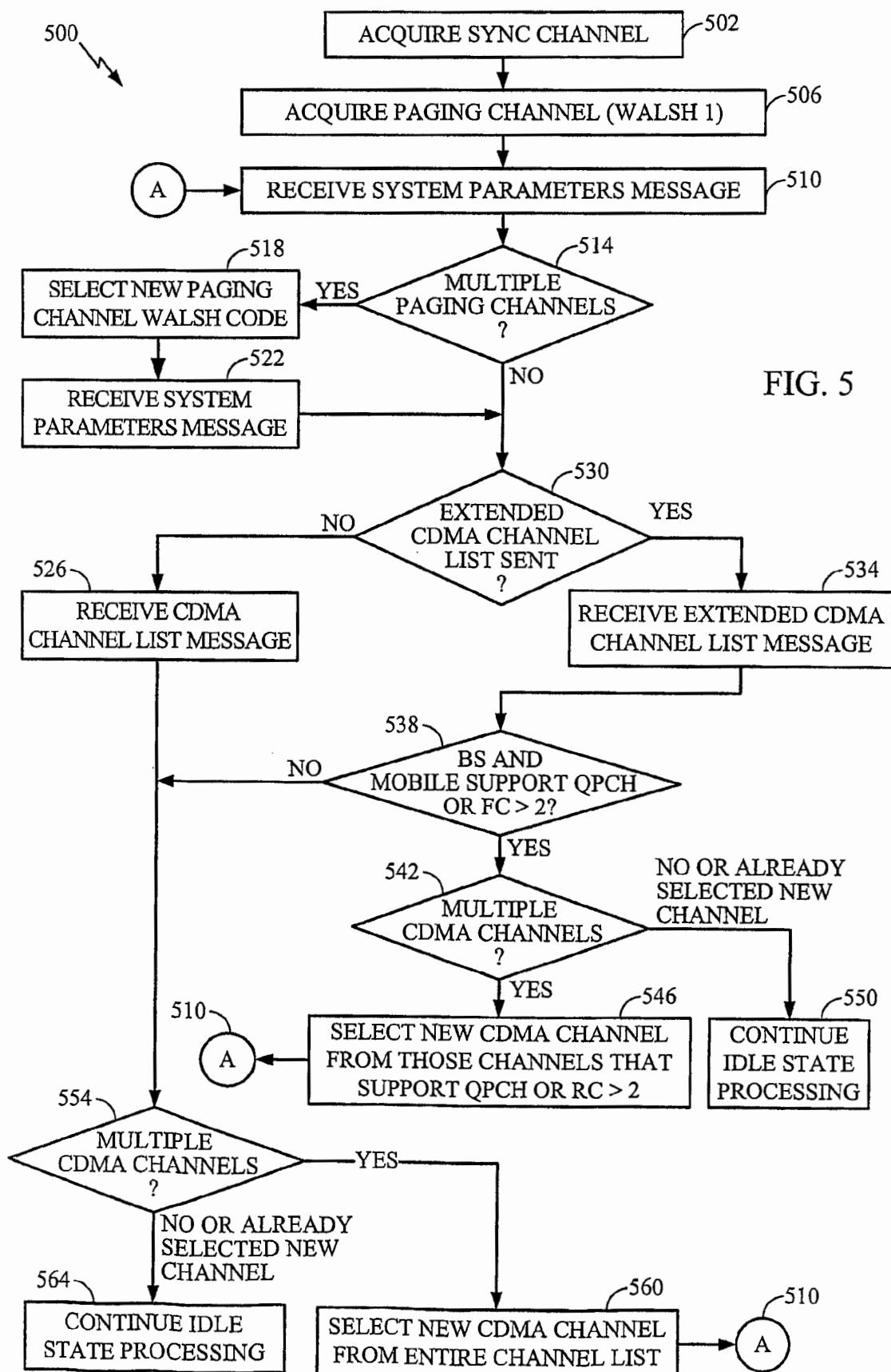
FIG. 3

FIELD	LENGTH (BITS)
P_REV	8
MIN_P_REV	8
SID	15
NID	16
PILOT_PN	9
LC_STATE	42
SYS_TIME	36
LP_SEC	8
LTM_OFF	6
DAYLT	1
PRAT	2
CDMA_FREQ	11

FIG. 4

FIG. 4
(CONTINUED)

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FIELD	LENGTH (BITS)
MSG_TYPE; ('00000001')	8
ACK_SEQ	3
MSG_SEQ	3
ACK_REQ	1
VALID_ACK	1
ACK_TYPE	3
MSID_TYPE	3
MSID_LEN	4
MSID	8' MSID_LEN
AUTH_MODE	2
AUTHR	0 OR 18
RANDC	0 OR 8
COUNT	0 OR 6
→ REG_TYPE	4
→ SLOT_CYCLE_INDEX	3
MOB_P_REV	8
→ SCM	8
→ MOB_TERM	1
RESERVED	6

FIG. 6

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FIELD	LENGTH (BITS)
MSG_TYPE	00000111

ONE OR MORE OCCURRENCES OF THE FOLLOWING RECORD:

ACK_SEQ	3
MSG_SEQ	3
ACK_REQ	1
VALID_ACK	1
ADDR_TYPE	3
ADDR_LEN	4
ADDRESS	8* ADDR_LEN
ORDER	011011
ADD_RECORD_LEN	001
ORDER-SPECIFIC FIELDS (IF USED)	00000001

FIG. 7

FIELD	LENGTH (BITS)
MSG_TYPE; ('00000001')	8
ACK_SEQ	3
MSG_SEQ	3
ACK_REQ	1
VALID_ACK	1
ACK_TYPE	3
MSID_TYPE	3
MSID_LEN	4
MSID	8' MSID_LEN
AUTH_MODE	2
AUTHR	0 OR 18
RANDC	0 OR 8
COUNT	0 OR 6
REG_TYPE	4
SLOT_CYCLE_INDEX	3
MOB_P_REV	8
SCM	8
MOB_TERM	1
RESERVED	6

FIG. 8
(CONTINUED)

FIELD	LENGTH (BITS)
MSG_TYPE; ('00000001')	8
ACK_SEQ	3
MSG_SEQ	3
ACK_REQ	1
VALID_ACK	1
ACK_TYPE	3
MSID_TYPE	3
MSID_LEN	4
MSID	8' MSID_LEN
AUTH_MODE	2
AUTHR	0 OR 18
RANDC	0 OR 8
COUNT	0 OR 6
REG_TYPE	4
SLOT_CYCLE_INDEX	3
MOB_P_REV	8
SCM	8
MOB_TERM	1
RESERVED	6

FIG. 8

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FIELD	LENGTH (BITS)
MSG_TYPE; ('00000100')	8
ACK_SEQ	3
MSG_SEQ	3
ACK_REQ	1
VALID_ACK	1
ACK_TYPE	3
MSID_TYPE	3
MSID_LEN	4
MSID	8' MSID_LEN
AUTH_MODE	2
AUTHR	0 OR 18
RANDC	0 OR 8
COUNT	0 OR 6
→ MOB_TERM	1
→ SLOT_CYCLE_INDEX	3
MOB_P_REV	8
→ SCM	8
REQUEST_MODE	3
SPECIAL_SERVICE	1
SERVICE_OPTION	0 OR 16
PM	1
DIGIT_MODE	1
NUMBER_TYPE	0 OR 3
NUMBER_PLAN	0 OR 4

FIG. 9

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FIELD	LENGTH (BITS)
MSG_TYPE; ('00000001')	8
PILOT_PN	9
CONFIG_MSG_SEQ	6
SID	15
NID	16
REG_ZONE	12
→ TOTAL_ZONES	3
→ ZONE_TIMER	3
→ MULT_SIDS	1
→ MULT_NIDS	1
BASE_ID	16
BASE_CLASS	4
PAGE_CHAN	3
MAX_SLOT_CYCLE_INDEX	3
→ HOME_REG	1
→ FOR_SID_REG	1
→ FOR_NID_REG	1
→ POWER_UP_REG	1
→ POWER_DOWN_REG	1
→ PARAMETER_REG	1
→ REG_PRD	7
BASE_LAT	22
BASE_LONG	23
→ REG_DIST	11
SRCH_WIN_A	4

FIG. 10

WEIGHT BASED HASHING COUPLED WITH A 2-LEVEL-HASHING LOGIC 1/2

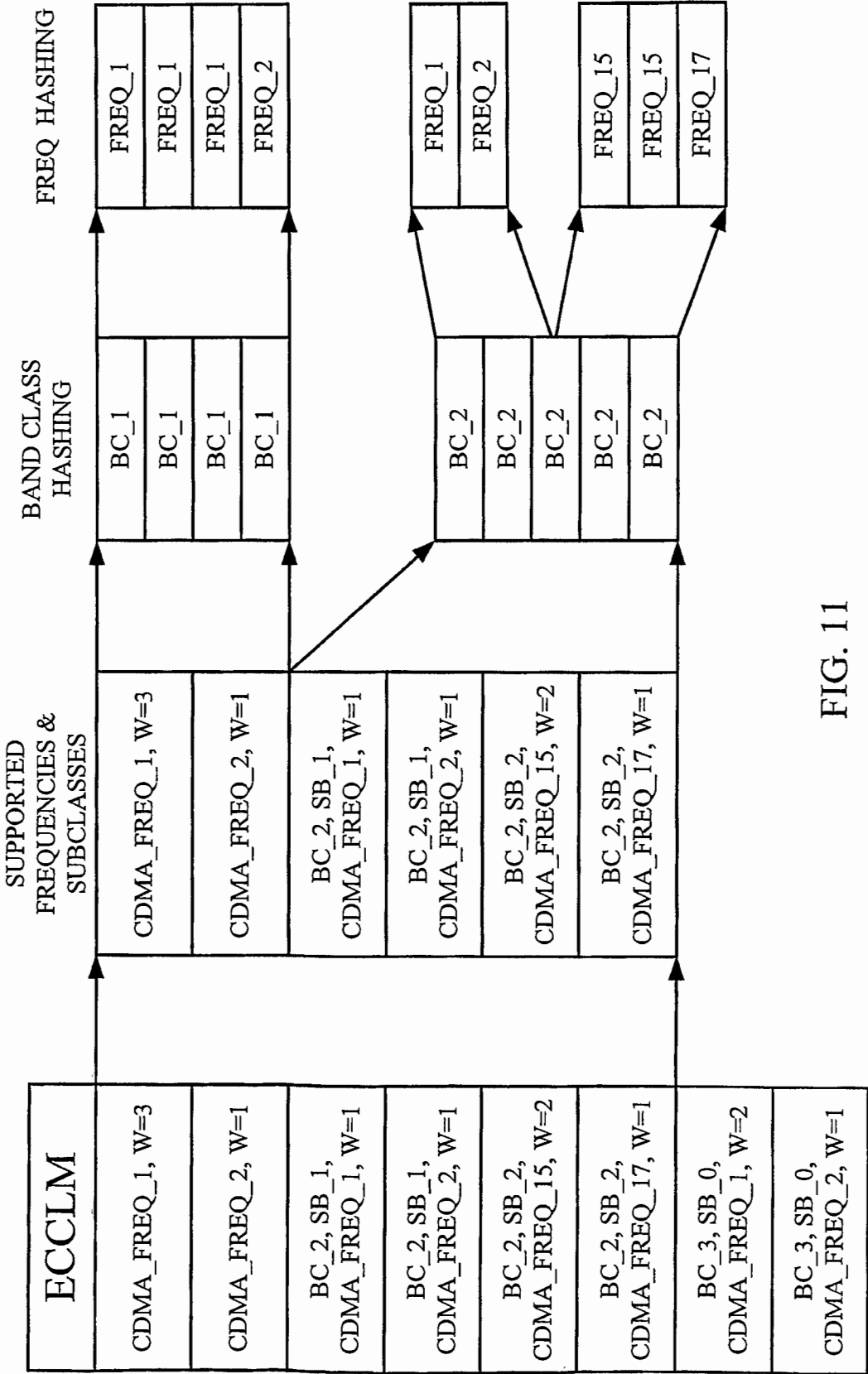


FIG. 11

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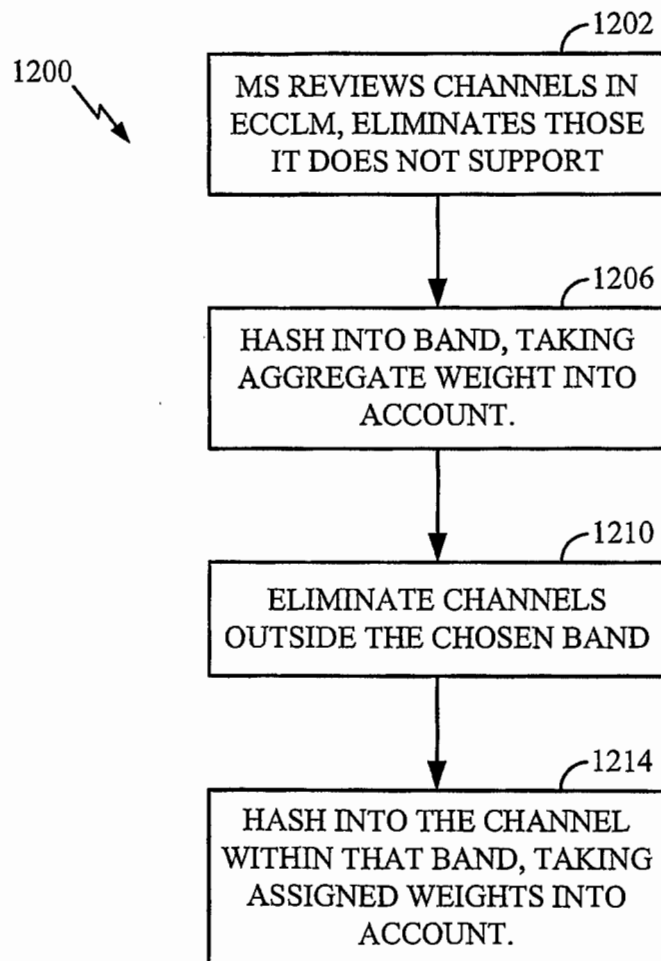


FIG. 12

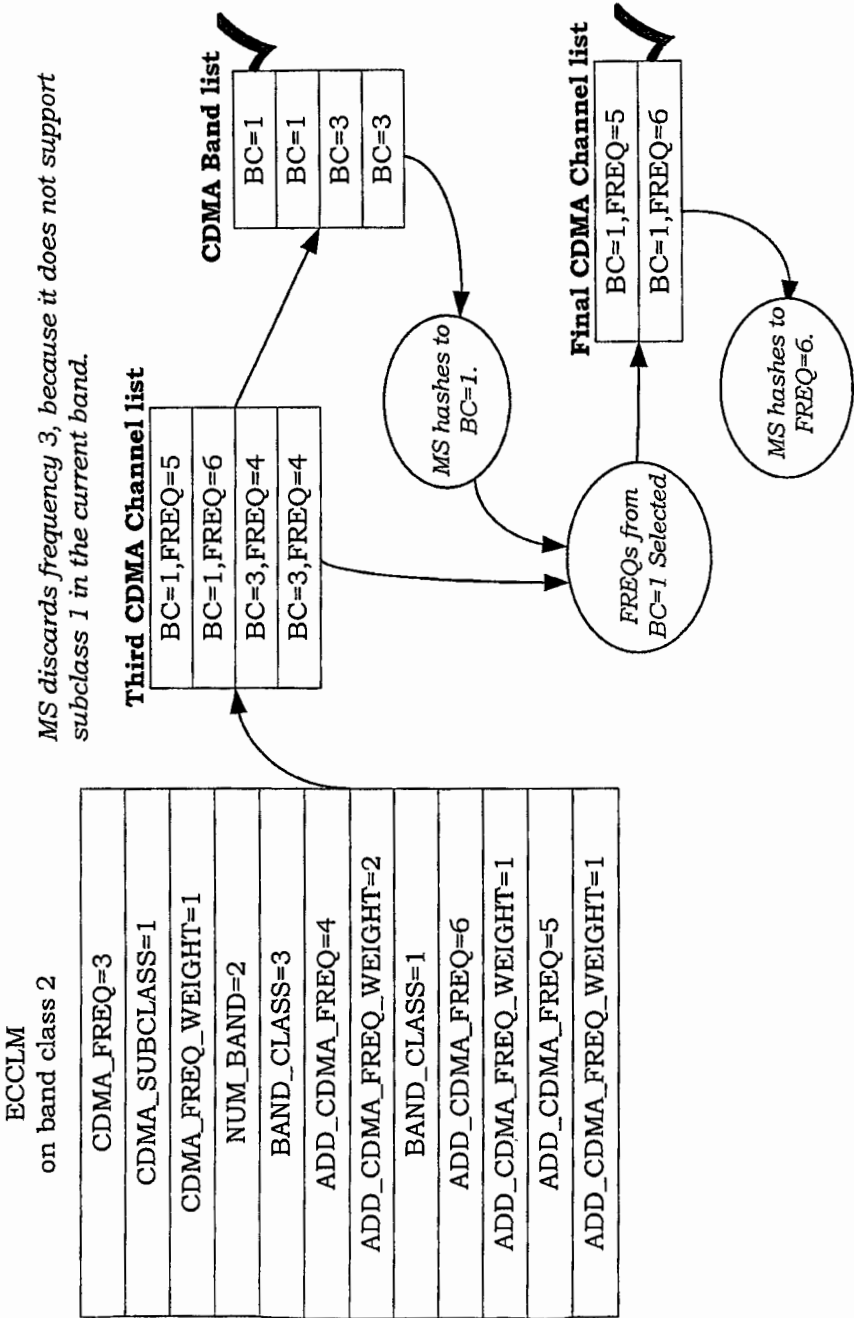


FIG. 13